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## $Latency\ numbers\ every\ programmer\ should\ know\ \ \textit{Jeff Dean (http://research.google.com/people/jeff/)}$

File View Edit Visualize Merge Labs

Showing all rows options	
operation	
L1 cache reference	
Branch mispredict	
L2 cache reference	
Mutex lock/unlock	
Main memory reference	
Compress 1K bytes with Zippy	
Send 2K bytes over 1 Gbps network	
Read 1 MB sequentially from memory	
Round trip within same datacenter	
Disk seek	
Read 1 MB sequentially from disk	
Send packet CA->Netherlands->CA	