ZHENGRONG WANG

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EDUCATION

University of California, Los Angeles, *Department of Computer Science* — Los Angeles, U.S.
Master of Science in Computer Science — *Sep. 2016 - Jul. 2018*(expected)

Tsinghua University, *Department of Electronic Engineering* — Beijing, P.R. China
Bachlor of Engineering in Electronic Engineering — *Aug. 2012 - Jul. 2016*
GPA: 91/100

ETH Zürich, *Department of Information Technology* — Zürich, Switzerland
Exchange Student, International Academic Program — *Sept. 2014 - Feb. 2015*
GPA: 5.50/6.00

PROFESSIONAL & PERSONAL SKILLS

Mathematic: Familiar with calculus, linear algebra, probability theory, discrete mathematics, algorithms.

Computer Capability: Skilled at C/C++, Python, MATLAB.

Language Proficiency: English: Toefl 114; German: B1 Level(MCER).

SELECTED PROJECTS & INTERNSHIPS

Yart-cpp, yet another ray tracer in C++ — *Sep. 2015 - Present*

- Implement direct lighting and bidirectional path tracing.
- Lambertian, specular, refraction and Cook-Torrance BRDF are supported.
- Use OC-Tree to accelerate complex mesh.
- Repo: https://github.com/seanzw/yart-cpp

Light C-Compiler in C# — *Dec. 2015 - Present*

- Support C99 standard and generate x86 assembly.
- Handwritten regular expression, NFA scanner generator and parser combinator library.
- Scanner generated by handwritten scanner generator.
- Parser generated by handwritten parser combinator library.
- Semantic analysis that checks types and symbols;
- Code generation to x86 assembly on Windows (can be directly used by clang assembler)
- Repo: https://github.com/seanzw/lcc

OpenCL@FPGA (Undergraduate Thesis) — *Sep. 2015 - Jun. 2016*

- Supervised by Assoc. Prof. Fei Qiao, Tsinghua University
- Use OpenCL to implement CNN on Xilinx Alpha Data FPGA, and accelerate with pipeline.
- Paper on TENCON 16: Optimizing Convolutional Neural Network on FPGA under Heterogeneous Computing Framework with OpenCL

MicroPython on FPGA, Dept. EEE, Imperial College London — *Jul. 2015 - Aug. 2015*

- Supervised by Prof. Peter Y. K. Cheung, Head of Dept. EEE.
- Port MicroPython on Altera DE0-Nano-SoC FPGA.
- Build FFT example with DMA.
- Repo: https://github.com/seanzw/MicroPythonFPGA

Computer Vision Engineering Internship, DeepGlint, Beijing — *May. 2015 - Jun. 2015*

- Work in the computer vision group to develop a ”mirror” demo for a skeleton recognition system.

AWARDS AND HONORS

Second-class Scholarship for Excellent Freshmen, *Tsinghua University* *Oct. 2012*

Wang Zhaosheng Scholarship for Excellent Studeng from Dongguan, *Wang Zhaosheng Fundation* *Oct. 2012*

Second Prize in 30^{th} Chinese National Physics Contest(non-physical group A) *Dec. 2013*

Ranked No.5 in National Matriculation Test(Science), Guangdong Province (5/600,000) *Jun. 2012*

EXPERIENCE

Courses in CS

- Compilers by Alex Aiken, Stanford University
- Operating System Engineering, MIT
- Programming Languages by Dan Grossman, University of Washington
- Machine Learning by Andrew Ng, Stanford University
- Algorithms Part I & II by Robert Sedgewick, Princeton University
- Introduction to Computer Science and Programming, MIT
- Introduction to Probability, MIT
- Advanced Computer Graphics, Tsinghua University
- Computer Networks, Tsinghua University
- Software Engineering, Tsinghua University
- Computer Graphics (5.25/6), ETH Zurich
- Computer Vision (5.5/6), ETH Zurich

Children Education Program Volunteer, *Dream a Dream, Bangalore, India* *Jul. 2013 - Sept. 2013*