NC State University

Department of Electrical and Computer Engineering

ECE 463/563 (Prof. Rotenberg)

Project #3: Dynamic Instruction Scheduling

REPORT TEMPLATE (Version 1.0)

by

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NCSU Honor Pledge: "I have neither given nor received unauthorized aid on this project."

Student's electronic signature: Sebastian Echeverry
                                            (sign by typing your name)

Course number: 463
                    (463 or 563 ?)

Grading Breakdown, Experiments, and Report

[0 – 50 points] Simulator development effort

You may earn up to 50 points for handing in significant commented code for your simulator, even if the simulator does not compile, run, and validate. To receive the maximum of 50 points, there needs to be a good-faith attempt at coding the functionality of all pipeline stages described in Section 5 of the Project 3 specification as well as the supporting code (parsing arguments, reading trace file, producing outputs, the Advance_Cycle() function, the do-while() simulator loop, *etc*.). Partial credit will be assessed at a coarse granularity based on a qualitative assessment by the TAs of the amount of functionality coded.

[30 points] Validation

Gradescope will evaluate your simulator on the eight validation runs, "val1.txt" through "val8.txt", posted on the website. Gradescope will also evaluate your simulator on two mystery runs. Each validation run and mystery run is worth 3 points. Gradescope must say that you match <u>all eight validation runs</u> to get credit for the experiments with the simulator (report), however.

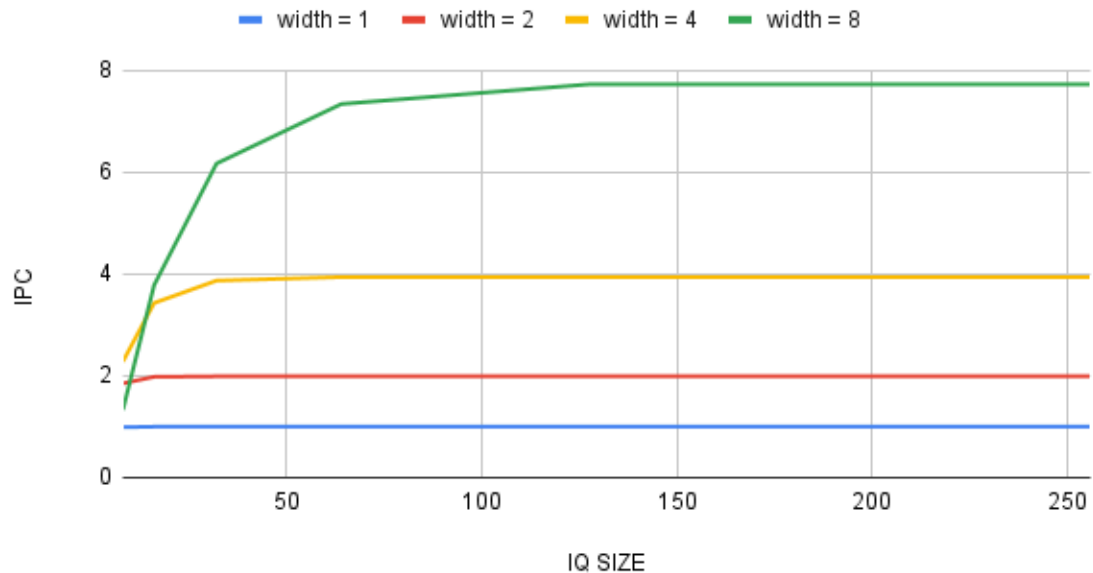## *[20 points] Report: graphs, graph analysis, and discussion*

Note: you can only get credit for the report if Gradescope says that you match <u>all eight validation runs</u>.

A. Large ROB, effect of IQ_SIZE

1. **Graphs [10 points]:** Keep ROB_SIZE fixed at 512 entries so that it is not a resource bottleneck. For each benchmark (gcc and perl), make a graph with IPC on the y-axis and IQ_SIZE on the x-axis. Use IQ_SIZE = 8, 16, 32, 64, 128, and 256. Plot 4 different curves (lines) on the graph: one curve for each of WIDTH = 1, 2, 4, and 8. Title the two graphs "gcc, ROB=512" and "perl, ROB=512", respectively.
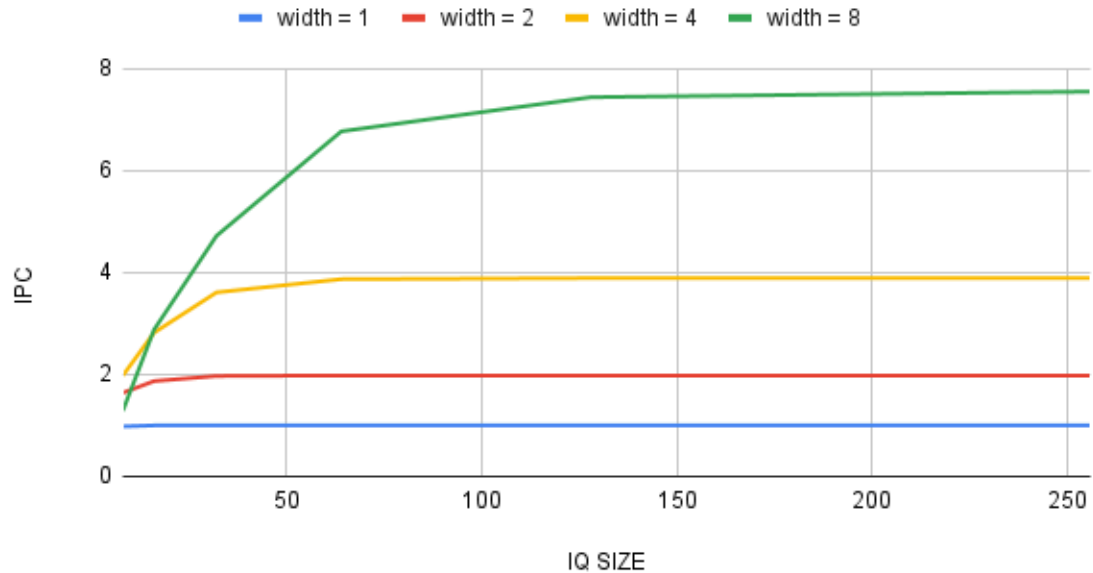
   <mark><< INSERT GRAPH "gcc, ROB=512" HERE >></mark>

GCC TRACE

Legend: width = 1, width = 2, width = 4, width = 8

Y-axis: IPC (0, 2, 4, 6, 8)
X-axis: IQ SIZE (50, 100, 150, 200, 250)

<< INSERT GRAPH "perl, ROB=512" HERE >>

PERL TRACE

Legend: width = 1, width = 2, width = 4, width = 8

Y-axis: IPC (0, 2, 4, 6, 8)
X-axis: IQ SIZE (50, 100, 150, 200, 250)

Grading rubric: (1) 5 points will be deducted for each missing graph. (2) For each graph that exists in the report, the TAs will check for missing or blatantly incorrect datapoints. As there are 24 datapoints (6 iq sizes x 4 widths), the TAs will deduct 0.2 points for each missing or blatantly incorrect datapoint that is spotted.

2. **Graph Analysis [2 points]:**
Using the data in the graphs above, for each WIDTH (1, 2, 4, and 8), find the minimum IQ_SIZE that still achieves within 6% of the IPC of the largest IQ_SIZE (256). This exercise should give four optimized IQ_SIZE's per benchmark, one optimized for each of WIDTH = 1, 2, 4, and 8. Tabulate the results of this exercise as follows:

	"Optimized IQ_SIZE per WIDTH" Minimum IQ_SIZE that still achieves within 6% of the IPC of the largest IQ_SIZE	
	gcc	perl
WIDTH = 1	8	8
WIDTH = 2	16	16
WIDTH = 4	32	32
WIDTH = 8	64	128

Grading rubric: Each cell in the table is worth 0.25 points.

3. **Discussion [2 points]:**

o The goal of a superscalar processor is to achieve an IPC that is close to WIDTH, which is the peak theoretical IPC of the processor. As we increase WIDTH, we observe that a <larger> IQ is needed to achieve this goal. This is because, with greater WIDTH, the IQ needs to look <farther> in the dynamic instruction stream to find <more> independent instructions that can issue in parallel to WIDTH execution lanes, each cycle.

o For WIDTH=8, perl's "optimized IQ_SIZE" is <greater than> gcc's "optimized IQ_SIZE" (reference your table above).
Why might this be the case?
a. Perhaps perl has <more> data-dependent instructions within a fixed window of instructions, such that it <must look farther> in the dynamic instruction stream to get the same number of independent instructions as gcc.
b. Perhaps perl has <more> long-latency instructions within a fixed window of instructions as compared to gcc.
c. All of the above: both a and b are plausible explanations.
Answer: <c>

Grading rubric: Each yellow blank is worth 0.25 points. Note that there are eight yellow blanks.

B. Effect of ROB_SIZE

4. **Graphs [6 points]:** For each benchmark (gcc and perl), make a graph with IPC on the y-axis and ROB_SIZE on the x-axis. Use ROB_SIZE = 32, 64, 128, 256, and 512. Plot 4 different curves (lines) on the graph: one curve for each of WIDTH = 1, 2, 4, and 8. For a given WIDTH, use the optimized IQ_SIZE for that

WIDTH, as obtained from the table in Section A.2 above. Title the two graphs "gcc, optimized IQ_SIZE per WIDTH" and "perl, optimized IQ_SIZE per WIDTH", respectively.
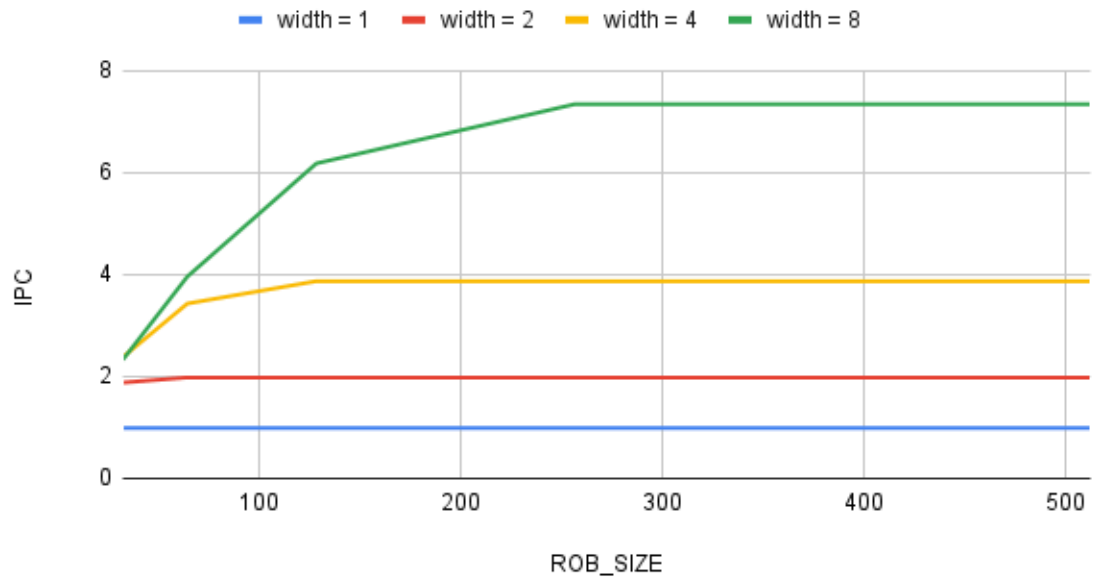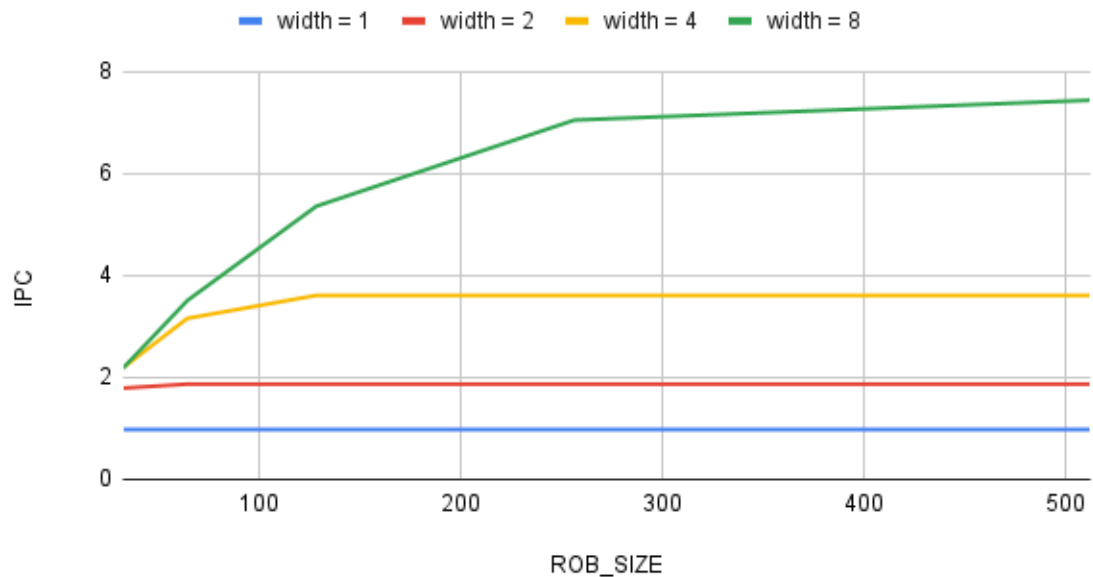
GCC TRACE OPTIMIZED IQ SIZE

PERL TRACE OPTIMIZED IQ SIZE

Grading rubric: (1) 3 points will be deducted for each missing graph. (2) For each

graph that exists in the report, the TAs will check for missing or blatantly incorrect datapoints. As there are 20 datapoints (5 rob sizes x 4 widths), the TAs will deduct 0.15 points for each missing or blatantly incorrect datapoint that is spotted.