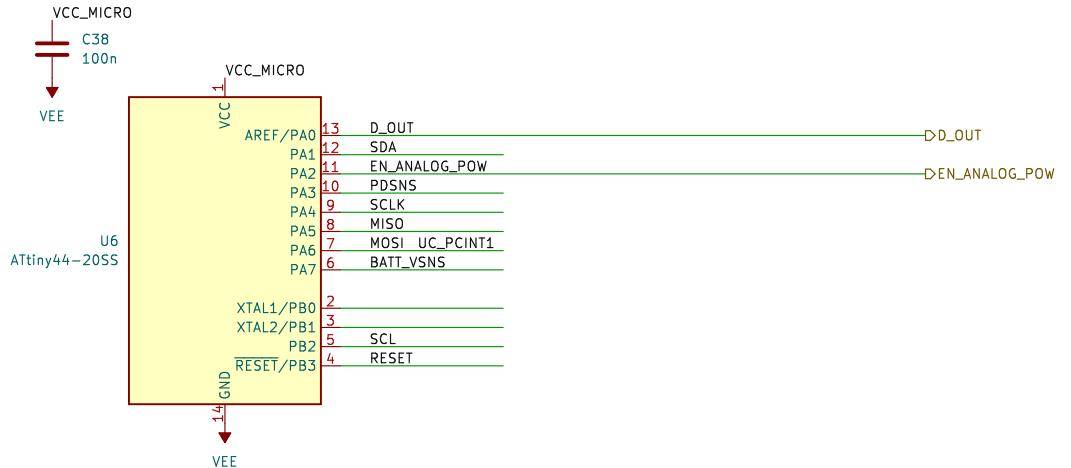
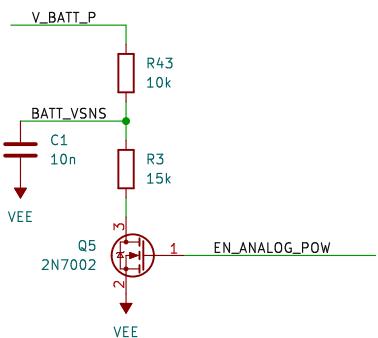
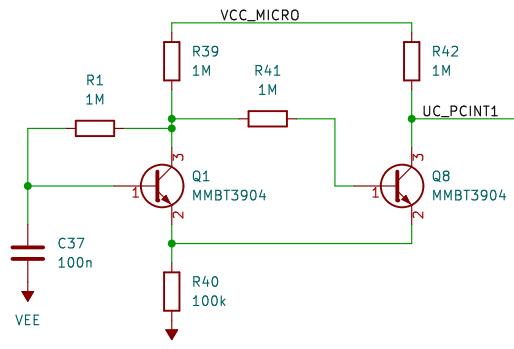


VCC_MICROD → VCC_MICRO

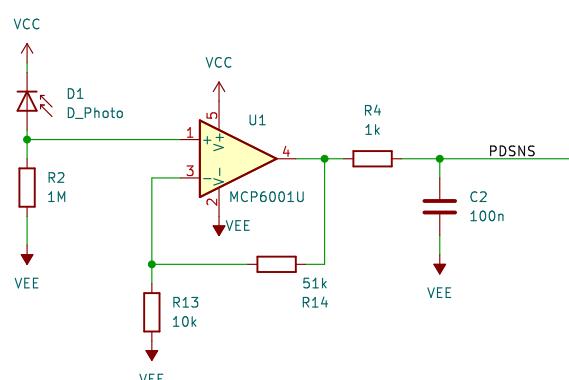
V_BATT_PD → V_BATT_P

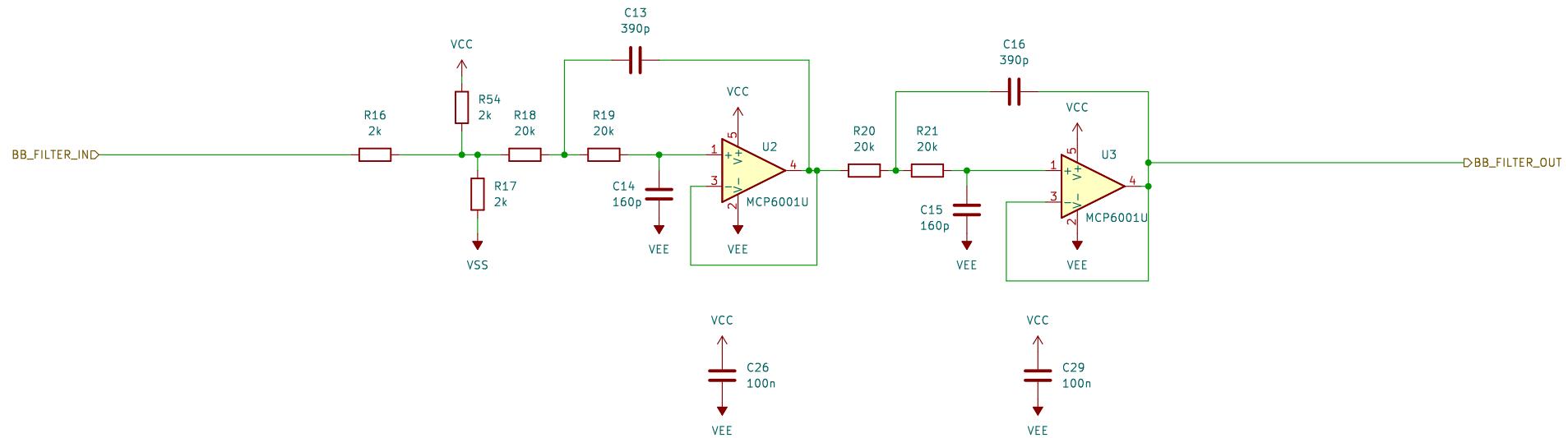


current draw
-3mA

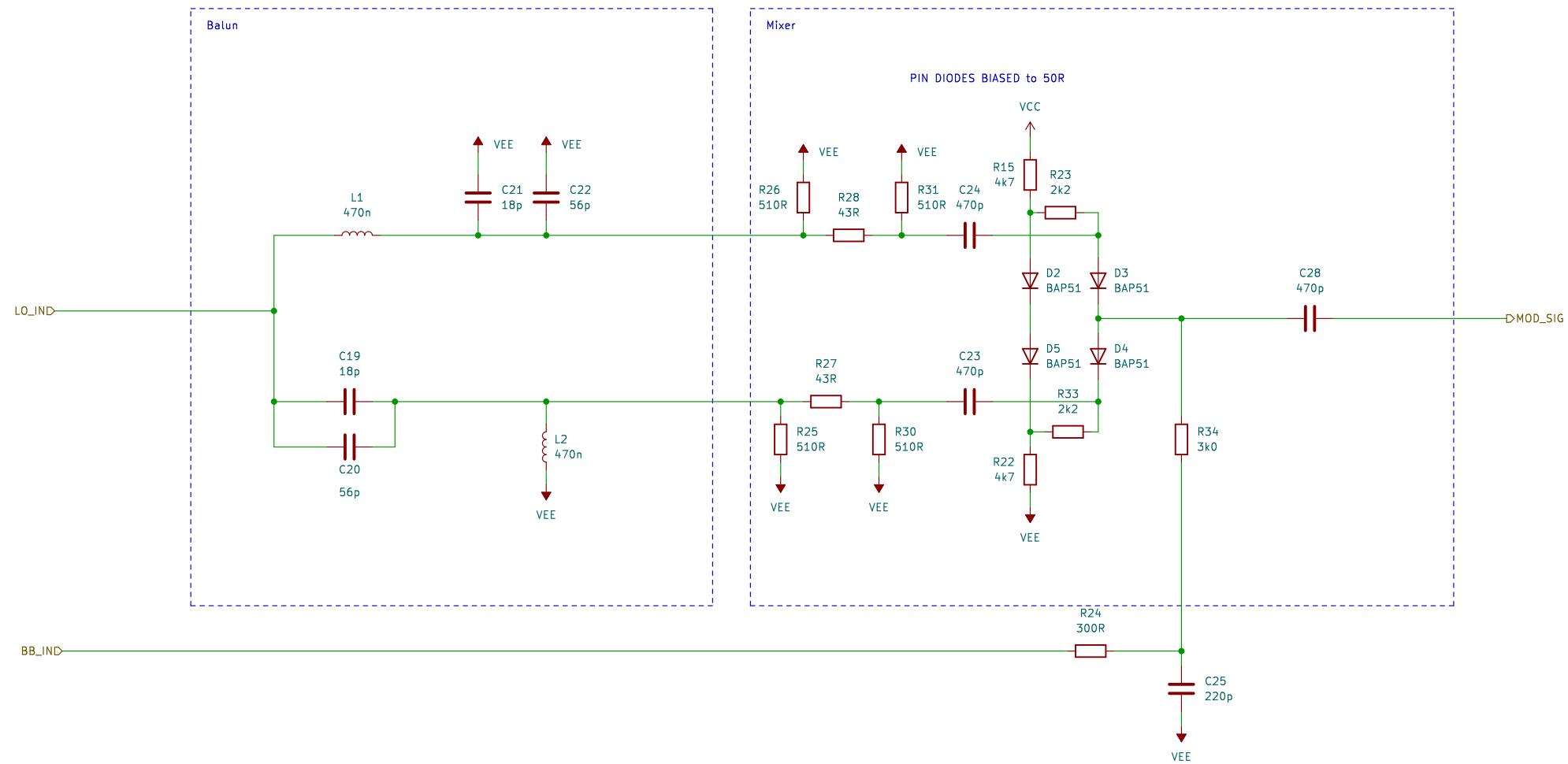
J1
Programming_Header

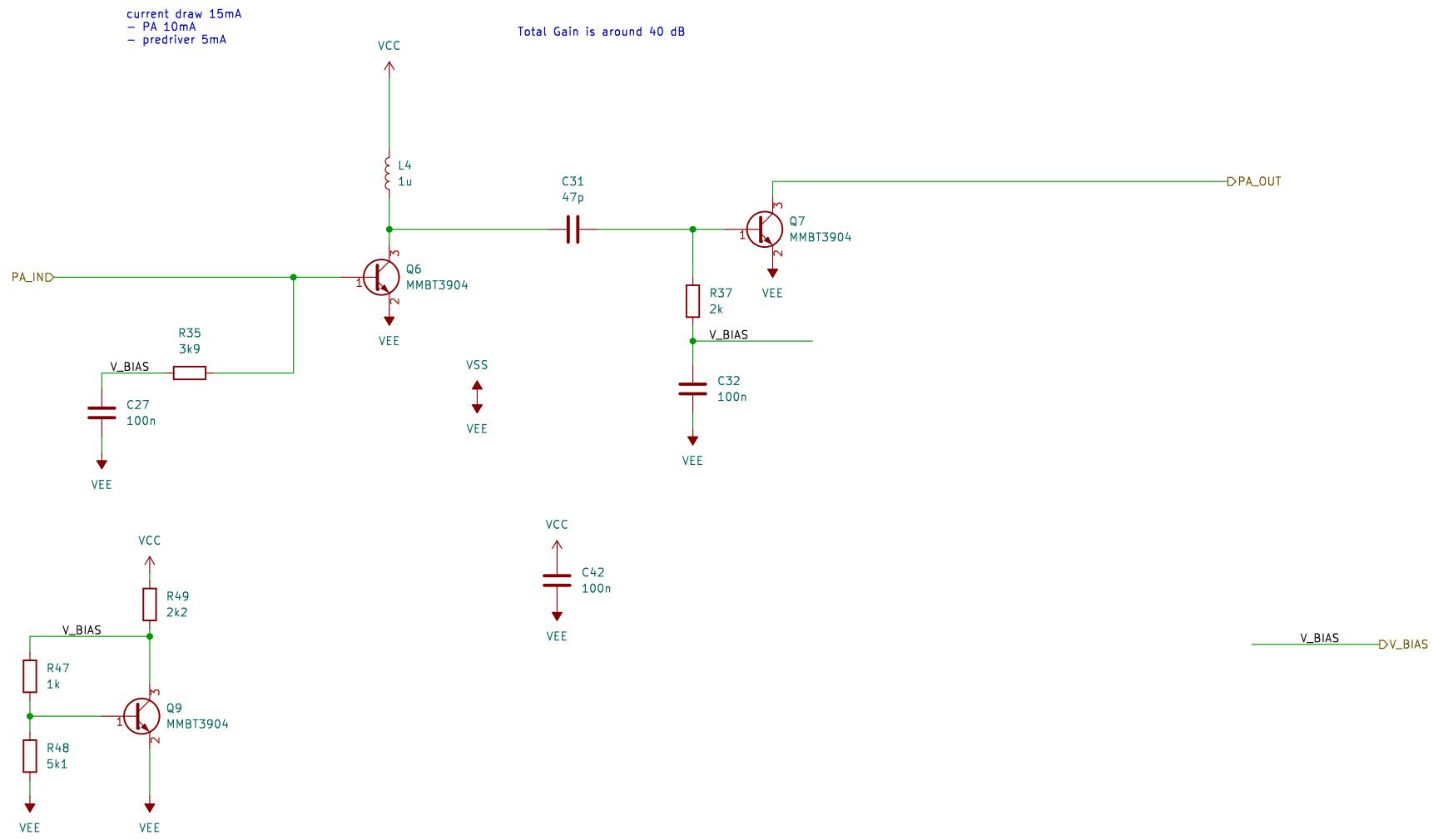
MISO	5	MISO
MOSI	4	MOSI
SCLK	3	SCLK
RESET	2	RESET
GND	1	

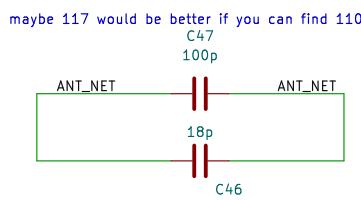
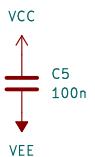
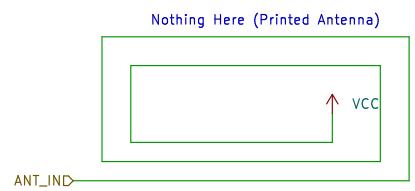


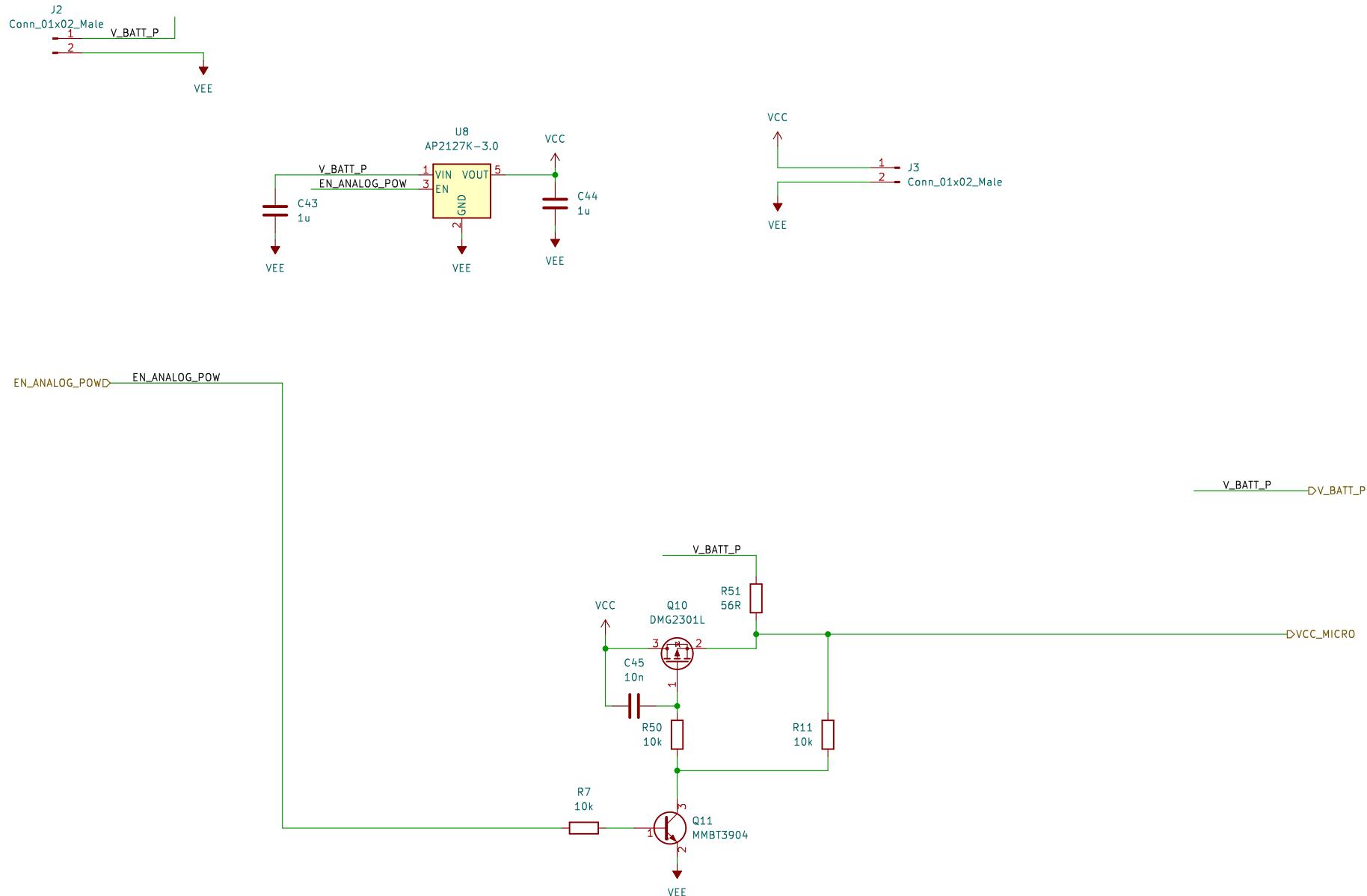


Current draw 1mA









 H1
Mounting Hole

 H3
Mounting Hole

 H2
Mounting Hole

 H4
Mounting Hole

