

Taller 1 - Arquitectura de Computadores

Sebastián Quesada Rojas, estudiante, Ingeniería en Computadores, *Instituto Tecnológico de Costa Rica*
Link al repositorio: <https://github.com/sebasqr22/ARQUI1/tree/Taller-1-ensambladores>



1 INSTALACIÓN

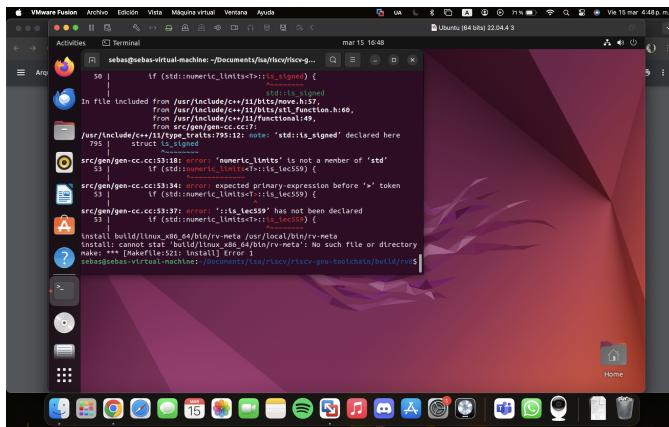


Fig. 1. Error instalación risc v

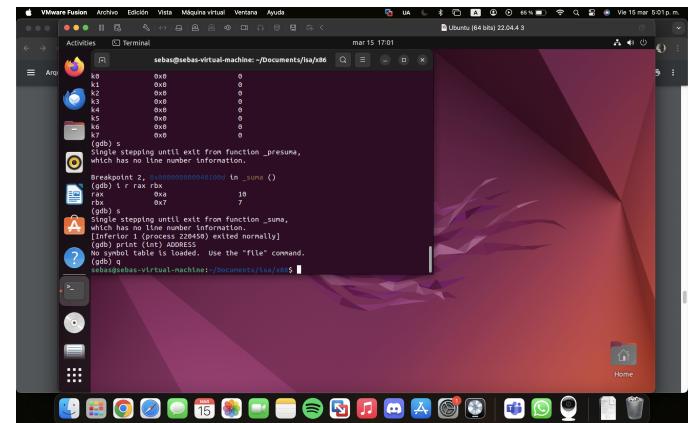


Fig. 3. Error en instalacion x86

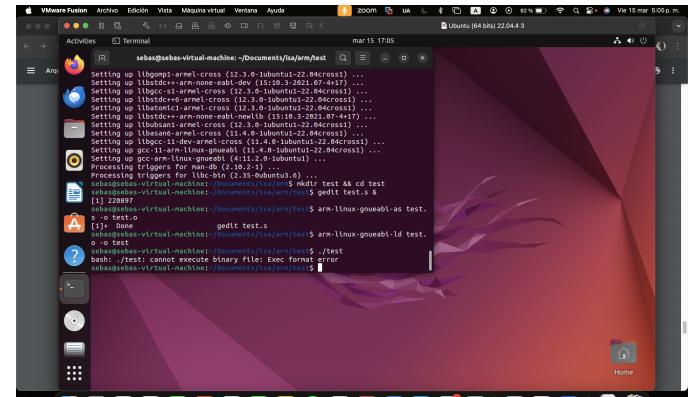


Fig. 4. Error en prueba ARM

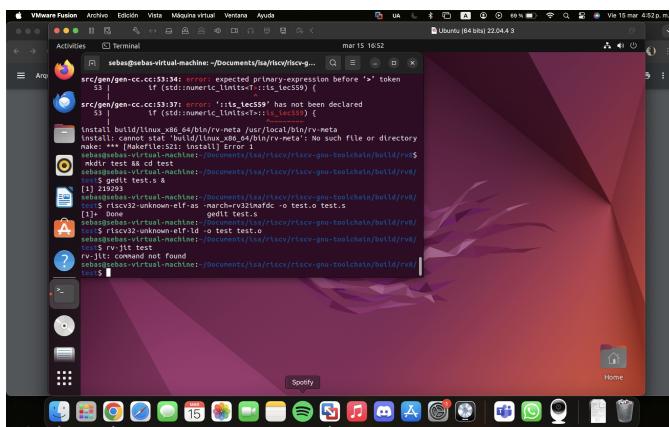


Fig. 2. Error comando RISC V

2 PRUEBA CON CÓDIGO PROPIO

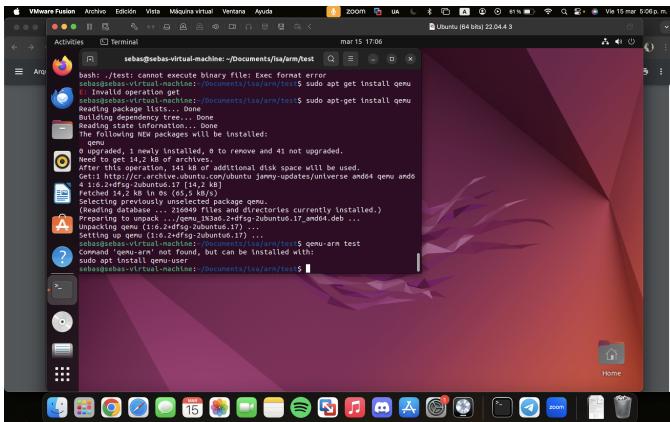


Fig. 5. Error comando qemu

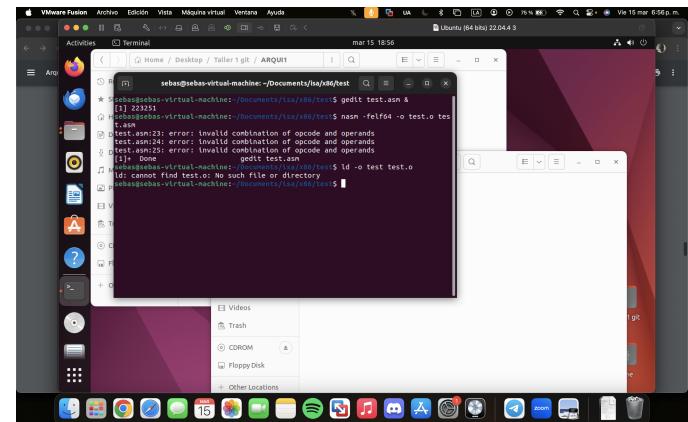


Fig. 8. Error prueba con x86

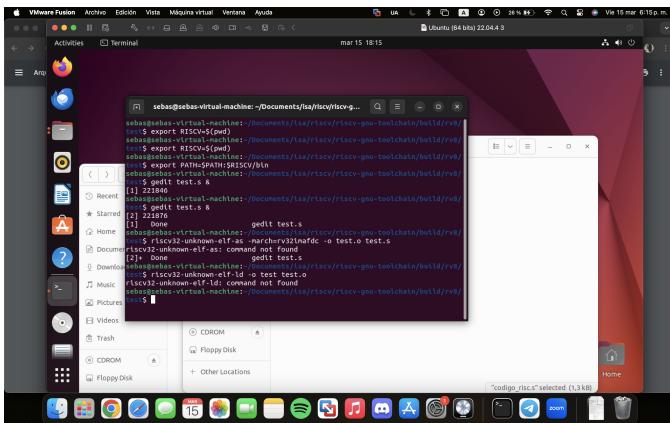


Fig. 6. Error al ejecutar RISC V

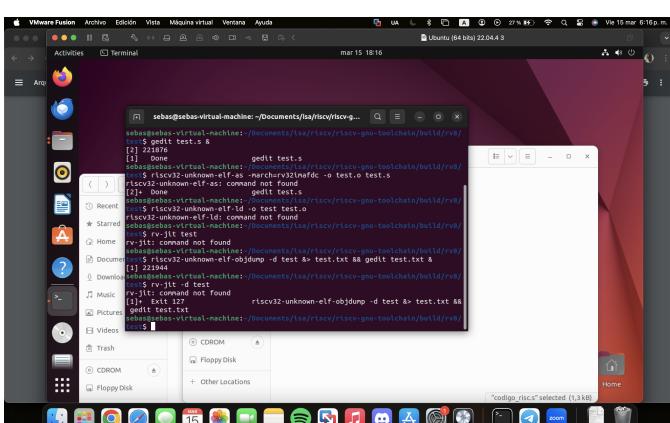


Fig. 7. Comando no encontrado para RISC V

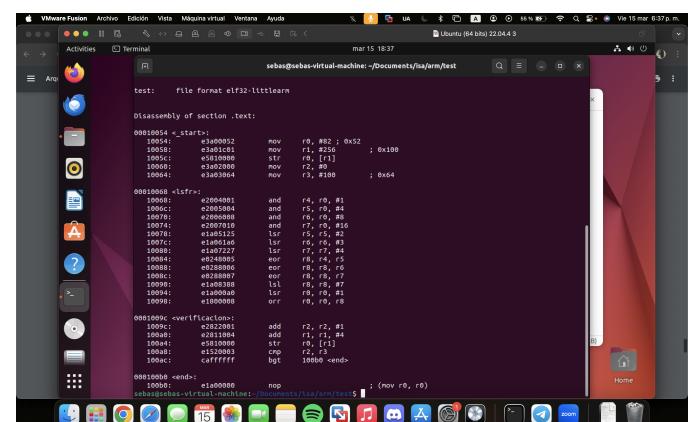


Fig. 9. Prueba con ARM