

SEBASTIAN MENDOZA

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EDUCATION

Texas A&M University

Bachelor of Science, Computer Engineering

College Station, TX

Aug. '16–Dec. '20

- Cumulative GPA: 3.12
 - Major Coursework GPA: 3.69
 - Awarded Dean's Honor Roll in Fall 2018 for exemplary performance in academics.
- Related Coursework: Software Engineering Studio, Microprocessor System Design, Design and Analysis of Algorithms, Computer Architecture, Statistics

WORK EXPERIENCE

Sketch Recognition Lab (Texas A&M University)

May '19–Sep '19

Undergraduate Research Assistant

- Created a canvas that stores the user's signature in a database using **ReactJS** and **Node.js**
- Connected database to front-end using **Sequelize** for user storage and **bcrypt.js** for encryption
- Presented and demonstrated results of individual and team research once a week

PROJECTS

IMDb Project

Group Project

- Led team of 3 engineers by setting up two weekly meetings to discuss progress and ensure we were on pace to complete the project by given deadline.
- Imported necessary table data into our **SQL** database and verified its correctness through the use of **PSQL** and **SQL** commands
- Designed and developed GUI to interface our IMDb using **Swing** and **AWT (Java)**
- Gained an understanding of how to use **GitHub** to share code with team

Single-Cycle Processor

Individual Project

- Integrated components of 64-bit Single-Cycle Processor using **Verilog**, which utilizes a subset of **ARMv8** instructions
- Verified that the proper output was produced through extensive test benches
- Completed 2 months ahead of the scheduled deadline

Mountain Paths

Individual Project

- Applied knowledge of **C++** to develop a program which finds the safest trail in a mountainous area
- Utilized to advice hikers on what the best trail is in order to reduce the risk of injury or getting lost

Traffic Light Controller

Individual Project

- Built a traffic light as described by a Finite State Machine using **Verilog** to make the conditions of the road less dangerous for drivers
- Programmed **FPGA** to simulate the traffic light and verify that the timing of the lights is correct

SKILLS

Programming Languages

C++, Python, Javascript (Node.js, ReactJS, Sequelize, HTML, CSS), Java (Swing, AWT), SQL, Verilog, ARMv8

Development Tools

Microsoft Visual Studio, PyCharm, IntelliJ, LabVIEW, PSpice