

74- TYPE #	DESCRIPTION	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
00 ALL	4 NAND2	1A	1B	1Y	2A	2B	2Y	GND	3Y	3A	3B	4Y	4A	4B	VCC		
01 RX	4 NAND2	1A	1Y	2A	2B	2B	2Y	GND	3A	3B	3Y	4A	4B	VCC			
01 H	4 NAND2	1A	1Y	2A	2B	2B	2Y	GND	3A	3B	3Y	4A	4B	VCC			
02 RLX	4 NOR2	1A	1Y	2A	2B	2B	2Y	GND	3A	3B	3Y	4A	4B	VCC			
03 RLX	4 NAND2	1A	1Y	2A	2B	2B	2Y	GND	3A	3B	3Y	4A	4B	VCC			
04 ALL	6 INVERT	1A	1Y	2A	2B	3A	3Y	GND	4Y	4A	5Y	5A	6Y	6A	VCC		
05 RLX	4 NOR2	1A	1Y	2A	2B	3A	3Y	GND	4Y	4A	5Y	5A	6Y	6A	VCC		
06 RLX	6 BUFFER	1A	1Y	2A	2B	3A	3Y	GND	4Y	4A	5Y	5A	6Y	6A	VCC		
07 RLX	6 BUFFER	1A	1Y	2A	2B	3A	3Y	GND	4Y	4A	5Y	5A	6Y	6A	VCC		
08 ALL	4 AND2	1A	1Y	2A	2B	2B	2Y	GND	3Y	3A	3B	4Y	4A	4B	VCC		
09 RLX	4 AND2	1A	1Y	2A	2B	2B	2Y	GND	3Y	3A	3B	4Y	4A	4B	VCC		
10 ALL	3 NAND3	1A	1B	1Y	2A	2C	2Y	GND	3Y	3A	3B	3C	1Y	1C	VCC		
11 ALL	3 NAND3	1A	1B	1Y	2A	2C	2Y	GND	3Y	3A	3B	3C	1Y	1C	VCC		
12 RX	3 NAND3	1A	1B	1Y	2A	2C	2Y	GND	3Y	3A	3B	3C	1Y	1C	VCC		
13 RX	2 NAND4	1A	1B	1C	1NC	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC			
14 RX	6 INVERT	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
15 HX5	3 AND3	1A	1B	1C	1D	2Y	2C	2Y	GND	3Y	3A	3B	3C	1Y	1C	VCC	
16 R	6 INVERT	1A	1B	1C	1D	2Y	2C	2Y	GND	3Y	3A	3B	3C	1Y	1C	VCC	
17 R	6 BUFFER	1A	1B	1C	1D	2Y	2C	2Y	GND	3Y	3A	3B	3C	1Y	1C	VCC	
18 X	6 INVERT	1A	1B	1C	1D	2Y	2C	2Y	GND	3Y	3A	3B	3C	1Y	1C	VCC	
20 ALL	2 AND4	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
21 RHX	2 AND4	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
22 RHX5	2 AND4	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
23 R	2 NOR4	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
24 X	4 NAND2	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
25 R	2 NOR4	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
26 RLX	4 NAND2	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
27 RX	3 NOR3	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
28 RX	4 NOR2	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
29 RLX	4 NOR2	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
30 RLX	1 NOR8	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
31 RLX	1 NOR8	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
32 RLX	4 NOR24MA	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
33 RLX	4 NOR2	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
34 RLX	4 NOR2	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
35 RLX	4 NOR2	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
36 RLX	4 NOR2	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
37 RLX	4 NOR2	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
38 RXS	4 NAND2	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
39 RXS	4 NAND2	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
40 RHX5	2 NAND4	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
41 RL	1 DECODER	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
42 RLX	1 DECODER	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
43 RL	1 DECODER	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
44 RL	1 DECODER	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
45 RL	1 DECODER	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
46 RLX	1 DECODER	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
47 RLX	1 DECODER	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
48 RX	1 DECODER	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
49 RX	1 DECODER	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
50 RLX	2 AND2-NOR2	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
51 RHS	2 AND2-NOR2	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
51 LX	2 AND2-NOR2	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
52 H	1 AND2(3,2,2)-OR2	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
53 R	1 AND2-NOR4	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
53 H	1 AND2(2,2,2)-NOR4	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
54 R	1 AND2-NOR4	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
54 H	1 AND2(2,2,2)-NOR4	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
54 LX	1 AND2(3,2,2)-NOR4	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
55 H	1 AND2-NOR4	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
56 RLX	2 EXPANDER4	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
61 H	3 EXPANDER3	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
62 H	1 AND2(3,2,2)-OR4	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
63 X	6 CURRENT-SENSE-INTERFACE	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
64 S	1 AND4(2,2,2)-NOR4	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
65 S	1 AND4(2,2,2)-NOR4	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
70 R	1 FF	JK PET PR CLR (AND-ED JK)	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC			
71 H	1 FF	JK MS PR CLR (AND-ED RS)	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC			
71 L	1 FF	RS MS PR CLR (AND-ED RS)	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC			
72 RLX	2 FF	JL CLR (CK*) (CR*, PR*) (LS IS NET)	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC			
74 ALL	2 LATCH	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
75 RLX	2 D-TYPE PET CLR (CK*) (PR*) (HLS)	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
76 RLX	2 FF	JL CLR (CK*) (CR*, PR*) (HLS)	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC			
77 RX	2 LATCH2	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
78 LX	2 FF	JL PR COM-CLR COM-CLK (PR*)	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC			
78 LX	2 FF	JL PR COM-CLR COM-CLK (PR*)	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC			
80 R	1 ADDER	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
81 R	1 ADDER	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
82 R	1 ADDER	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
83 RX	1 ADDER	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
84 R	1 RAM	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
85 RXS	1 COMPARATOR	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
85 L	1 COMPARATOR	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
86 RXS	4 XOR2	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
87 H	1 TRUE/COMP-ZERO/ONE	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
88 R	1 ROM	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
89 RLX	1 RAM	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC				
90 RLX	1 COUNTER-10	(2-5) CLR SET9	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC			
91 RLX	1 COUNTER-10	SISO GATED-INPUT	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC			
92 RX	1 COUNTER-12	(2-6) CLR	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC			
93 RX	1 COUNTER-16	(2-8) CLR	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC			
93 L	1 COUNTER-16	(2-8) CLR	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC			
94 R	1 SHREG64	SISO DUAL-PAR-PRESETS	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC			
95 RX	1 SHREG64	PIPO L/R CLR	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC			
96 RLX	1 SHREG64	PIPO L/R CLR	1A	1B	1C	1D	1Y	GND	2Y	2A	2B	2C	2D	2VCC			
97 R	1 RATE-MULTIPLIER	6-BIT SYNC	1A														

#	Type	#	Description	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	
245	X	8	BUS-XCEIVER			TS ST		DIR	A1	A2	A3	A4	A5	A6	A7	AB	GND	B8	B7	B6	B5	B4	B3	
246	R	1	DECODER			4/7SEG OC 30V		B	C	LT*	B1*	D	A	GND	QE*	OC*	OB*	QA*	OG*	VCC				
247	RX	1	DECODER			4/7SEG OC 15V		B	C	LT*	B1*	D	A	GND	QE*	OC*	OB*	QA*	OG*	VCC				
248	RX	1	DECODER			4/7SEG OC		B	C	LT*	B1*	D	A	GND	QE*	OC*	OB*	QA*	OG*	VCC				
251	RXS	1	SER-SELECTOR			1-OF-4 TS COM-SSEL		D3	02	01	Y*	W*	S*	GND	C	B	A	07	DO	D5	D4	D3	D2	
252	X	2	ADDRESSABLE-LATCH			4-BIT		B	C	103	IC1	1C1	1C1	1C1	1C1	2C1	2C1	2C1	2C1	2C1	2C1	2C1	2C1	
257	X	1	SELECTOR			1-OF-2 TS COM-SEL		A*	B*	1A	1B	1C	1D	1A	1B	1C	1D	08	018	028	038	028	038	
258	X	4	SELECTOR			1-OF-2 TS COM-SEL		A*	B*	1A	1B	1C	1D	1A	1B	1C	1D	09	38	38	38	38	38	
259	RX	1	ADDRESSABLE-LATCH			8-BIT		A	B	C	00	01	02	03	GND	04	05	06	07	01	06	05	04	
260	X	2	NORS																01	06	05	04	03	
261	X	1	MULTIPLIER			2-BIT BY 4-BIT		B3	B4	G	M2	04*	03	02	GND	01	00	M0	M1	B0	B1	B2	VCC	
265	R	4	COMPLEMENTARY-OUTPUT-ELEMENTS					A1	1B	1Y	2A	2A	2B	GND	3A	3B	3Y	3Z	4Y	4A	4B	VCC		
270	S	1	ROM			512-BY-8 OC		ADG	ADF	ADC	ADD	AD0	AD1	AD2	AD3	AD0	AD1	AD2	AD3	AD0	AD1	AD2	VCC	
271	S	1	ROM			256-BY-8 OC		AD0	ADB	ADC	ADD	AD0	AD1	AD2	AD3	AD0	AD1	AD2	AD3	AD0	AD1	AD2	VCC	
273	RXS	8	D-TYPE PET COM-CLK COM-CLR			CR*	10	1D	2D	20	30	3D	4D	40	GND	CK	50	50	60	70	70	80	80	VCC
274	S	1	MULTIPLIER			4-BIT BY 4-BIT		A1	A2	A3	B0	B1	B2	B3	GND	50	60	70	80	80	80	80	VCC	
275	X	1	WALLACE-TREES			7-BIT SLICE TS		2N	2N	2N	C2N	2N	2N	2N	GND	2N0	2N1	2N2	2N3	2N4	2N5	2N6	VCC	
278	R	1	PET			JK NET COM-CLR-PD		G1*	G2*	1K*	10	20	2*	20X	2J	2K	2N	30	30	30	30	30	30	VCC
279	RX	1	PRIORITY-REGISTER			4-BIT		G	D3	D4	GND	10	11	12	13	01	02	VCC						
280	X	1	SR-LATCH			(INPUTS ACTIVE LOW)		1R	1S1	1S2	1S3	2R	2S	2S	GND	30	31	32	33	40	45	45	VCC	
281	S	1	ALU-SHIFT4					G	H	N	I	EVN	OEND	GND	A	B	C	D	E	F	F	VCC		
283	RXS	1	ADDER			4-BIT FULL		A1	A2	RS1	RS2	RC	LR	C3	C4	P*	GND	Q3	Q4	Q4	Q5	Q6	Q7	VCC
284	R	1	MULTIPLIER			4-BIT BY 4-BIT		F3	F2	F1	F0	M	AS2	AS1	AS0	RL	CK	A0	VCC					
285	R	1	MULTIPLIER			4-BIT BY 4-BIT		NC	NC	NC	NC	NC	NC	VCC										
287	S	1	PROM			256-BY-4 TS		ADG	ADF	ADC	ADD	AD0	AD1	AD2	AD3	AD0	AD1	AD2	AD3	AD0	AD1	AD2	VCC	
288	S	1	PROM			32-BY-8 TS		AD0	AD1	AD2	AD3	D0	D1	D2	D3	D0	D1	D2	D3	D0	D1	D2	VCC	
289	X	1	COUNTER-10			(2-5) CORNER-POWER		AD0	AD1	S*	W*	1D	2D	2C	2B	2A	2D	2C	2B	2A	2D	2C	2B	VCC
290	X	1	PRE-DIVIDER			31-STAGE PET		AD0	AD1	AD2	AD3	AD0	AD1	AD2	AD3	AD0	AD1	AD2	AD3	AD0	AD1	AD2	VCC	
293	RX	1	COUNTER-16			(2-8) CORNER-POWER		AD0	AD1	AD2	AD3	AD0	AD1	AD2	AD3	AD0	AD1	AD2	AD3	AD0	AD1	AD2	VCC	
294	X	1	PRE-DIVIDER			15-STAGE PET		A	B	T	CK1	CK2	CK3	CK4	CK5	CK6	CK7	CK8	CK9	CK10	CK11	CK12	VCC	
295	X	1	SHREG4			PIPO L/R TS		S1	I1	IA	IB	IC	ID	S*	GND	C	D	OB	OA	VCC				
299	X	1	SELECTOR-LATCH			1-OF-2		B2	A2	B1	C1	D2	GND	C1	12*	CK*	OD	OB	QA	VCC				
300	X	1	SHREG8			PIP0 BUS-I/O TS L/R CLR		S0	G1*	G2*	G0*	E0*	C0*	A0*	CR*	GND	SIR	CK	B0	DOD	F0*	H0*	VCC	
301	S	1	RAM			256-BY-1 OC		ADA	ADB	ADC	ADD	AD0	AD1	AD2	AD3	AD0	AD1	AD2	AD3	AD0	AD1	AD2	VCC	
302	X	1	RAM			256-BY-1 OC		AD0	AD1	E1*	E2*	E3*	E4*	E5*	E6*	E7*	E8*	E9*	E10*	E11*	E12*	E13*	VCC	
314	X	1	RAM			1024-BY-1 OC POWERDOWN		AD0	AD1	E1*	E2*	E3*	E4*	E5*	E6*	E7*	E8*	E9*	E10*	E11*	E12*	E13*	VCC	
315	X	1	OSCILLATOR-XTAL			120MHz		TK1	TK2	TK3	TK4	TK5	TK6	TK7	TK8	TK9	TK10	TK11	TK12	TK13	TK14	TK15	VCC	
321	X	1	OSCILLATOR-XTAL			1-2MHZ DIVIDE		TK1	TK2	TK3	TK4	TK5	TK6	TK7	TK8	TK9	TK10	TK11	TK12	TK13	TK14	TK15	VCC	
322	X	1	SHREG8			PIP0 SIGN-EXTEND CLR TS		G*	P*	1D	2D	2C	2B	2A	2D	2C	2B	2A	2D	2C	2B	2A	VCC	
323	X	1	SHREG8			PIP0 BUS-I/O TS L/R CLR		S1*	G1*	G2*	G0*	E0*	C0*	A0*	CR*	GND	SIR	CK	B0	DOD	F0*	H0*	VCC	
340	S	8	BUFFER			TS INV 64MA		S0	G1*	G2*	G0*	E0*	C0*	A0*	CR*	GND	SIR	CK	B0	DOD	F0*	H0*	VCC	
341	S	8	BUFFER			TS 64MA		D0	D5	D4	D3	D2	D1	D0	D*	GND	SIR	CK	B0	DOD	F0*	H0*	VCC	
344	S	8	BUFFER			TS 64MA		D0	D5	D4	D3	D2	D1	D0	D*	GND	SIR	CK	B0	DOD	F0*	H0*	VCC	
347	X	1	DECODER			4/7SEG 24MA 7V OC		D0	D5	D4	D3	D2	D1	D0	D*	GND	SIR	CK	B0	DOD	F0*	H0*	VCC	
348	X	1	PRIORITY-ENCDR			8/3 TS		B	C	LT*	B1*	D	A	GND	QE*	OC*	OB*	QA*	OG*	VCC				
350	S	1	SHIFTER			4-BIT TS 0.12.3-POS		D0	D5	D4	D3	D2	D1	D0	D*	GND	SIR	CK	B0	DOD	F0*	H0*	VCC	
351	R	2	SELECTOR			1-OF-8 TS COM-SEL		I1*	I2*	I3*	I4*	I5*	I6*	I7*	I8*	I9*	I10*	I11*	I12*	I13*	I14*	I15*	VCC	
352	X	2	SELECTOR			1-OF-4 INV-OUT		I1*	I2*	I3*	I4*	I5*	I6*	I7*	I8*	I9*	I10*	I11*	I12*	I13*	I14*	I15*	VCC	
353	X	2	SELECTOR-LATCH			1-OF-4 INV-OUT TS		I1*	I2*	I3*	I4*	I5*	I6*	I7*	I8*	I9*	I10*	I11*	I12*	I13*	I14*	I15*	VCC	
355	X	1	SELECTOR-REG			1-OF-8 LLS OC		D0	D5	D4	D3	D2	D1	D0	D*	GND	SIR	CK	B0	DOD	F0*	H0*	VCC	
356	X	1	SELECTOR-REG			1-OF-8 PET TS		D0	D5	D4	D3	D2	D1	D0	D*	GND	SIR	CK	B0	DOD	F0*	H0*	VCC	
357	X	1	SELECTOR-REG			1-OF-8 PET OC		D0	D5	D4	D3	D2	D1	D0	D*	GND	SIR	CK	B0	DOD	F0*	H0*	VCC	
362	X	1	CLOCK			4-PHASE FOR-TMS9900-MPU		TK1	TK2	TK3	TK4	TK5	TK6	TK7	TK8	TK9	TK10	TK11	TK12	TK13	TK14	TK15	VCC	
363	X	1	LATCH8			HLS TS COM-CTRL		OC*	10	1D	2D	20	30	40	40	GND	CK	50	50	60	70	70	80	VCC
364	X	1	LATCH8			1024-BY-1 OC		ADG	ADF	ADC	ADD	AD0	AD1	AD2	AD3	AD0	AD1	AD2	AD3	AD0	AD1	AD2	VCC	
365	X	1	BUS-DRIVER			6-BIT TS COM-ENBL		AD0	AD1	AD2	AD3	D0	D1	D2	D3	D0	D1	D2	D3	D0	D1	D2	VCC	
366	X	1	BUS-DRIVER			TS COM-ENBL INV-OUT		D0	D1	D2	D3	D0	D1	D2	D3	D0	D1	D2	D3	D0	D1	D2	VCC	
367	RX	6	BUS-DRIVER			TS 24-BN		I1*	I2*	I3*	I4*	I5*	I6*	I7*	I8*	I9*	I10*	I11*	I12*	I13*	I14*	I15*	VCC	
368	RX	6	BUS-DRIVER			TS 24-BN		I1*	I2*	I3*	I4*	I5*	I6*	I7*	I8*	I9*	I10*	I11*	I12*	I13*	I14*	I15*	VCC	
371	S	1	ROM			256-BY-8 TS		ADG	ADF	ADC	ADD	AD0	AD1	AD2	AD3	AD0	AD1	AD2	AD3	AD0	AD1	AD2	VCC	
373	X	1	SHREG8			HLS TS COM-CTRL		AD0	AD1	AD2	AD3	D0	D1	D2	D3	D0	D1	D2	D3	D0	D1	D2	VCC	
374	X	1	SHREG8			1024-BY-1 OC		AD0	AD1	AD2	AD3	D0	D1	D2	D3	D0	D1	D2	D3	D0	D1	D2	VCC	
375	X	1	LATCH4			HLS CORNER-POWER		D0	D1*	D2*	D3*	D0	D1*	D2*	D3*	D0	D1*	D2*	D3*	D0	D1*	D2*	VCC	
376	R	4	FF			4 FF-PHASE FOR-TMS9900-MPU		TK1*	TK2*	TK3*	TK4*	TK5*	TK6*	TK7*	TK8*	TK9*	TK10*	TK11*	TK12*	TK13*	TK14*	VCC		
377	X	8	FF			D-TYPE PET COM-CTRL		I1*	I2*	I3*	I4*	I5*	I6*	I7*	I8*	I9*	I10*	I11*	I12*	I13*	I14*	VCC		
379	X	4	FF			D-TYPE PET COM-CTRL		I1*	I2*	I3*	I4*	I5*	I6*	I7*	I8*	I9*	I10*	I11*	I12*	I13*	I14*	VCC		
381	X	1	ALU			4-BIT 8-FUNCTION RIP-CY OV		I1*	I2*	I3*	I4*	I5*	I6*	I7*	I8*	I9*	I10*	I11*	I12*	I13*	I14*	VCC		
382	X	1	ALU			4-BIT 8-FUNCTION RIP-CY OV		I1*	I2*	I3*	I4*	I5*	I6*	I7*	I8*	I9*	I10*	I11*	I12*	I13*	I14*	VCC		
384	X	1	MULTIPLIER			8-BY-1-BIT 2-COMP		I1*	I2*	I3*	I4*	I5*	I6*	I7*	I8*	I9*	I10*	I11*	I12*	I13*	I14*	VCC		
385	X																							