Topology 01: S1 OFF, S2 ON

Physical State: Phase 2 stores energy (S2 ON). Phase 1 transfers stored energy to output (S1 OFF, D8 ON).

Inductor Equations

Capacitor Equations

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dvC1/dt = iL2 / C1
dvC3/dt = (-iL6) / C3
dvC0/dt = (iL2 + iL5 - P/vC0) / C0
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Symmetric Operation: Topology 01 is the mirror of Topology 10. Phase 1 now transfers ($L2 \rightarrow C1 \rightarrow L5 \rightarrow D8 \rightarrow Cout$) while Phase 2 stores. L5 releases stored energy forward (diL5/dt > 0).