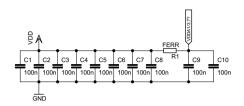
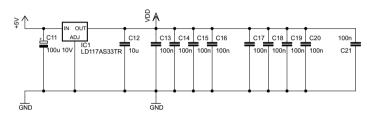
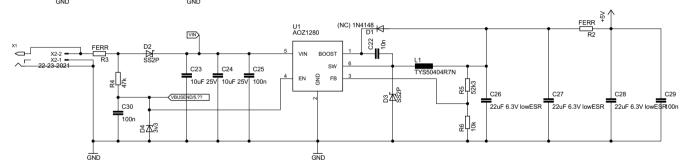
Axoloti Core 1.0 Schematics

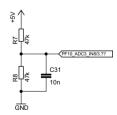
by Johannes Taelman

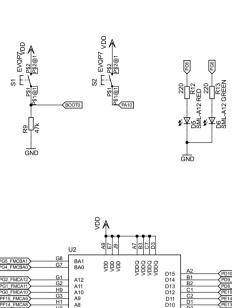


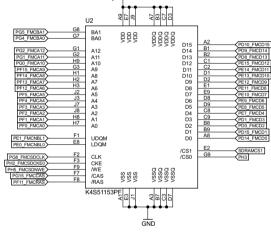




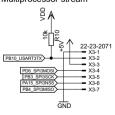
5V voltage supervisor



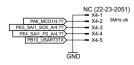


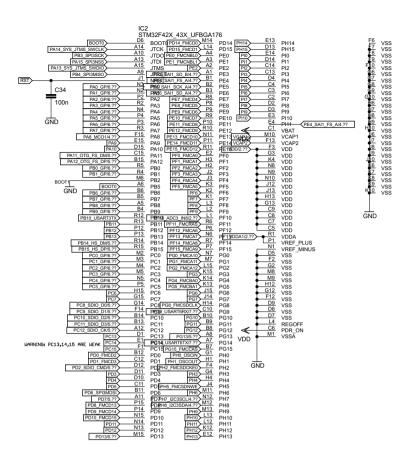


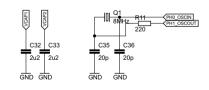
Multiprocessor stream

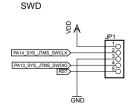


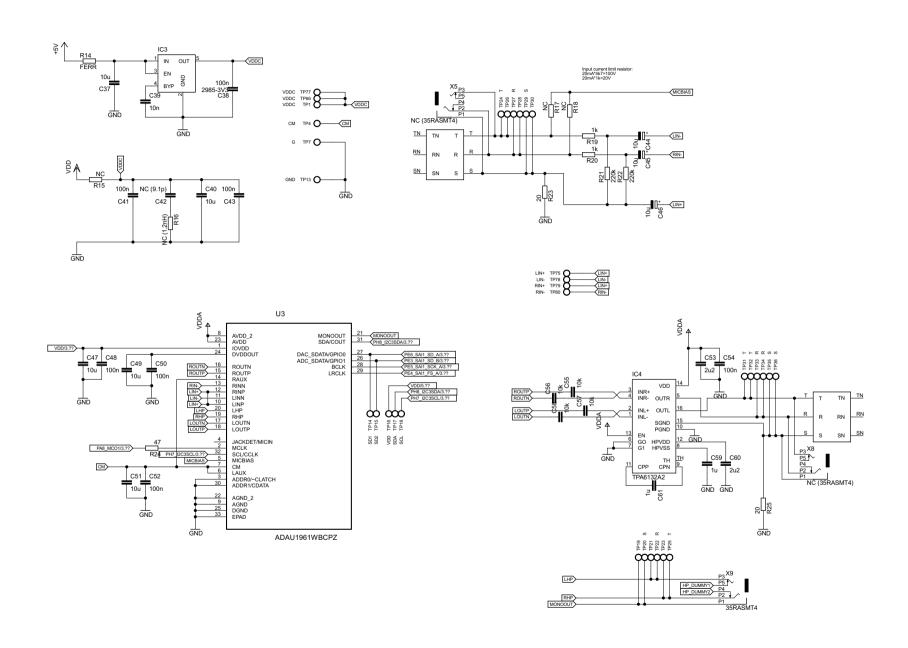
Multiprocessor sync

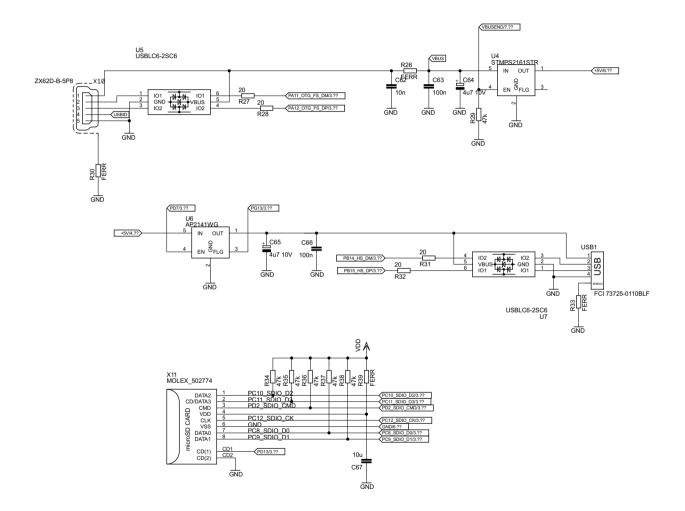


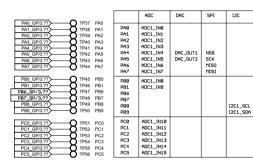




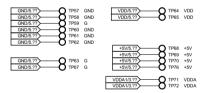


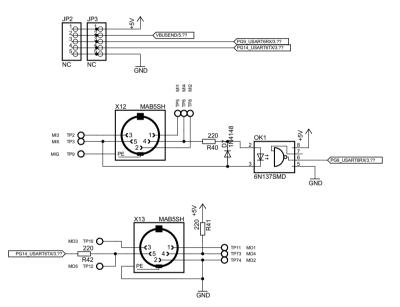






UART





F1	→ F2	MOUNT-HOLE3.0
F3	F4	MOUNT-HOLE3.0
O F5	O F6	MOUNT-HOLE3.0
O 15	O 15	MOUNT-HOLE3.0
() F7	F8	MOUNT-HOLE3.0
0	0	MOUNT-HOLE3.0
		MOUNT-HOLE3.0
		MOUNT-HOLE3.0

