

SEBIN SHAJI PHILIP

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EDUCATION

Università di Trento

September 2020 - July 2022

Master of Science in Embedded Systems

Trento, Italy

Courses: Real-time OS and middleware, Low power wireless networking for IoT, Robot planning and its applications. Research project on implementing Nonvolatile processor architecture (NVP for energy harvesters) on RISC-V core (ibex). (Minor: Entrepreneurship for Engineers, Business Development Lab)

Intermittent Computing Emulation of Ultra-Low-Power Processors: Evaluation of Backup Strategies for RISC-V - IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, April 20, 2022 [view].

KTH Royal Institute of Technology

September 2019 - July 2022

Master of Science in Embedded Systems

Stockholm, Sweden

Armada Career Host

EIT Digital Excellent Scholarship (C1)

Courses: Embedded Software (Realtime, Parallel computing and Models of Computation using ForSyDe), Computer Systems Architecture, Compilers and Execution Environments, Embedded Systems (HW SW co-design, Real-Time model and Priority driven scheduling, RTOS, Embedded Computing Platform).

TKMCE, University of Kerala

September 2012 - April 2016

Bachelor of Technology in Computer Science and Engineering.

Kerala, India

First Class with Distinction, GPA: 8.4/10

Courses: Digital Systems Design, Operating Systems and Network Programming, Data Structures and Algorithms, Microprocessor and Interfacing, High Performance Microprocessors, Digital Signal Processing, Distributed Systems, Digital Image Processing, Computer Graphics, Unix Systems Programming.

[Thesis] Entity Level Contextual Sentiment Detection of Topic Sensitive Influential Twitterers using SentiCircles.

IC3T Springer Journal: Published in 2016 [view]

Best project: Computer Society of India 5th In-App award

EXPERIENCE

Thales

August 2022 - December 2022

Software Engineer

Vienna, Austria

Involved in the support of TAS Control Platform (safety-critical fault-tolerant platform) as part of Thales Railway Signalling Solutions team. Contributed fixes towards TAS control platform NTP/Chrony time synchronization implementation and focused on TAS Core OS/Target handling work.

KAI (Kompetenzzentrum Automobil- und Industrielektronik), Infineon

October 2021 - June 2022

Master thesis student

Villach, Austria

Zephyr RTOS: Design of a Deterministic Ethernet Network for Machine-to-Machine (M2M) Communications[view]:

Thesis involve (i) writing complete XMC4 ethernet driver for Zephyr RTOS, supporting IEEE1588(PTP), VLAN tagging and DMA operations. (ii) Implementing timer, counter, clock and other peripheral driver and devicetrees of XMC for Zephyr (iii) Integrating network stacks like UDP, TCP, MQTT and MQTT-SN of Zephyr with ethernet (iv) integrating gPTP Zephyr stack for network time synchronization (IEEE 802.1AS) (v) Implementing real-time traffic shaping algorithms based on IEEE 802.1Qav (credit-based stream reservation) and investigate feasibility of IEEE802.1Qbv on XMC4. [See code here.](#)

ThinkSeed Systems

August 2021 - October 2021

Embedded Software Consultant

Remote

Design and develop android system services and C++ (JNI) layer for Apple wireless carplay stack interacting with Accessory (Automotive/Car dashboard) and iPhone (device). Including designing and implementing services and its state machines to maintain resource manager (BT, Audio, Video), wireless connection etc. (Ref: Apple Accessory Interface Specification: CarPlay, MFi etc.)

WindMachines AB

Embedded Software Engineer(intern)

July 2020 - December 2020

Stockholm, Sweden

Developing battery controller (charger/balancer) for Li-Ion and Pb-Acid batteries on a AVR based microcontroller (AT-MEGA32L) for novel Savonius Vertical axis wind turbines (50W – 2KW).

StepUp Air

Embedded Software Engineer(intern)

April 2020 - July 2020

Copenhagen, Denmark

Part of an early stage 2-member Embedded team responsible for developing code for 'StepUp Air', a breath measuring wearable for athletes. Technologies include BLE, ANT+, DFU, BOOTLOADER for Nordic Semiconductors nRF52 SoC.

Hewlett-Packard Enterprise

Firmware Engineer II

October 2018 - July 2019

India

Part of a dedicated 3-member team responsible for maintaining and do feature addition for firmware of HPE Apollo Gen10 server chassis controller (part of Baseboard Management Controller). Tasks include maintaining communication in RTOS via I2C, SPI etc., modifying cooling (fan/PWM), power regulation (PID controller) and BSP for new firmware requirements.

[server] Instant Power Reporting - MQTT Daemon: Designed and implemented an MQTT daemon (using PowerPC cross-compiled mosquito libs) on Linux initrd. This daemon exposed a very simple universal interface to communicate over TCP/IP (ethernet). Periodically reporting power(instant) events from the server tray controller to the central Power management unit(dashboard) was a use-case. It made use of both Apache Avro and JSON format as payloads.

Maintain firmware/state-machine of on-board chassis MegaCell (battery) on Smart-Array-Controller for HPE Apollo Gen10 servers.

AllGo Embedded Systems, Visteon Corp.

Embedded Software Engineer

July 2016 - Oct 2018

India

Part of multimedia and connectivity team responsible for requirement gathering, developing, documenting and bug fixing of the Embedded Linux based automotive multimedia stack as well as the connectivity stack for Tier1 automotive OEM companies.

Designed and Implemented WiFiPlugin for Wireless CarPlay and AndroidAuto device detection and connection. The library implementation made use of DNS-SD (Bonjour) protocol for service discovery and Hostapd for Wireless Access point management in Embedded Linux. (TATA Q5 Infotainment project)

Enabled direct playback from Android phone without using a device-driver: Removed complete device-driver dependency for MTP device read/write from MTP library(libmtp/linux) and redirect them into direct user-space(libusb) operations as part of AOSP project. Also implemented buffering feature for reducing read-latency and thus smooth audio playback music playback from MTP device. (Mitsubishi Infotainment project)

Design and implementation of Automotive MediaPlayer WAMP-C++ Binder layer (autobahn-cpp/bonefish) on top of CommonAPI(C++) layer for streamlining communication over JavaScript and C++. This implementation provides direct RPC calls as JSON message formats from WebInterface(Javascript) to the underlying stack (CommonAPI/C++).

Created and optimised gstreamer pipelines as part of codec/container validation on Renesas/imx platform and implemented audio-book/podcast support for embedded Sqlite3 based mediaplayer in C.

QBurst Technologies

Intern

May 2015 - June 2015

India

Designed and developed iOS app to calculate dimensions of an object (length x width) using iPhone camera in Objective-C. [See code here.](#)

TECHNICAL STRENGTHS

Tools	gcc, gdb, GNU Make, CMake, lcov, Coverity, cpp-lint, git, svn, RTC, Gerrit, IAR Workbench, Quartus Prime, ModelSim, Altera NiosII Qsys, MATLAB
OS	Linux/Ubuntu, Zephyr RTOS, Contiki, ThreadX, uCOS-II
Programming	C, C++, Bash shell scripting, Python, VHDL, System Verilog
Leadership skills	mentored and guided junior engineers for knowledge transfer, also collaborated with different R&D teams to gather all available resources/knowledge for designing embedded applications from scratch.

PROJECTS

[UniTrento] Course project of ‘Low power wireless networking for IoT’, for developing ‘Time Synchronization and Periodic Data Collection’ protocol on Contiki OS. Task involve developing time synchronized period data collection for wireless network nodes (with efficient duty-cycling for achieving low power – MAC layer), generating routing topology, scheduling transmissions and wakeups etc. [\[view\]](#)

[Master thesis code repo](#), which contains the out-of-tree drivers developed for the thesis. The main ethernet driver code can be found [here](#).

A simple POSIX shell implemented using a state machine [\[view\]](#).

HackerRank and IEEE Xtreme coding challenges [\[view\]](#).

[KTH] Parallel Multi-core (5-cores) implementation of Edge detection filters (Image processing) on Altera Nios II FPGA using bare metal and uCOS-II RTOS. Throughput requirement of 320 images per second (32x32 pixels). Involved Design space exploration of different actors and mapping different tasks on multiple processors considering real-time (deadline) requirements and utilizing parallel programming constructs. [\[view\]](#)

[KTH] Implemented efficient Vedic multipliers on FPGA for high performance cryptographic applications. [\[view\]](#)

[hobby-project] Implemented gstreamer based music playback application on Linux. The Linux userspace music player opened and played an OGG file in PC to alsasink by creating a filesrc-oggdemux-vorbisdec-alsasink pipeline in Gstreamer. [\[view\]](#)

[hobby-project] Basic USB device driver to read/write to bulk endpoint as part of Device driver study. [\[view\]](#)

[hobby-project] Multi-client (with username) supported TCP chat server as part of Socket programming study. [\[view\]](#)

REFERENCES

(Available upon request)