



# **CS223 Digital Design Section 2**

## **Project Report**

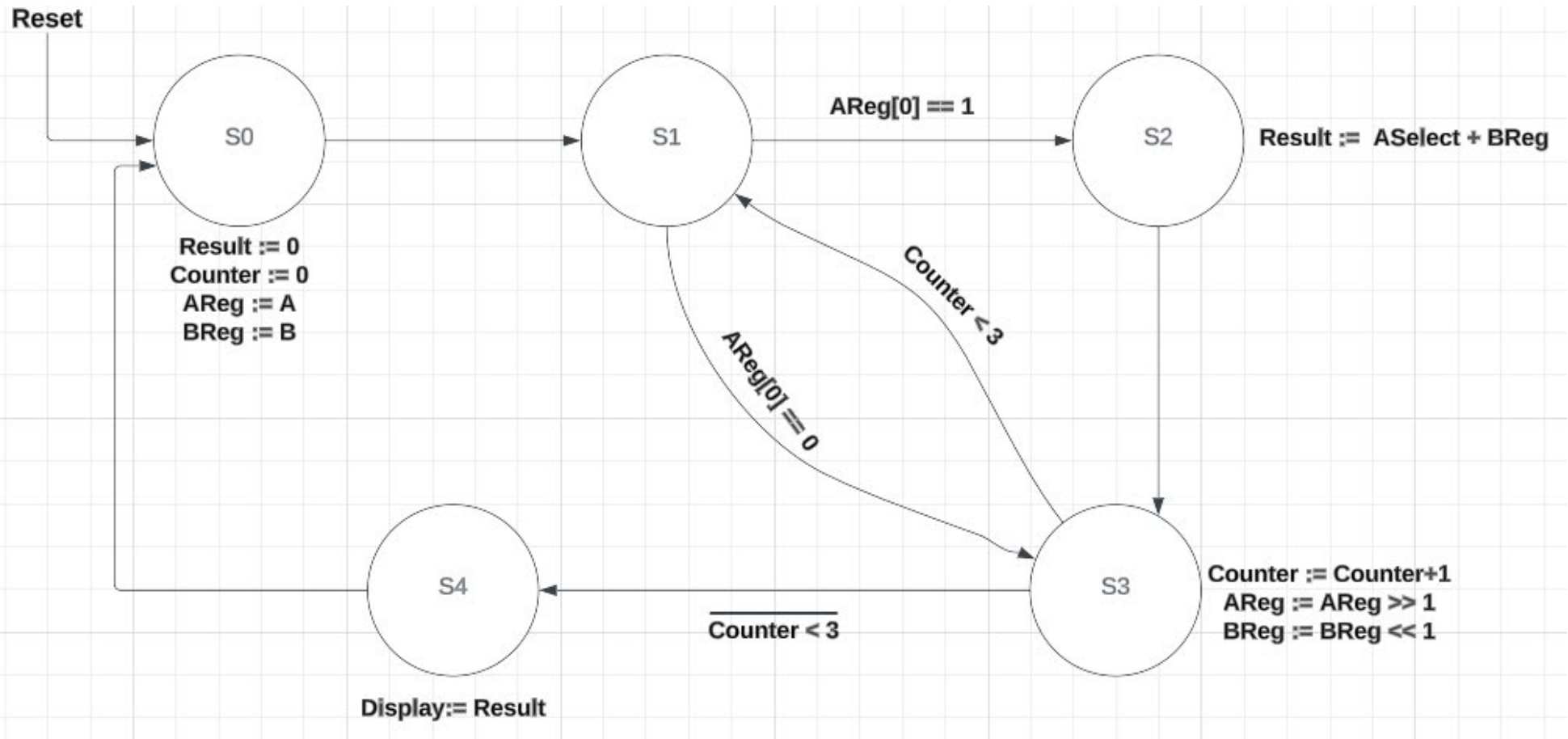
**Seçkin Alp Kargı**

**22001942**

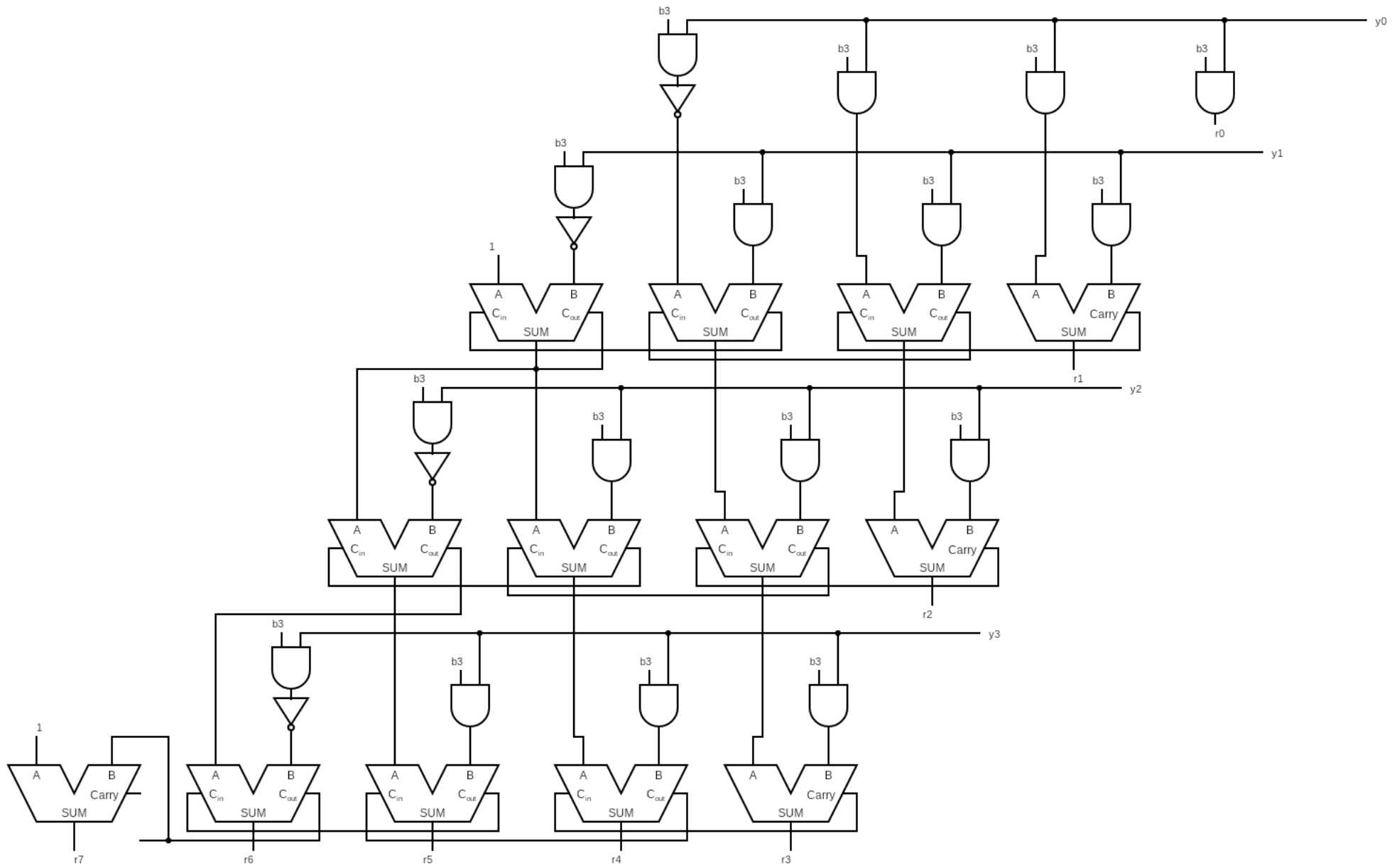
**CS**

**26.05.2023**

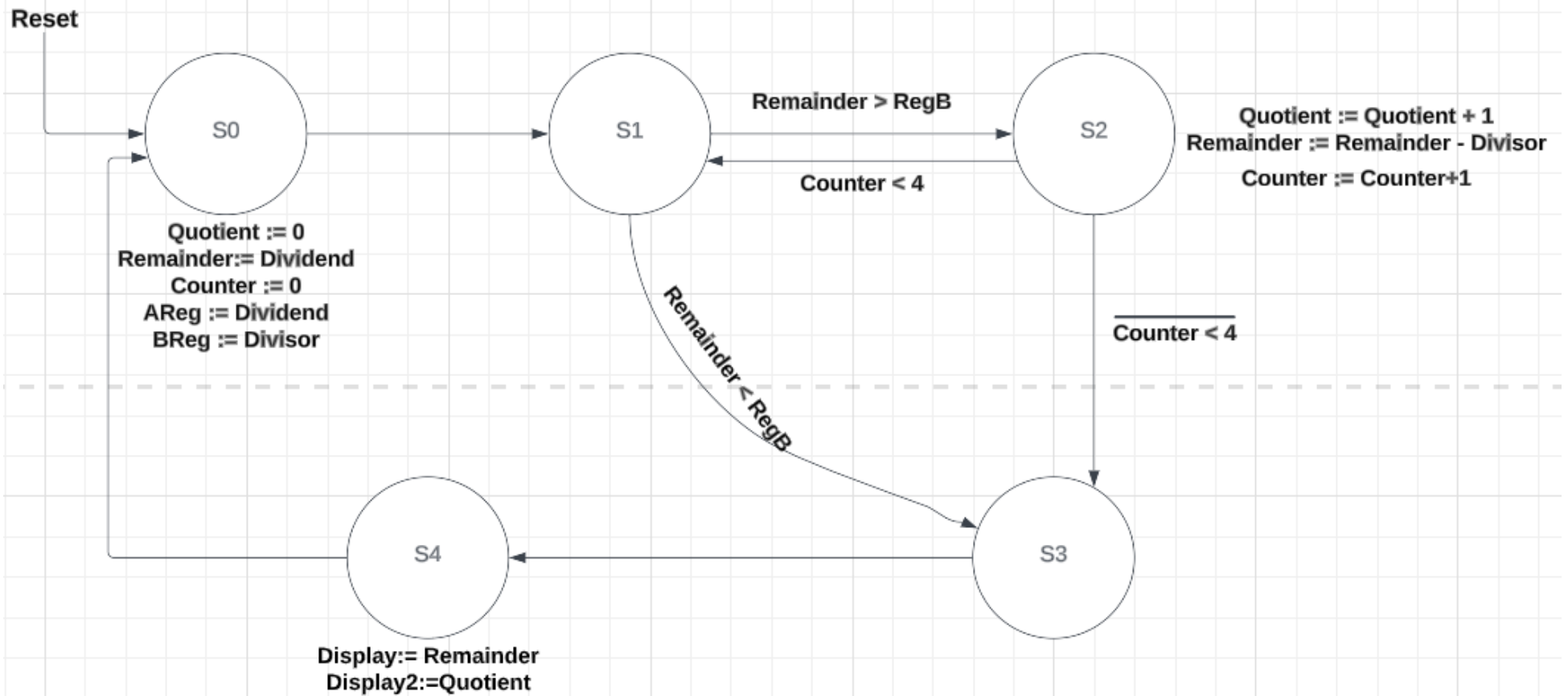
## HLSM Multiplication



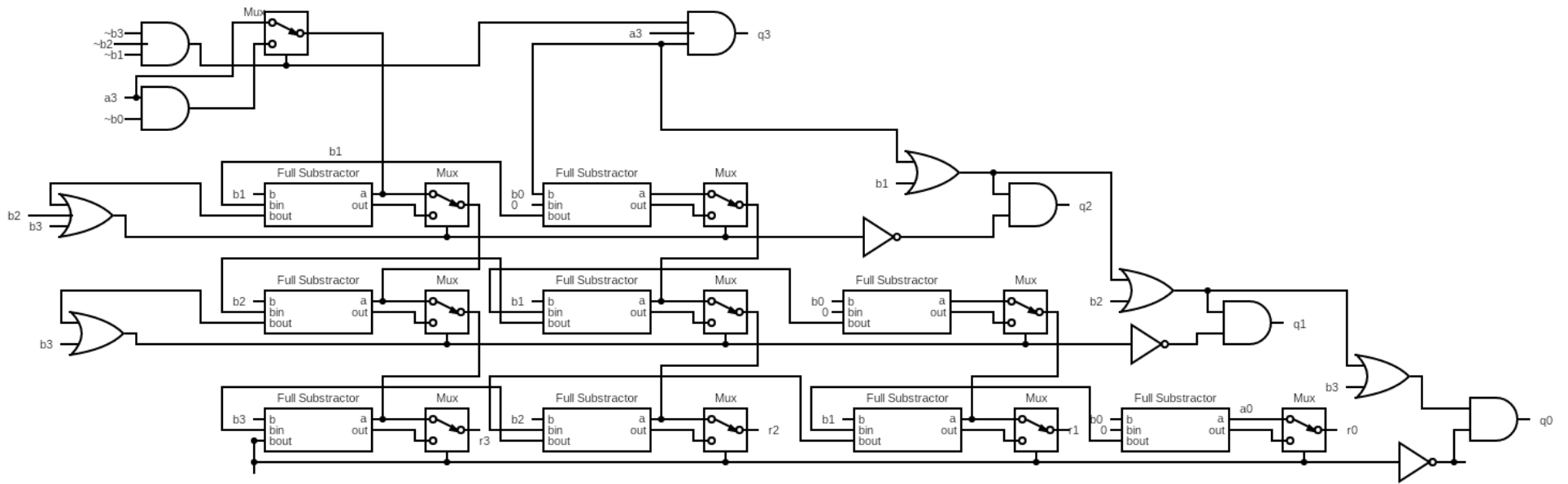
## RTL Schematic Multiplication



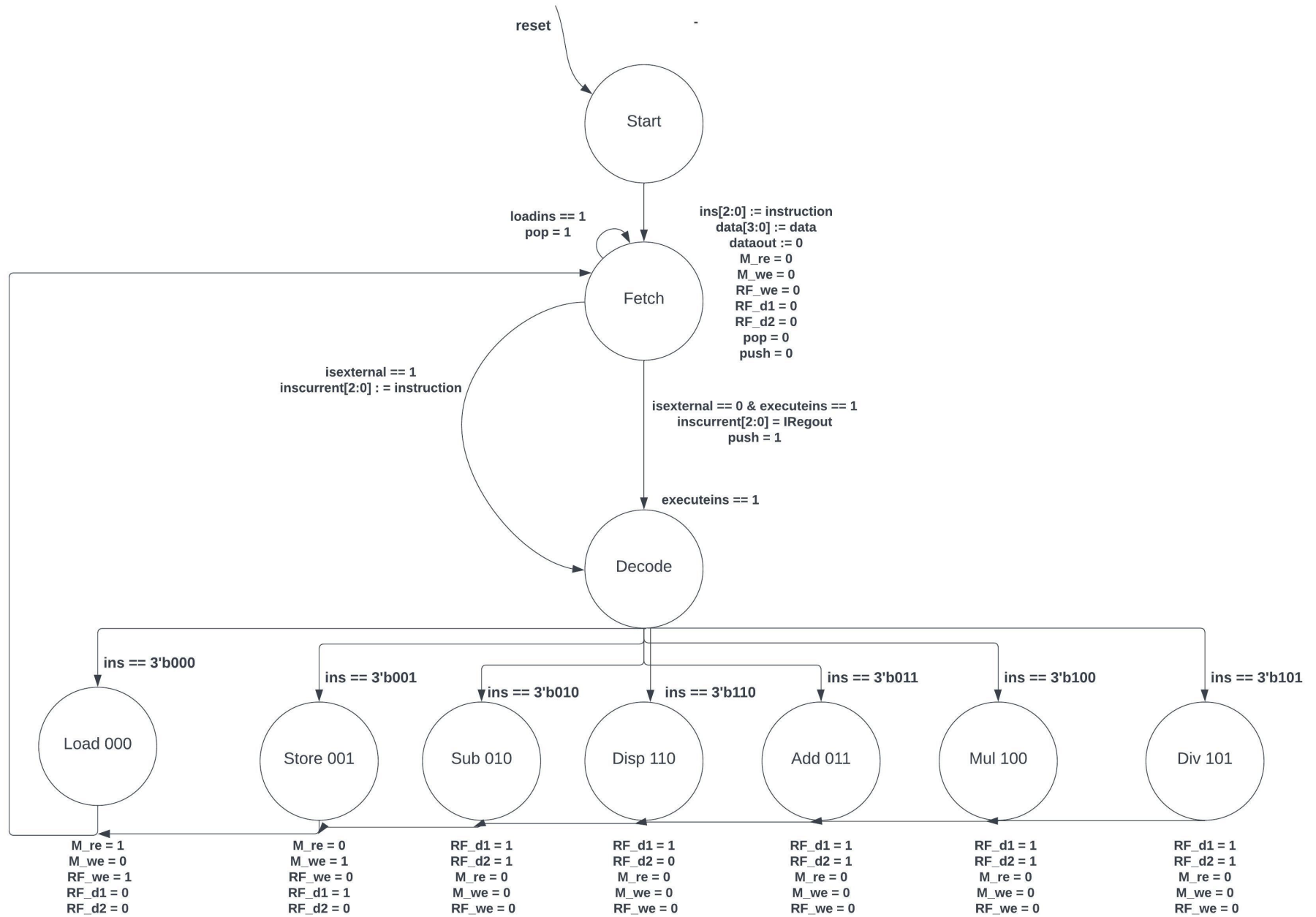
# HLSM Division



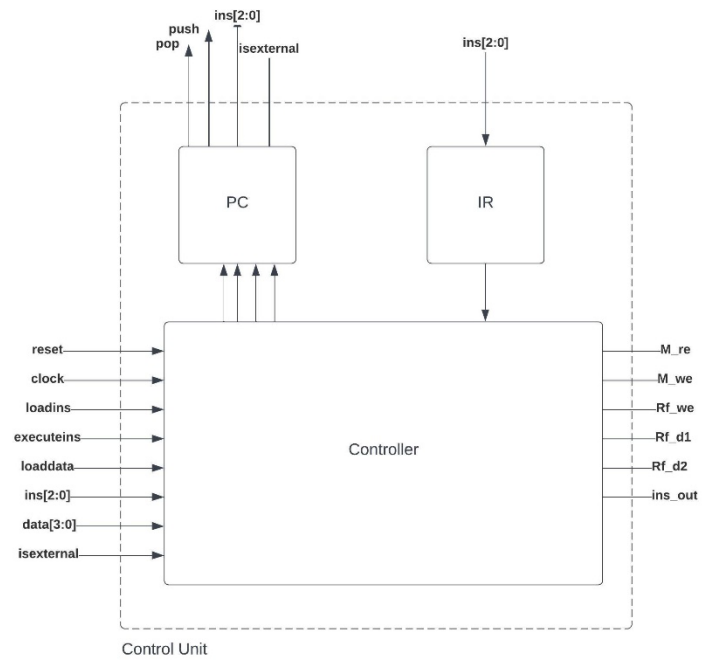
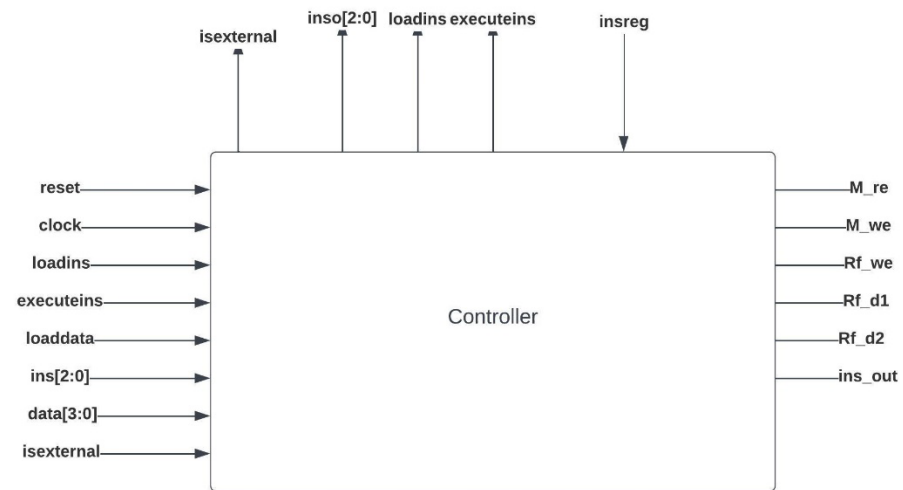
## RTL Schematic Division



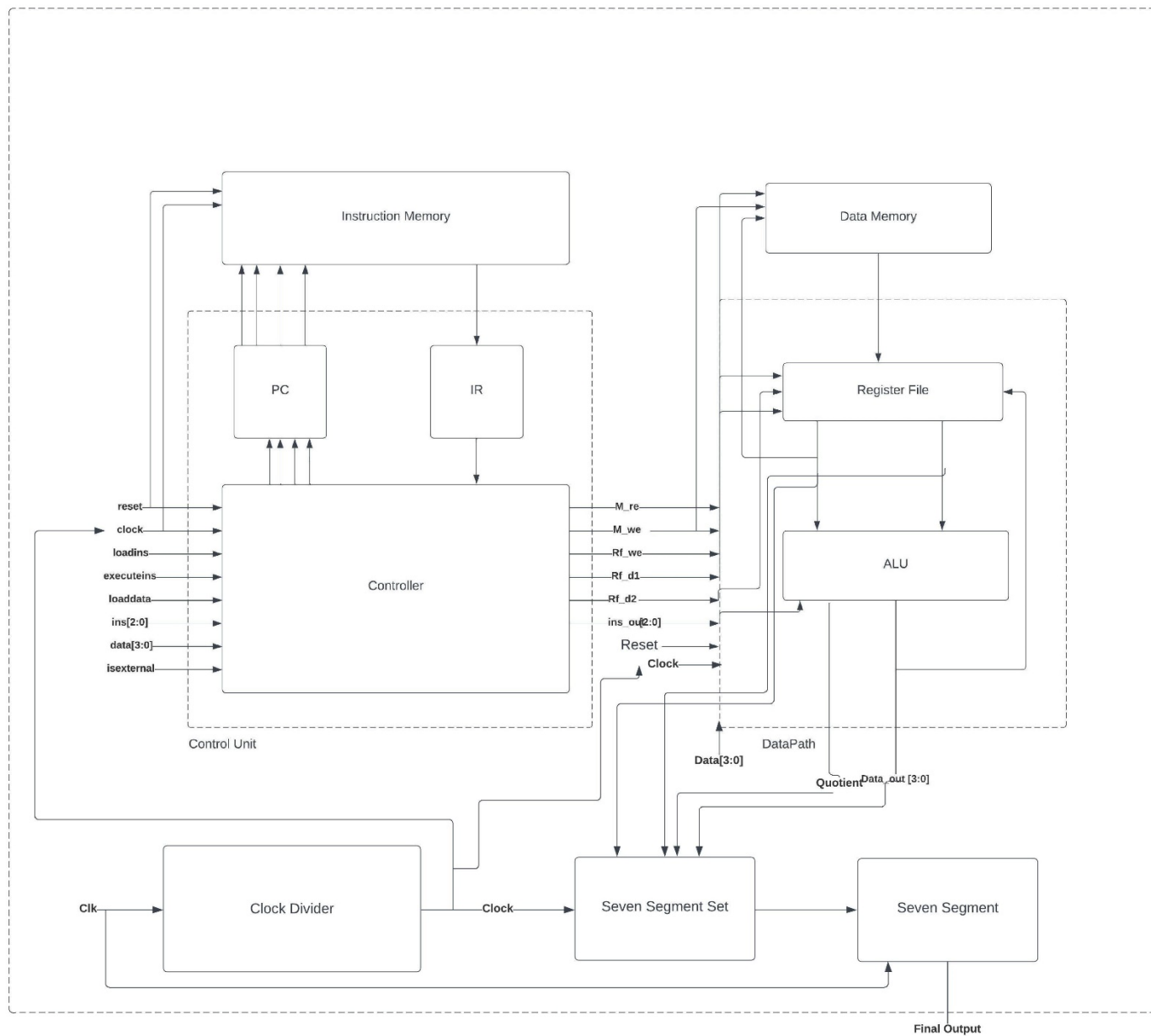
## Controller HLSM



## Controller Block Diagram



Controller DataPath Top Module Block Diagram





## TestBenchs