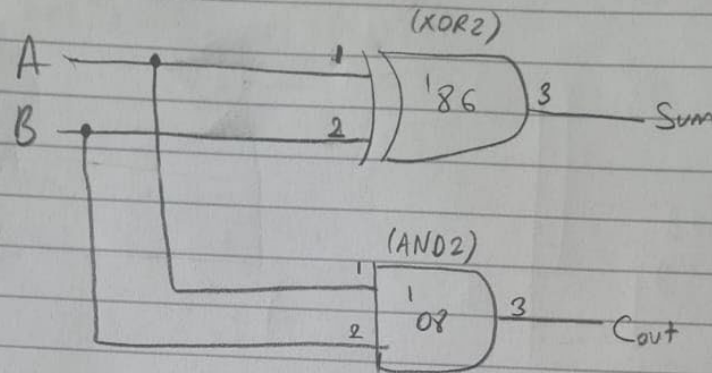


Seçkin Alp kargı - 22001942 - CS223/2
 Subject: half adder and subtractor

Date:

adder



IC list

- one 74LS86 Quad 2-input XOR gate
- one 74LS08 Quad 2-input AND gate

74LS86

+5V-14

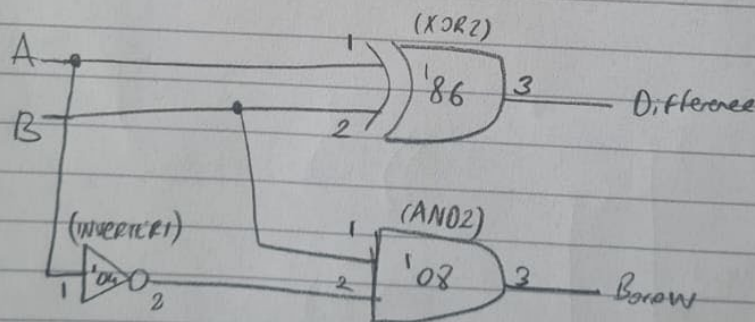
GND-7

74LS08

+5V-14

GND-7

Subtractor



IC list

- one 74LS86 Quad 2-input XOR gate
- one 74LS08 Quad 2-input AND gate
- one 74LS04 Quad 1-input inverter gate

74LS86

+5V-14

GND-7

74LS08

+5V-14

GND-7

74LS04

GND-7

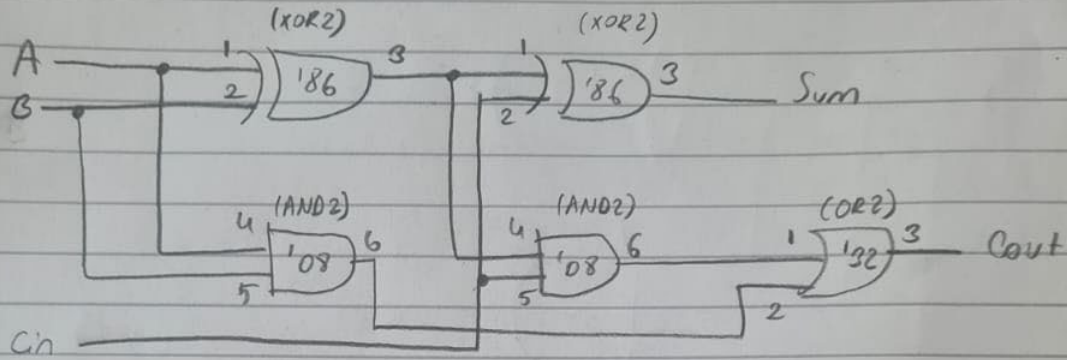
+5V-14

Seçkin Alp Korgı - 22001942 - CS223/2

Subject: full adder and subtracter

Date :/...../.....

adder

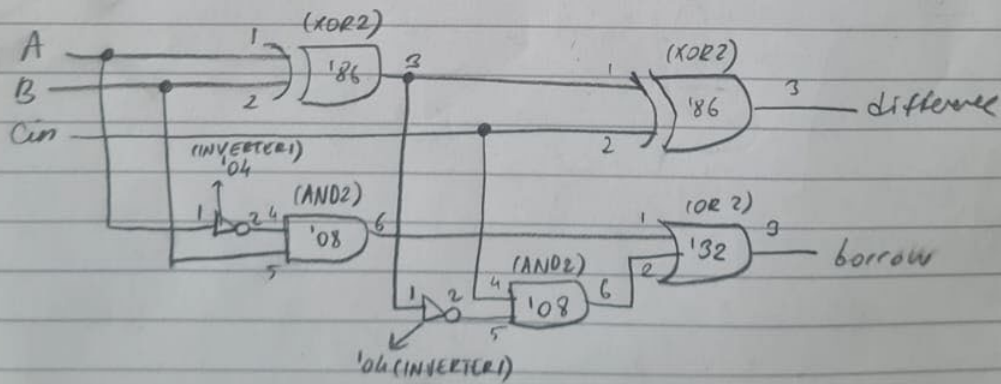


10 list

- one 74LS32 quad 2-input OR gate
- two 74LS86 quad 2-input XOR gate
- two 74LS08 quad 2-input AND gate

| | | |
|---------------|---------------|---------------|
| <u>74LS32</u> | <u>74LS86</u> | <u>74LS08</u> |
| GND-7 | GND-7 | GND-7 |
| +5V-14 | +5V-14 | +5V-14 |

Substrate



- relist

- one 74LS32 quad 2-input OR gate
- two 74LS08 quad 2-input AND gate
- two 74LS86 quad 2-input XOR gate
- two 74LS85 quad 2-input OR gate

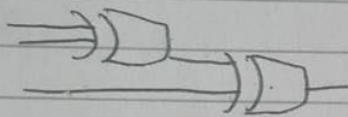
| | |
|-----------------|---------------|
| <u>746532</u> | <u>746532</u> |
| GND-7 | GND-7 |
| +5V-14 | +5V-14 |
| | |
| <u>746532.6</u> | <u>746532</u> |
| GND-7 | GND-7 |
| +5V-14 | +5V-14 |

Seçkin Alp Korgı - 22001962 - CS223/2

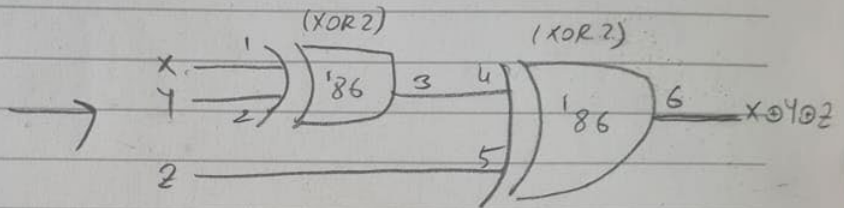
Subject: three input XOR gates using two input XOR gates

Date:/...../.....

Logic diagram



Circuit schematic



Kit list

- two 74LS86 quad 2-input XOR gate

74LS86

GND-7
+5V-14