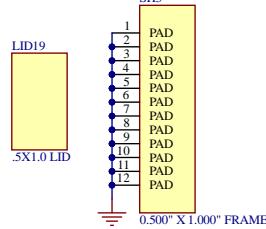
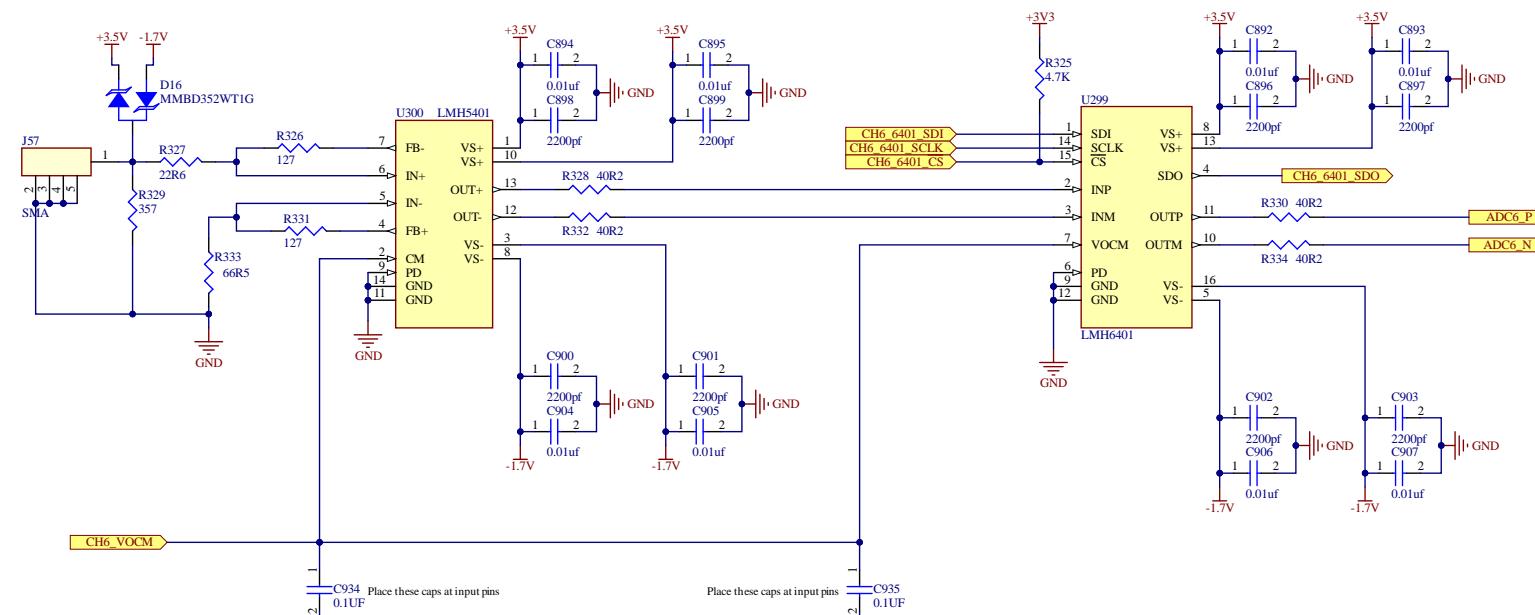
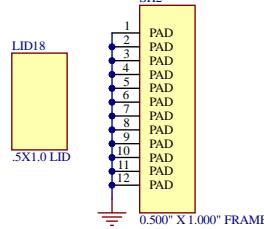
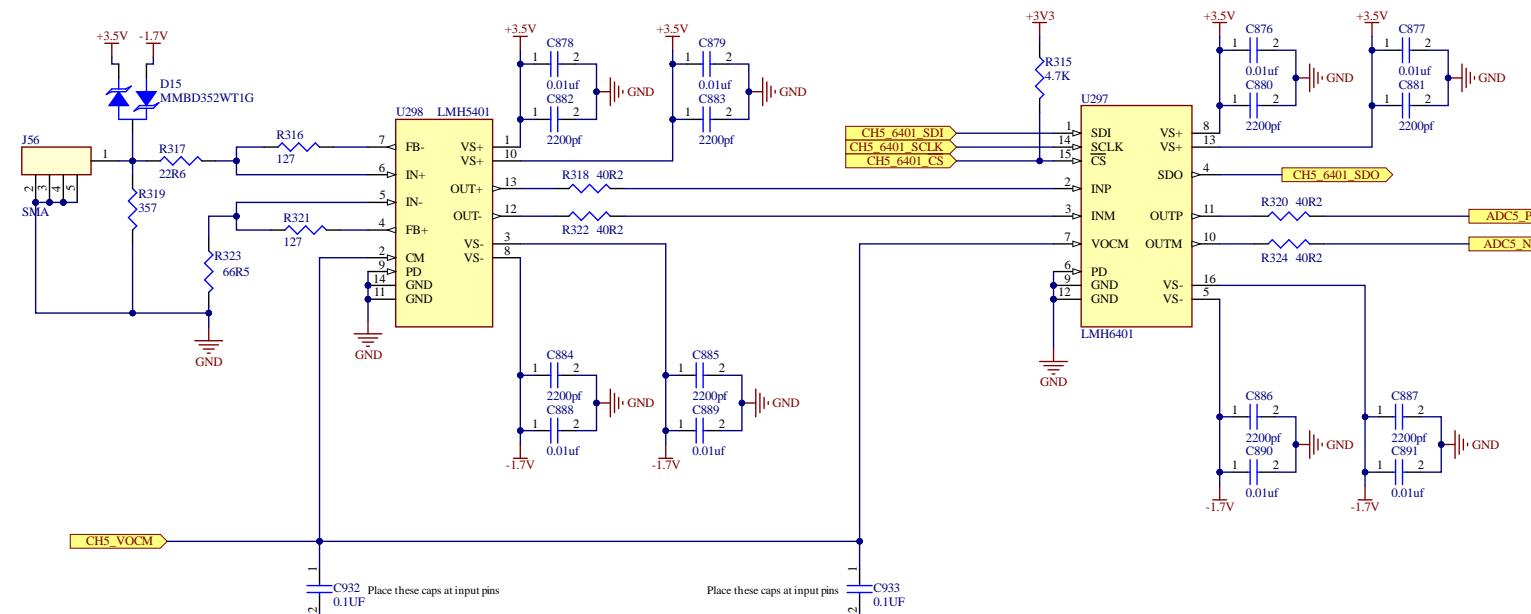


<b>Fermilab</b>			Operated by Fermi Research Alliance, LLC under Contract No. DE-AC02-07CH11359 with the United States Department of Energy.
Size	Rev	*	ENGINEER: G.Cancelo/K.Treptow
B	*	*	DRAWN: N.Wilcer
Number	Date:	12/16/2020	Sheet 2 of *
*	Time:	10:52:14 AM	APPROVED: *

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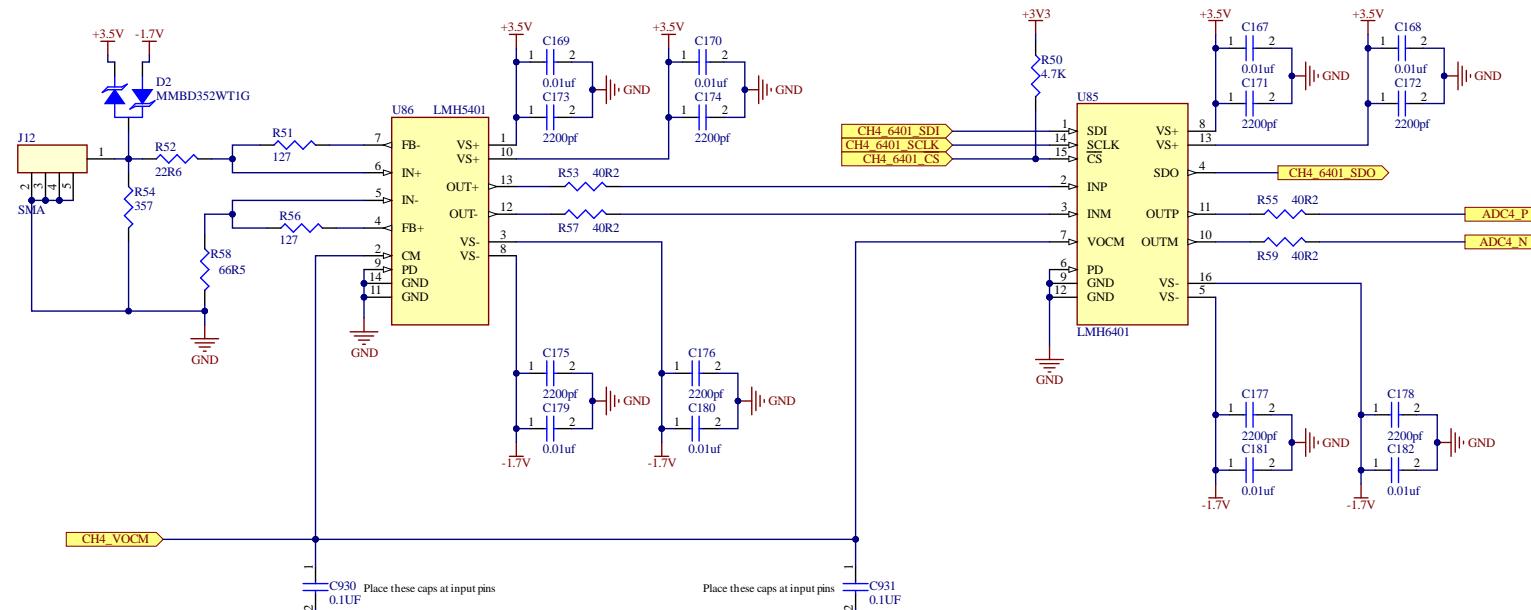
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Size	Rev	*	
B	*	*	
Number * Date: 12/16/2020 Sheet 2 of *			
	Time: 10:52:15 AM	CHECKED: *	APPROVED: *
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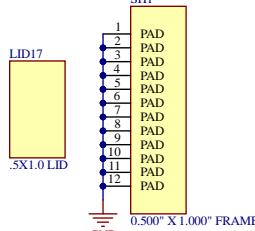
Operated by Fermi Research Alliance, LLC  
under Contract No. DE-AC02-07CH11359  
with the United States Department of Energy.

Size	Rev	*	ENGINEER: G.Cancelo/K.Treptow
B	*	*	DRAWN: N. Wilcer
Number	Date:	12/16/2020	Sheet
*	Time:	10:52:15 AM	2 of *
			APPROVED: *

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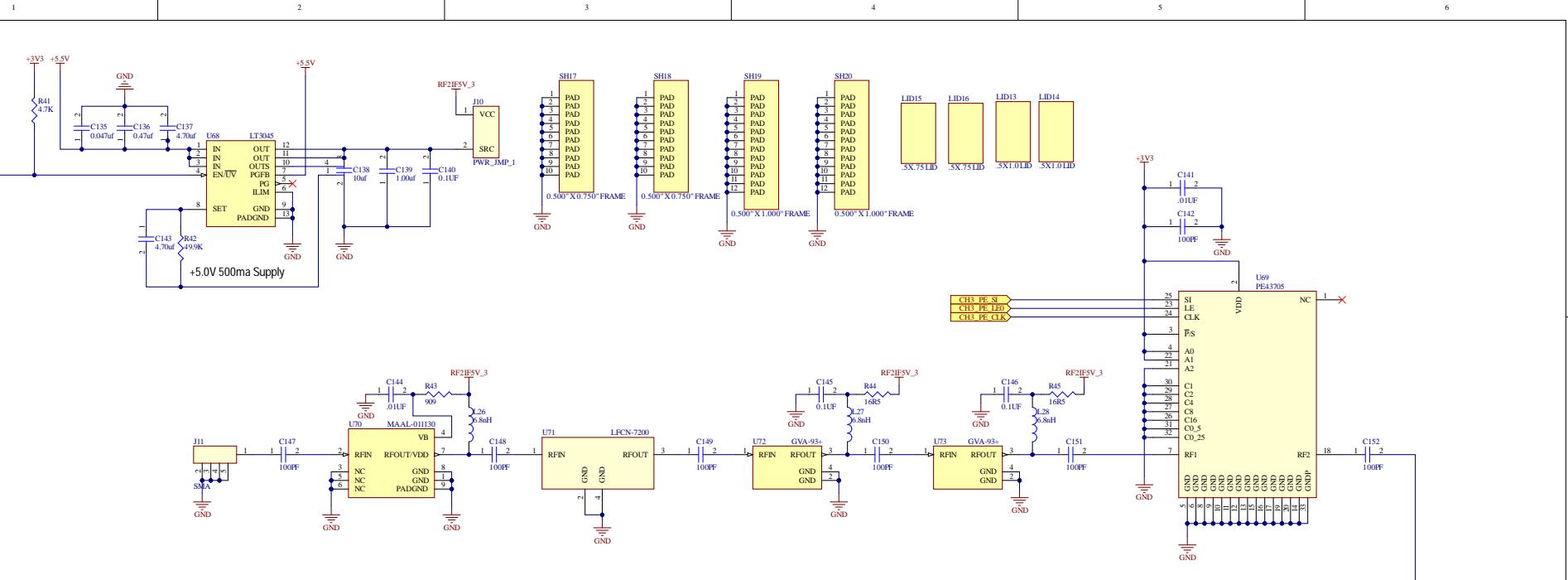
## Chan 4 RF to IF DC Input Signal Path



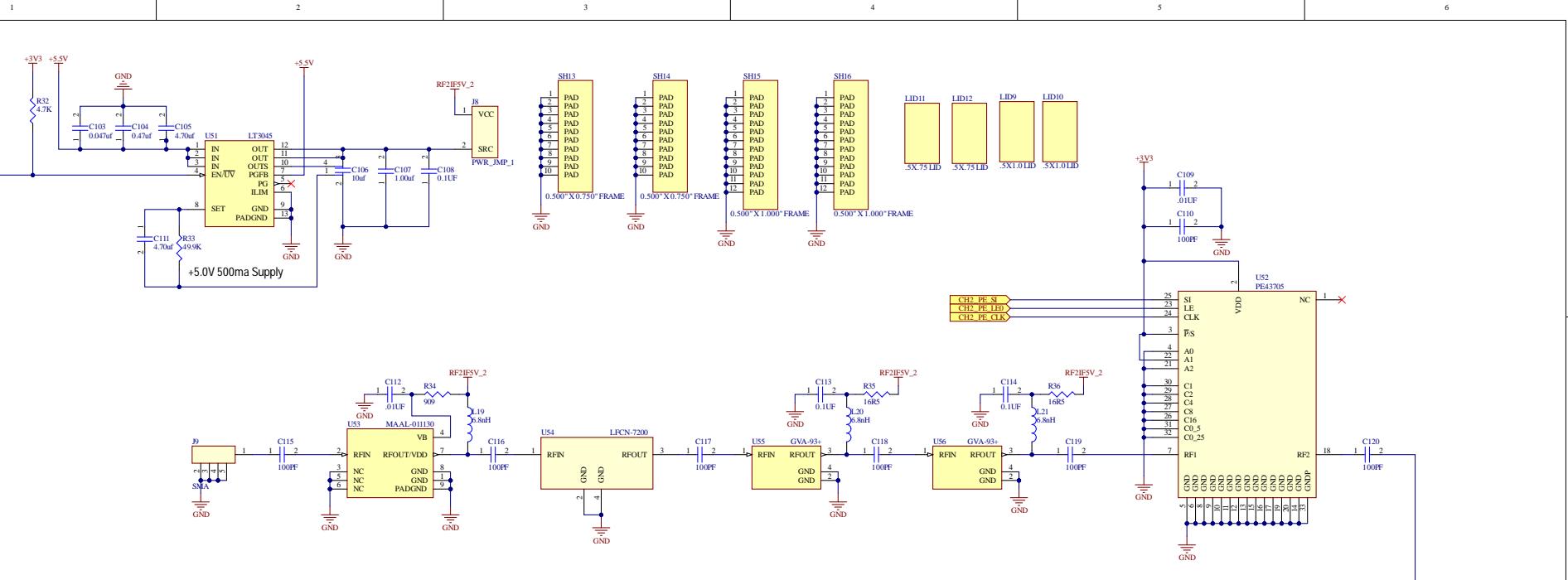
Operated by Fermi Research Alliance, LLC  
under Contract No. DE-AC02-07CH11359  
with the United States Department of Energy.

Size	Rev	*	ENGINEER: G.Cancelo/K.Treptow
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Number		Date: 12/16/2020	CHECKED: *
		Time: 10:52:15 AM	APPROVED: *

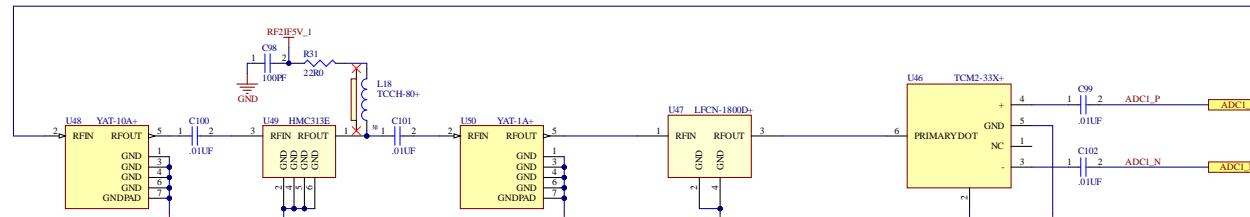
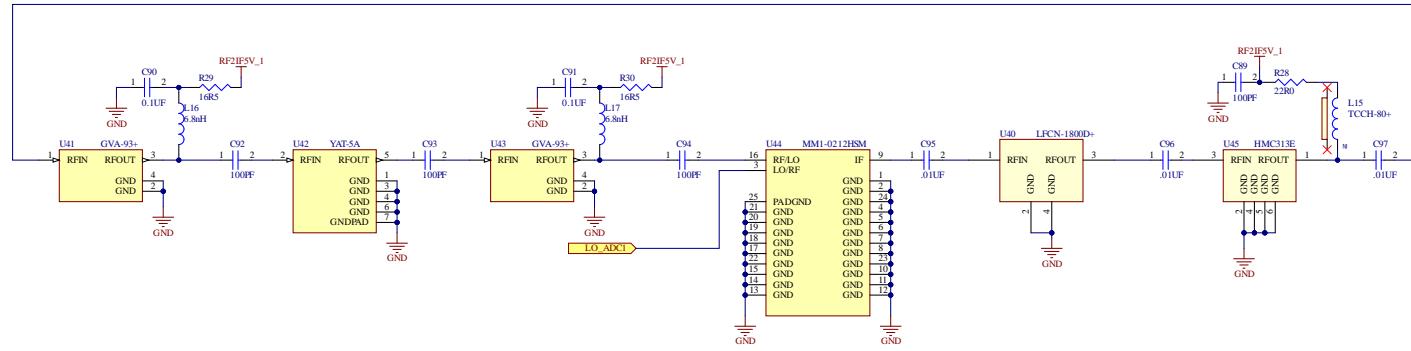
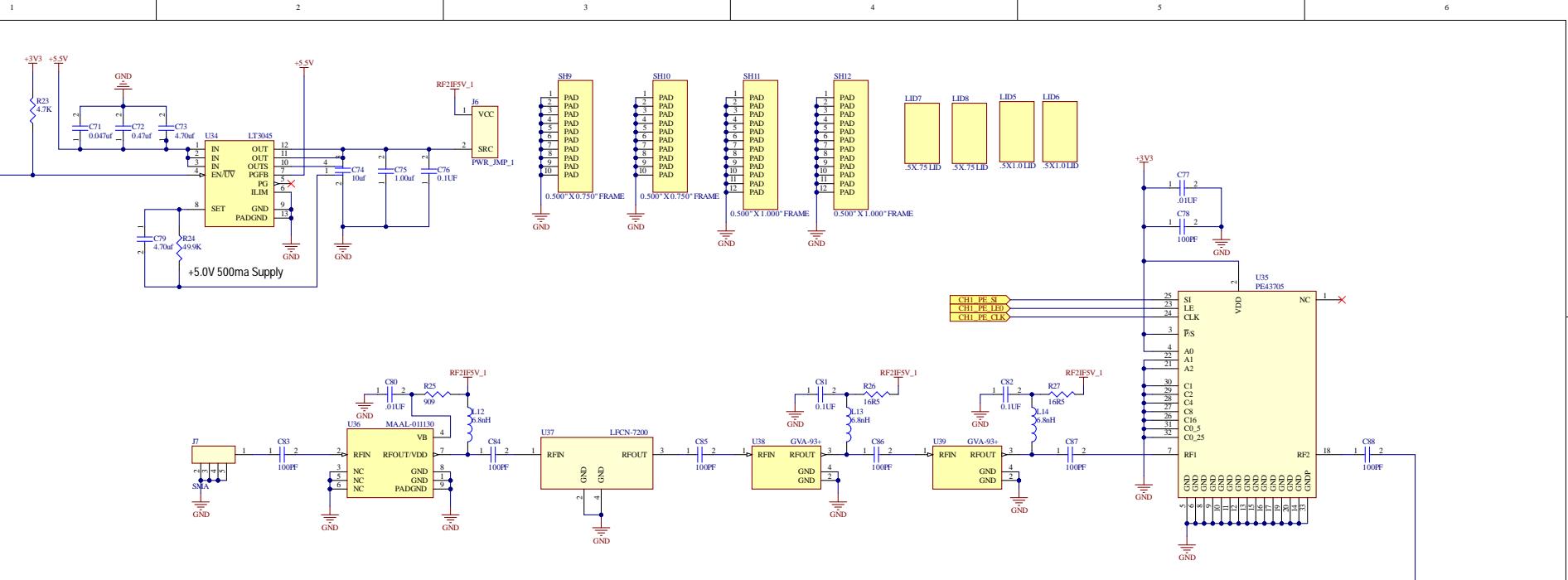
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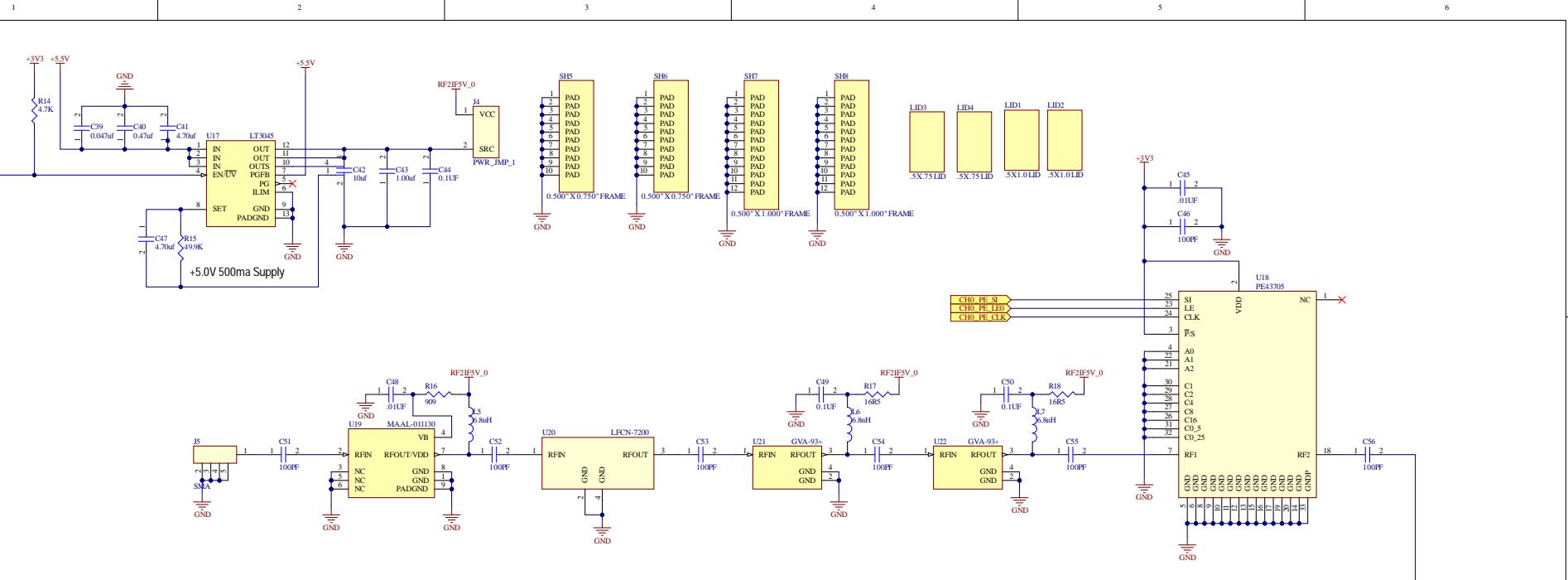
**Chan 3 RF to IF Input Signal Path**



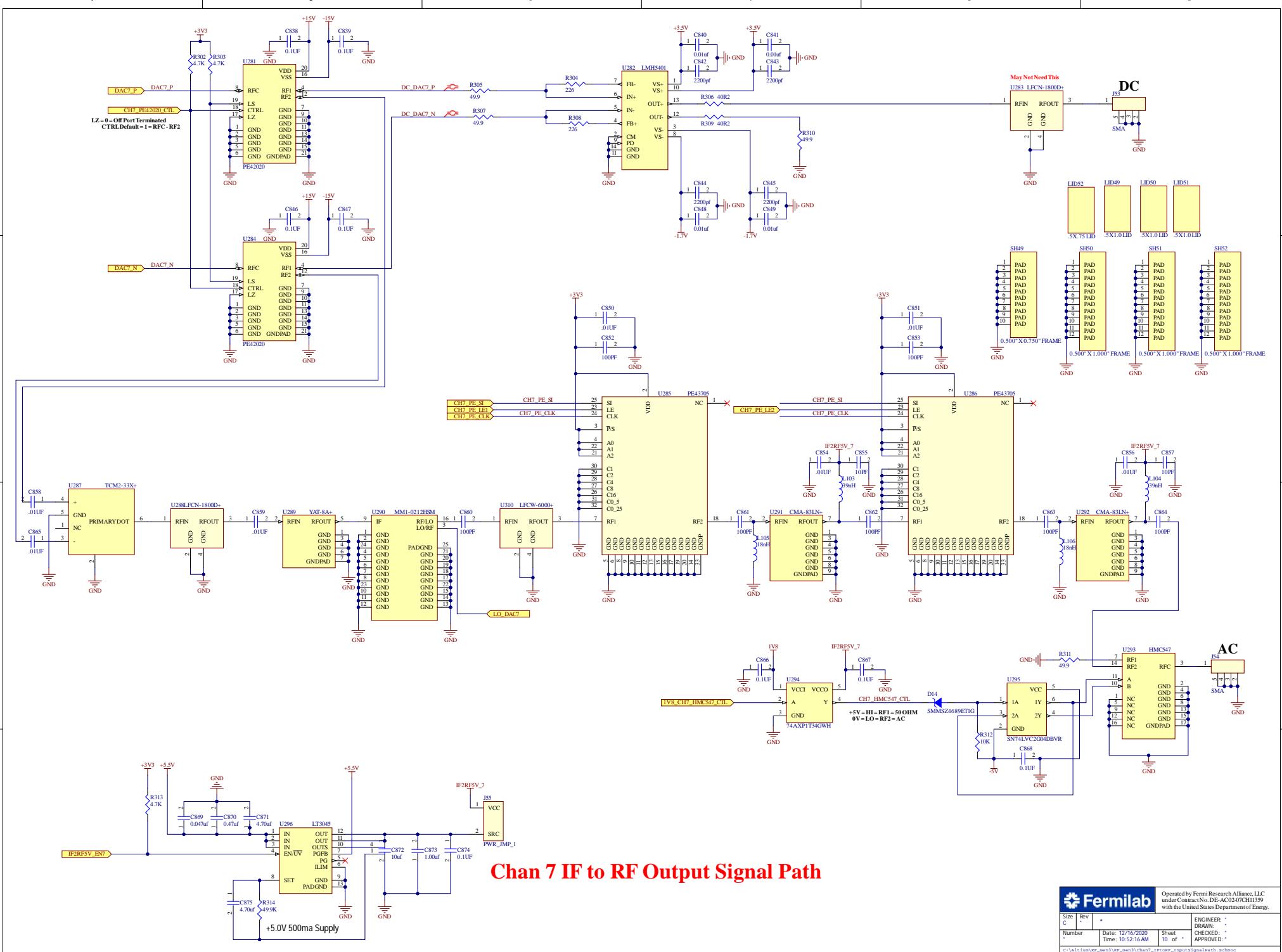
## Chan 2 RF to IF Input Signal Path

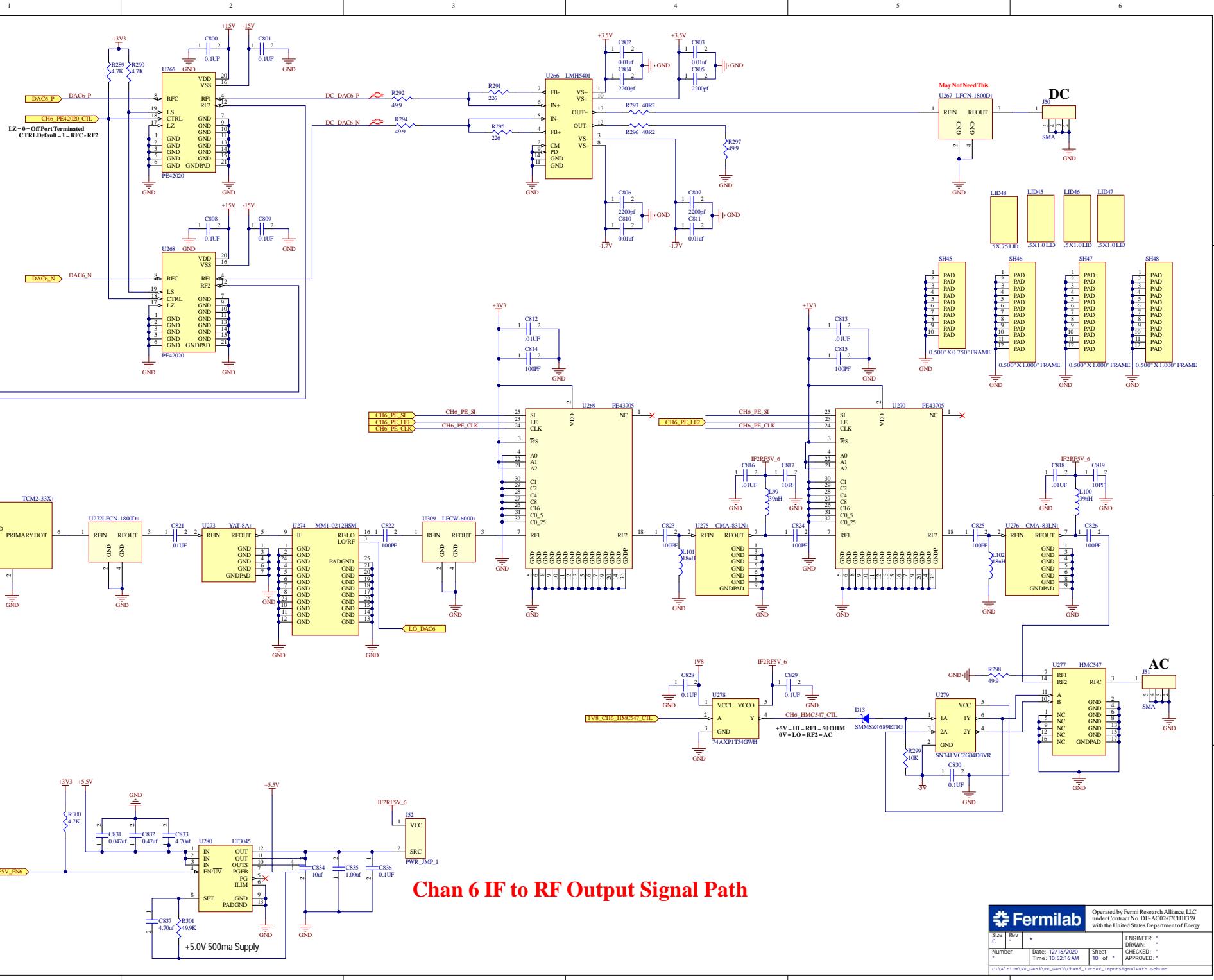


## Chan 1 RF to IF Input Signal Path



**Chan 0 RF to IF Input Signal Path**



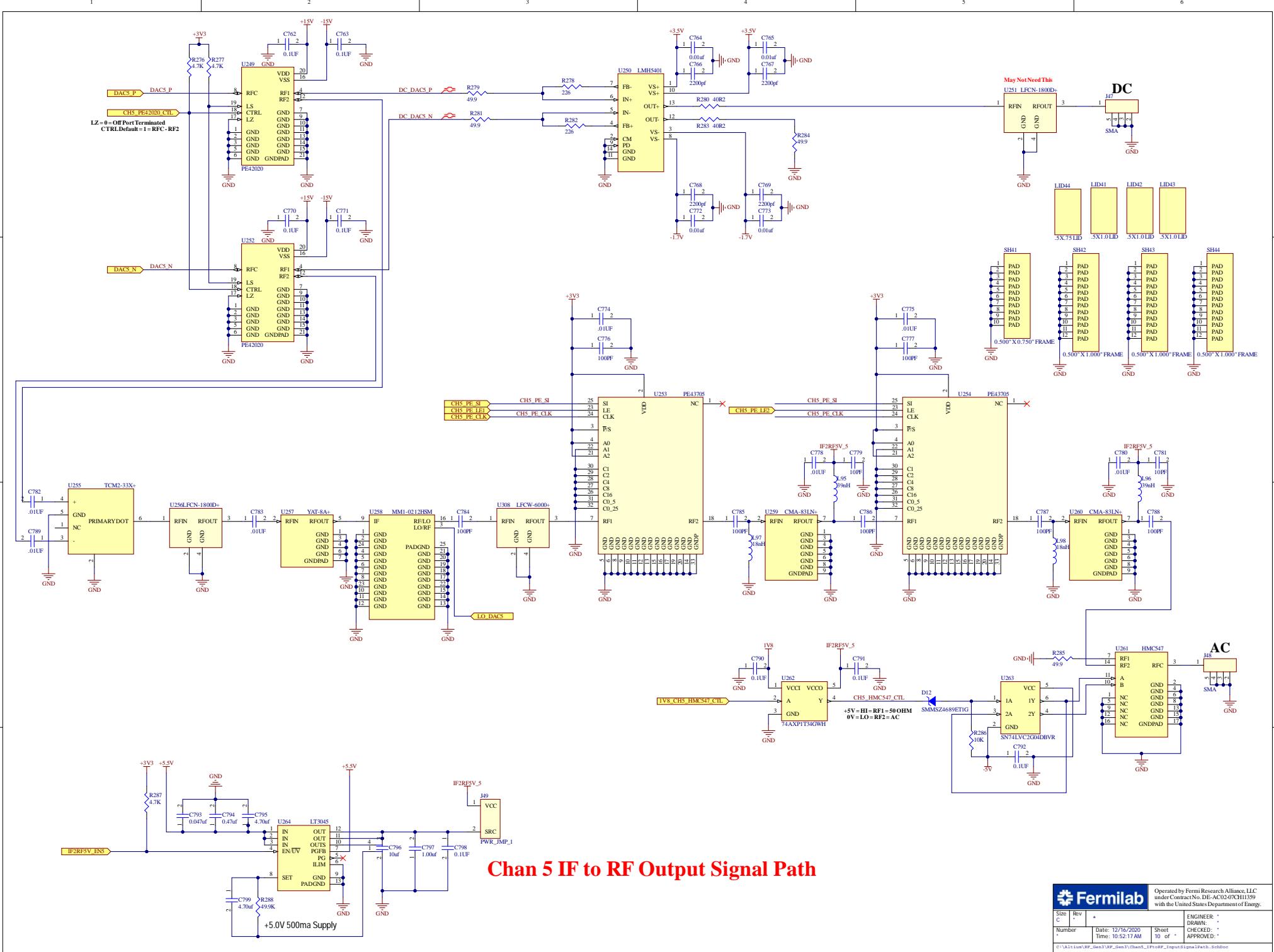


Fermilab

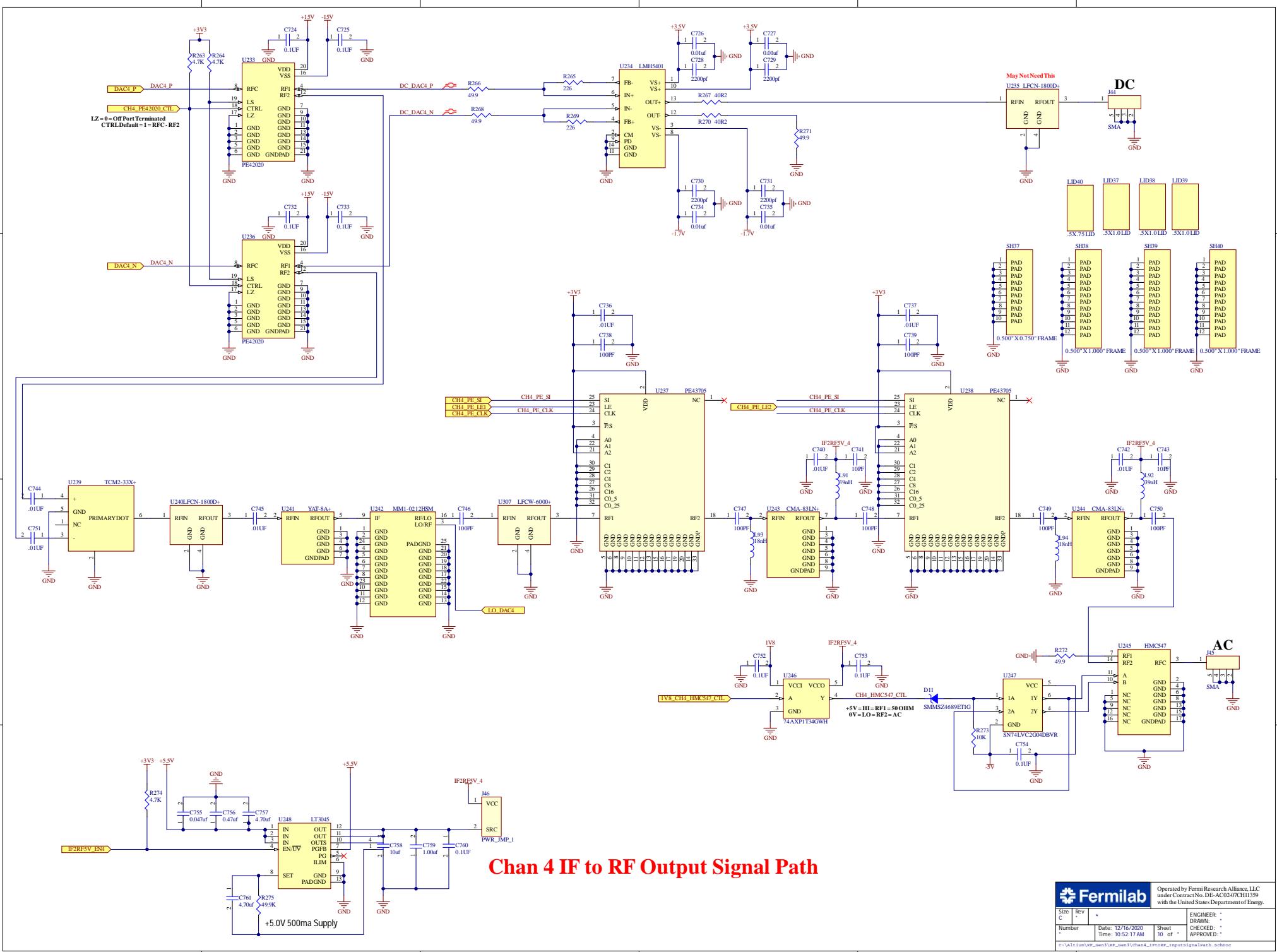
Operated by Fermi Research Alliance, LLC  
under Contract No. DE-AC02-07CH11359  
with the United States Department of Energy.

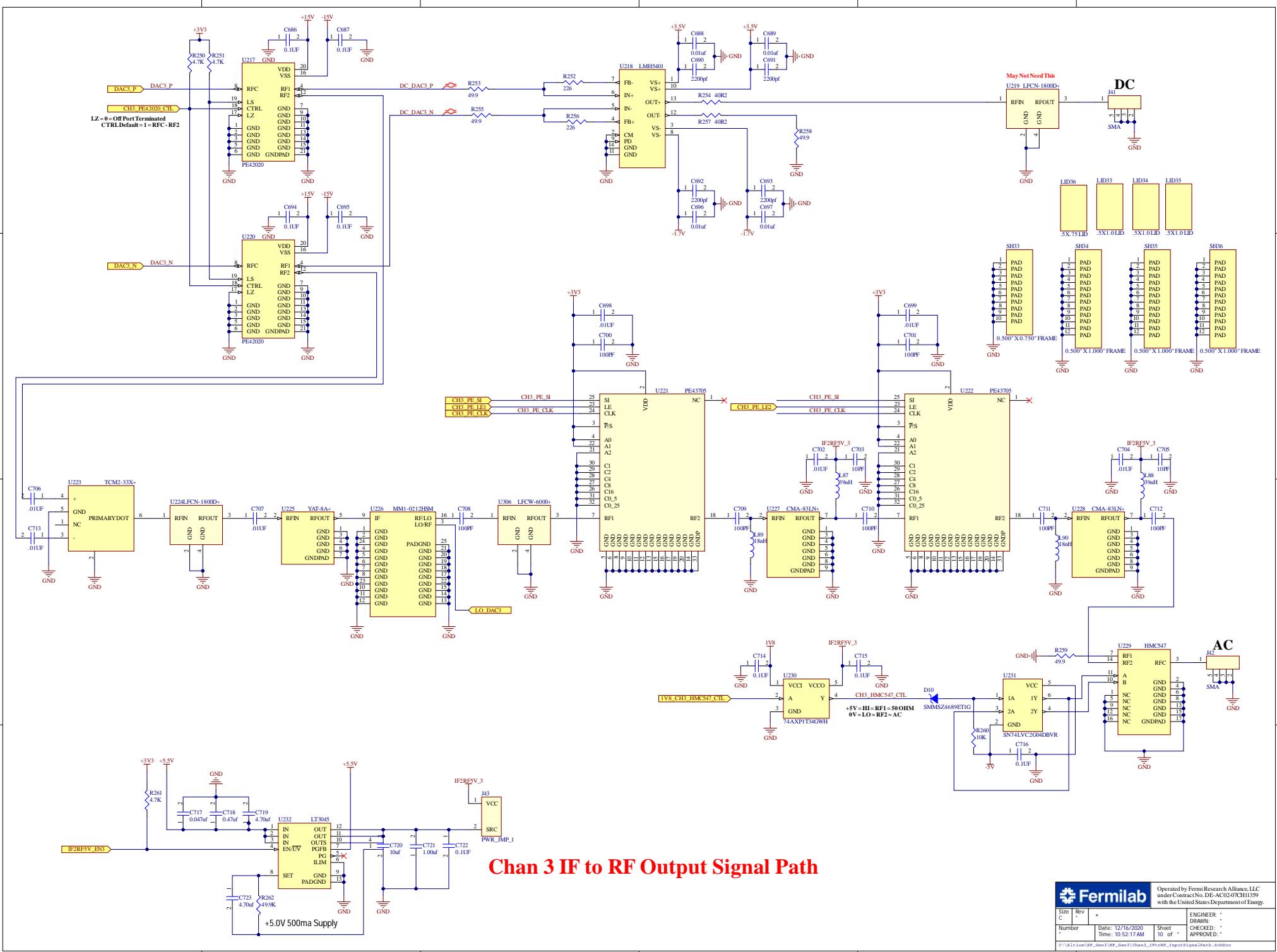
Size	Rev	*
C		
Number	Date: 12/16/2020	Sheet 10 of *
	Time: 10:52:16 AM	APPROVED: *

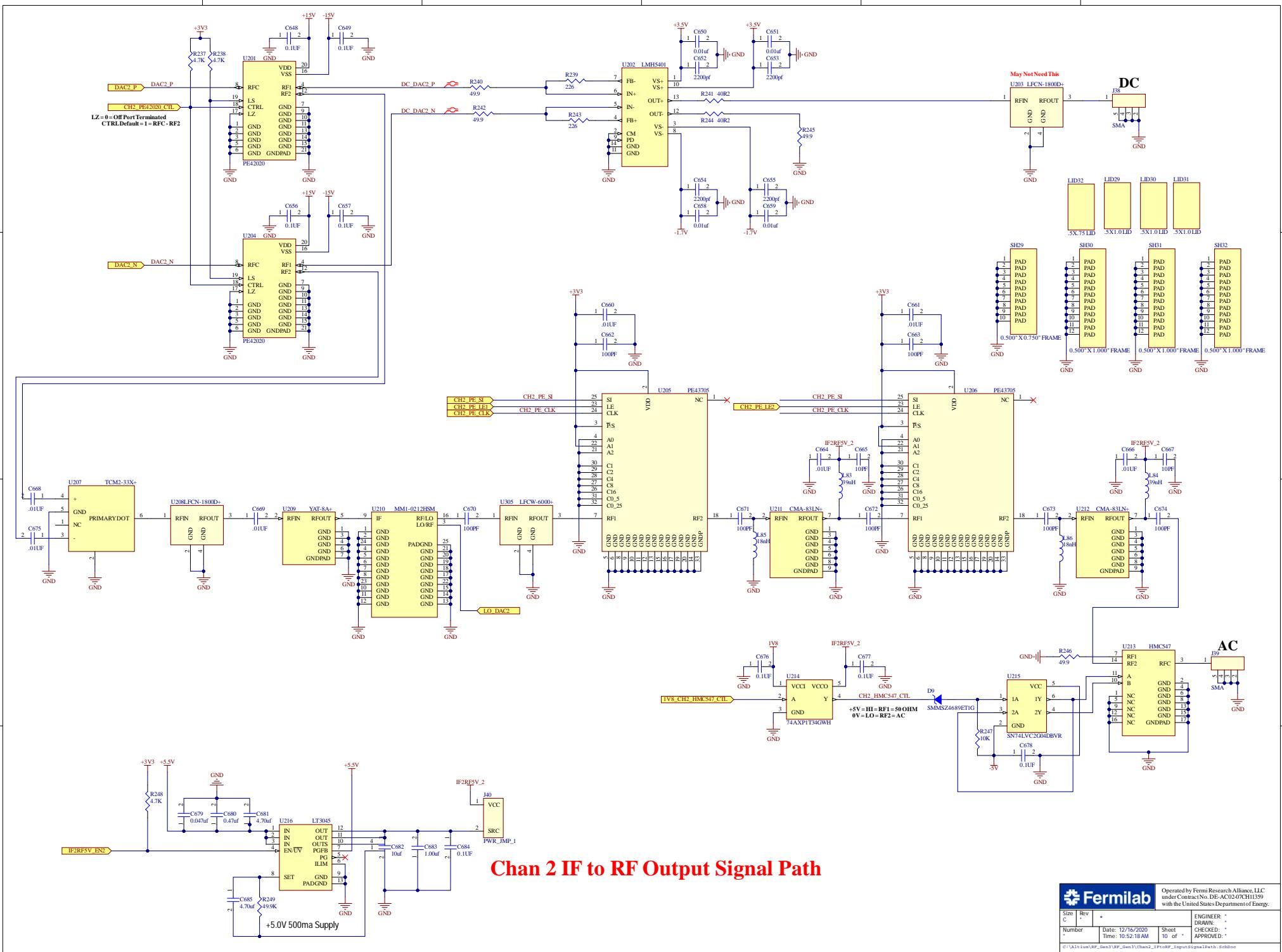
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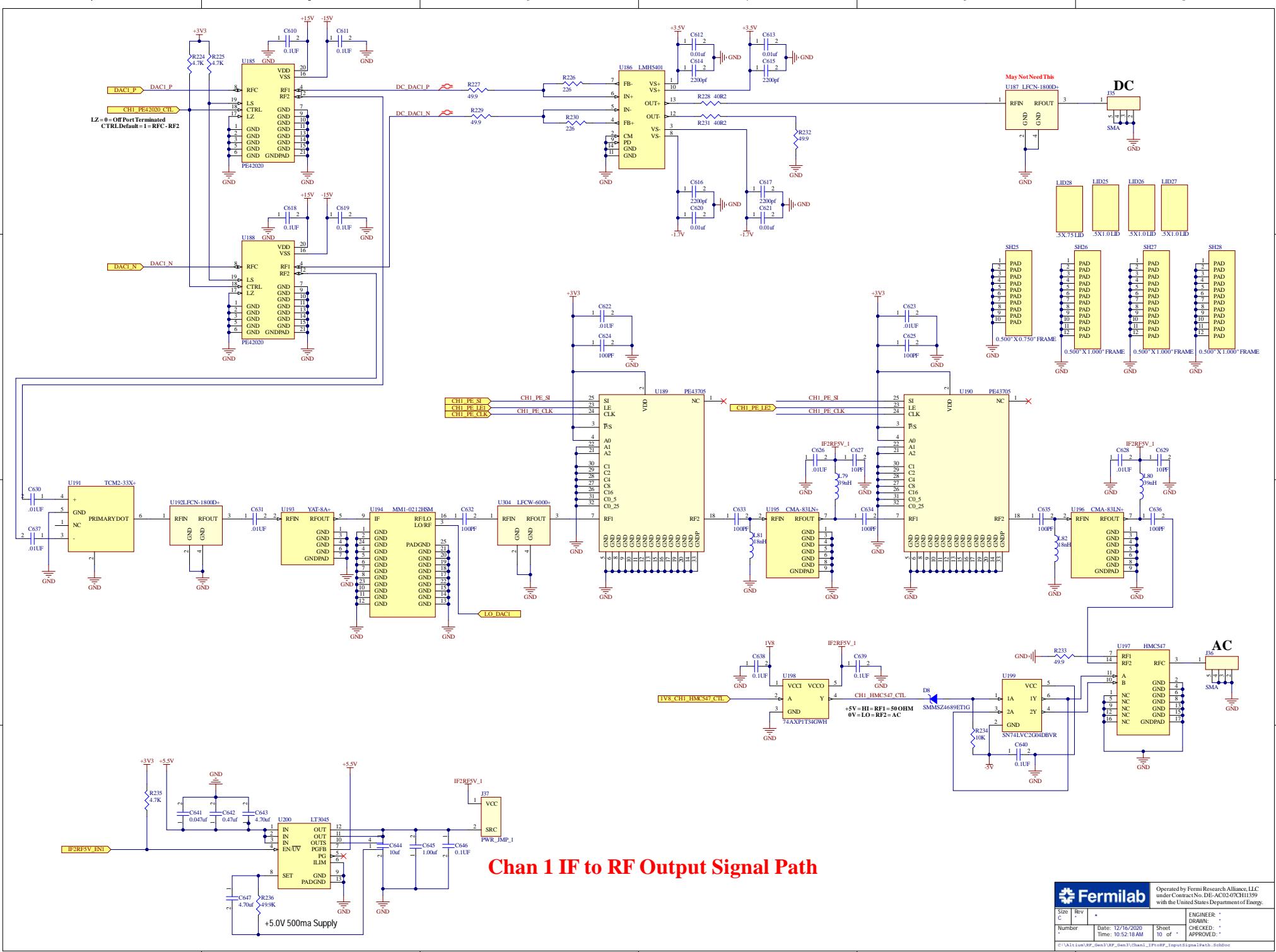


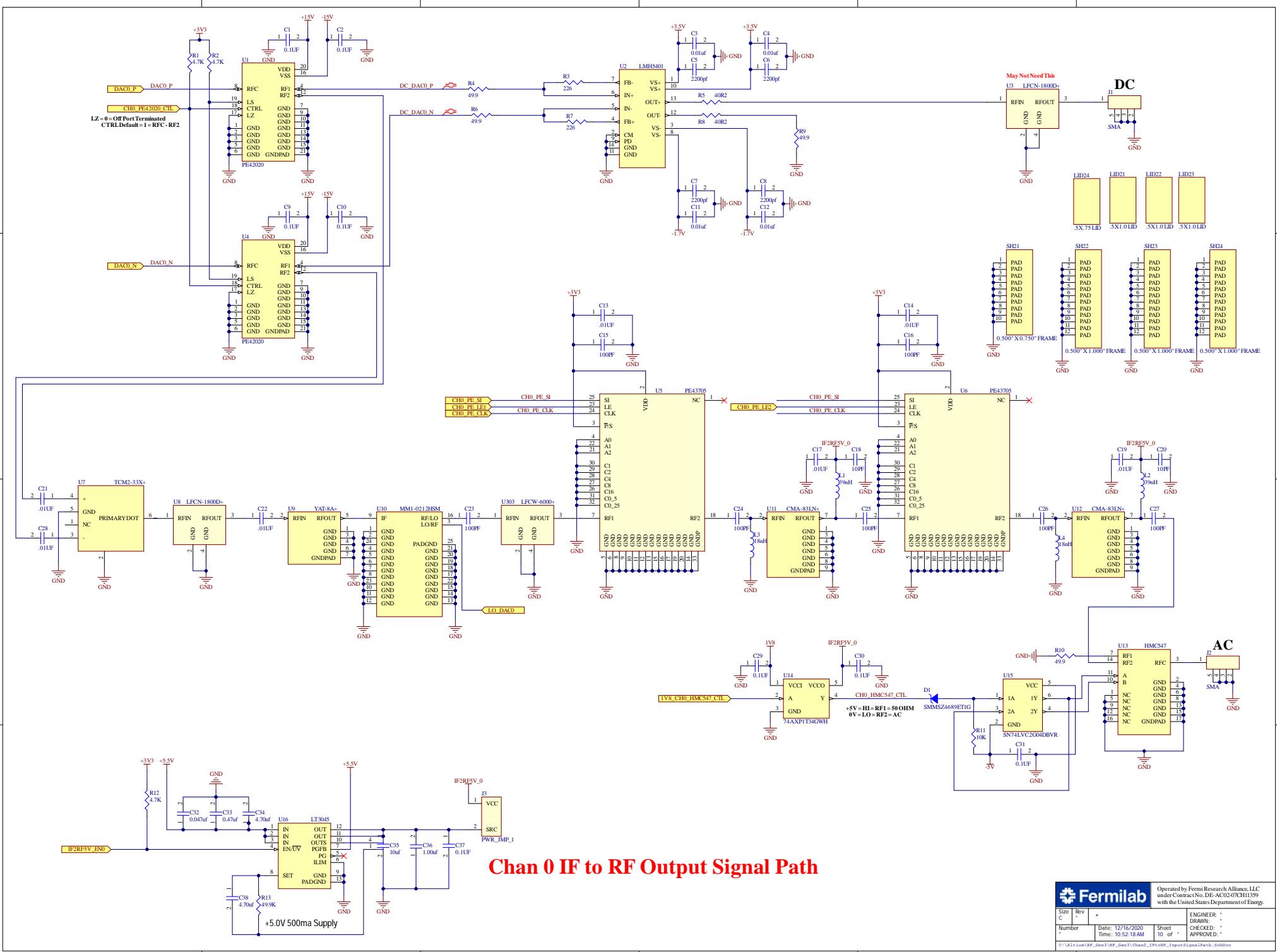
## Chan 5 IF to RF Output Signal Path

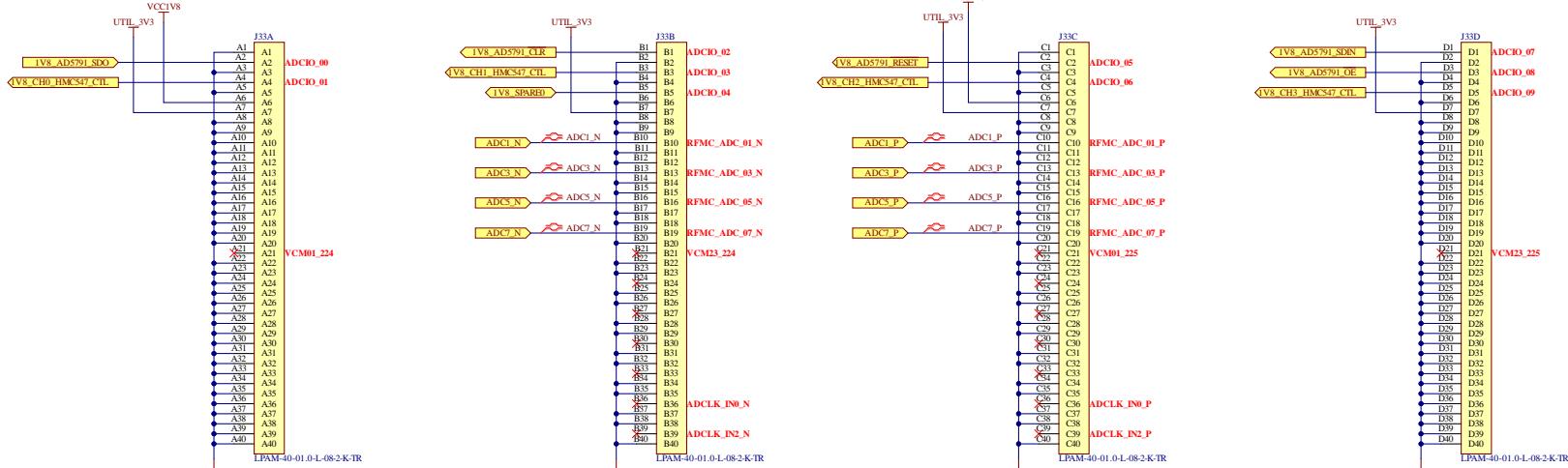




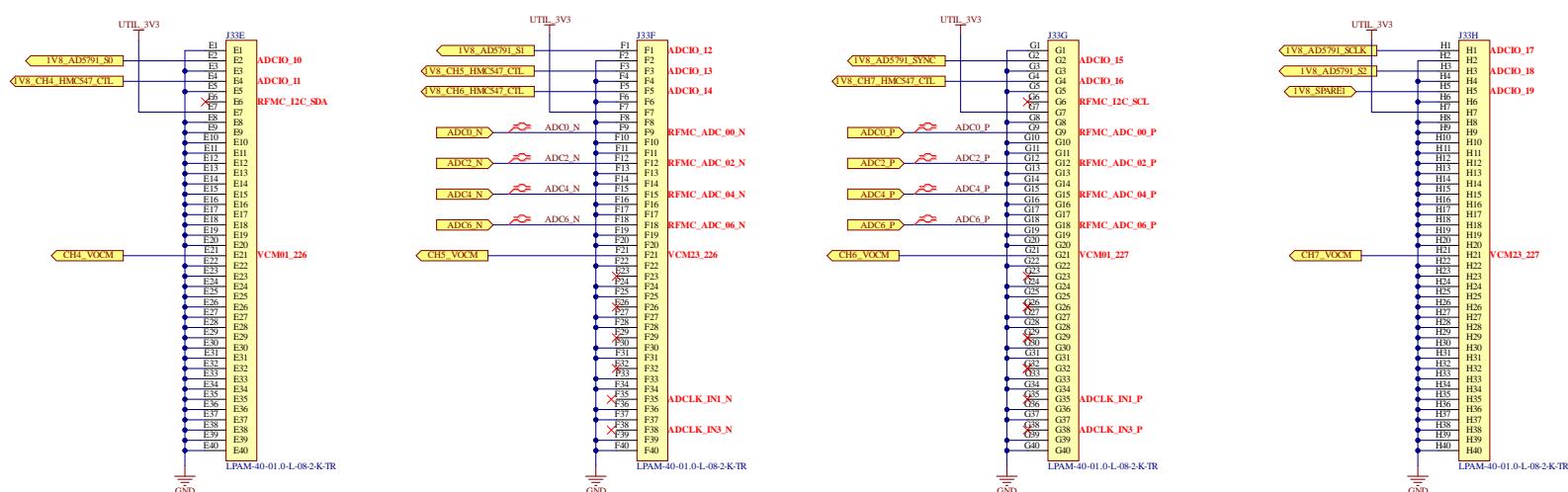


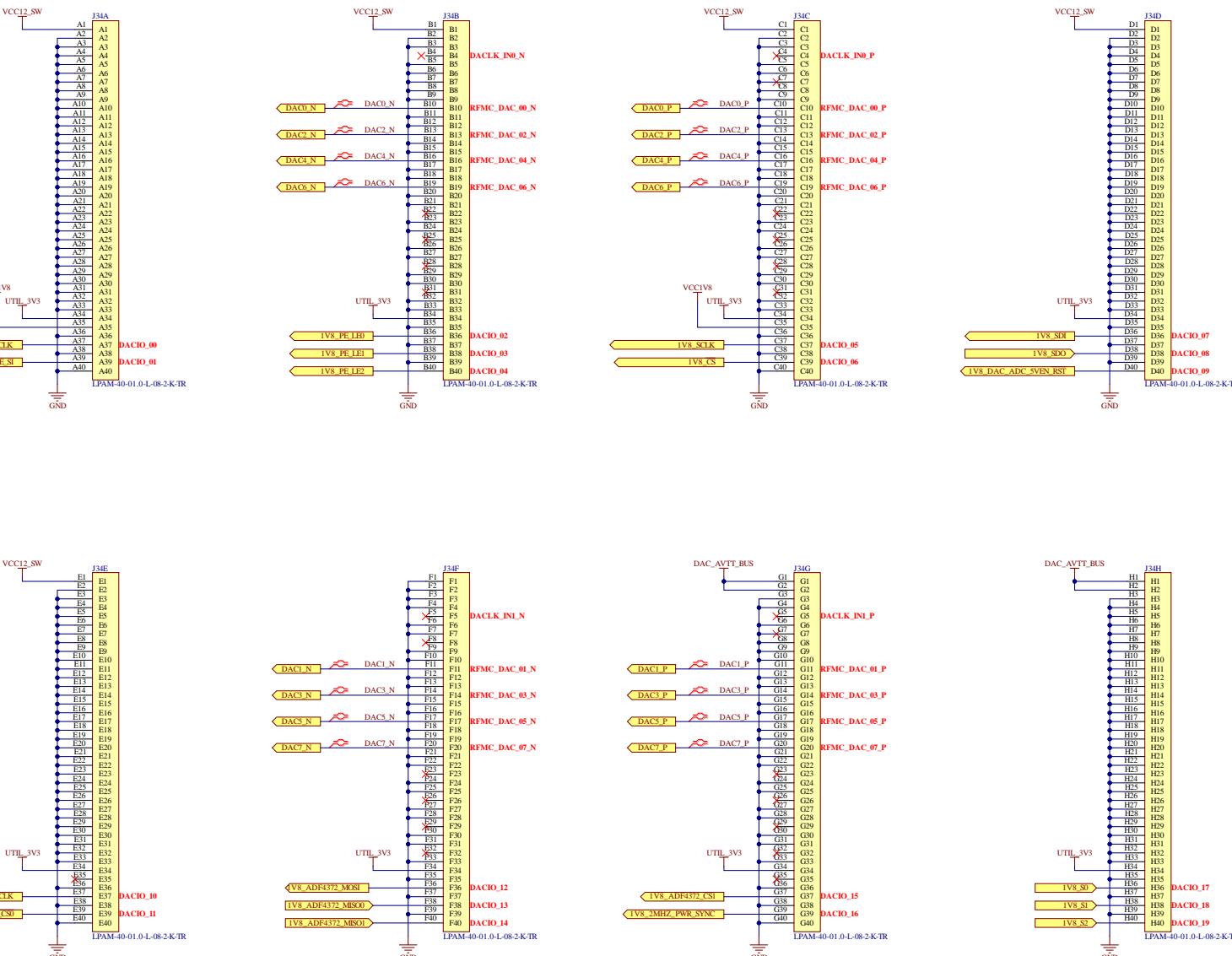


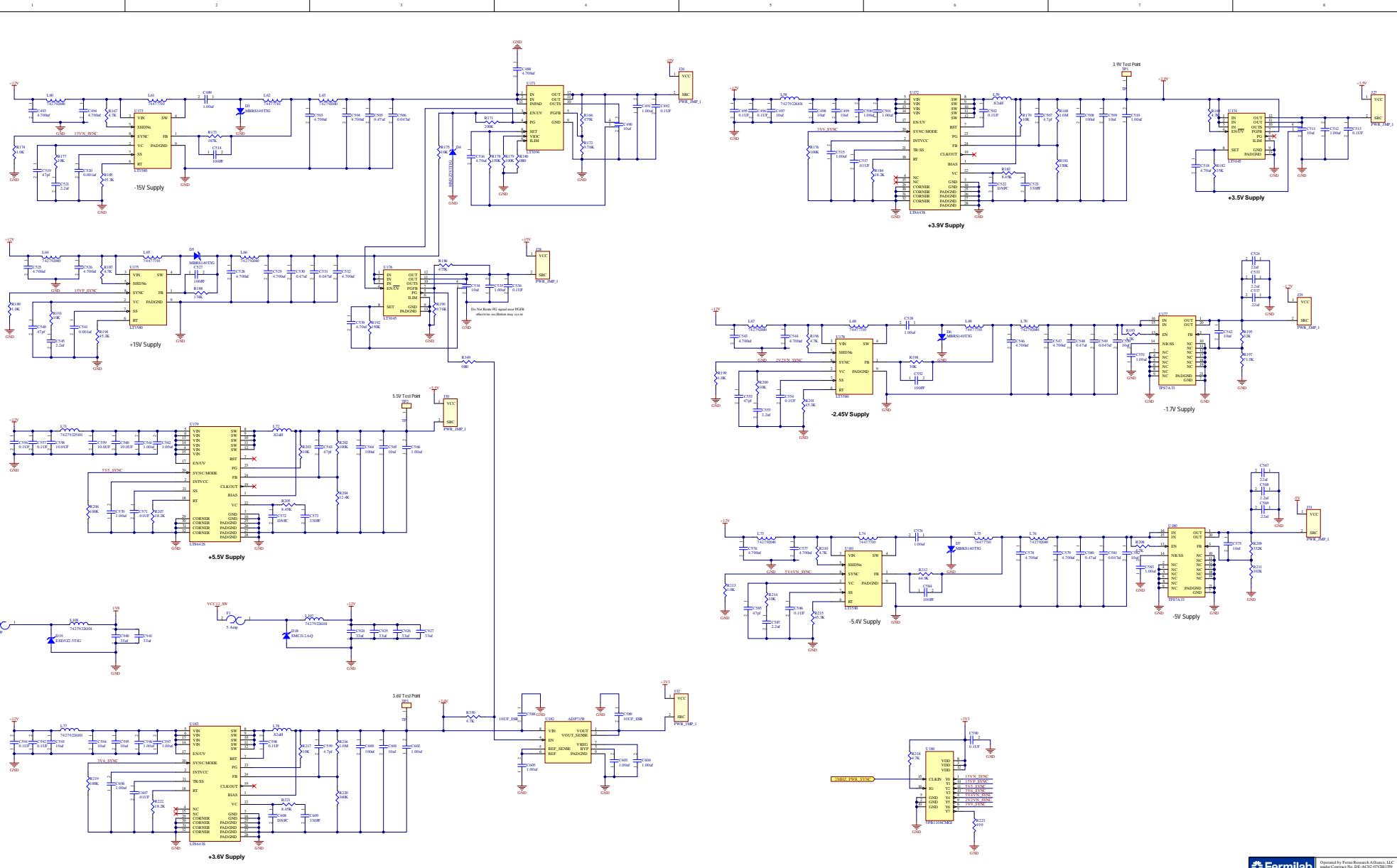


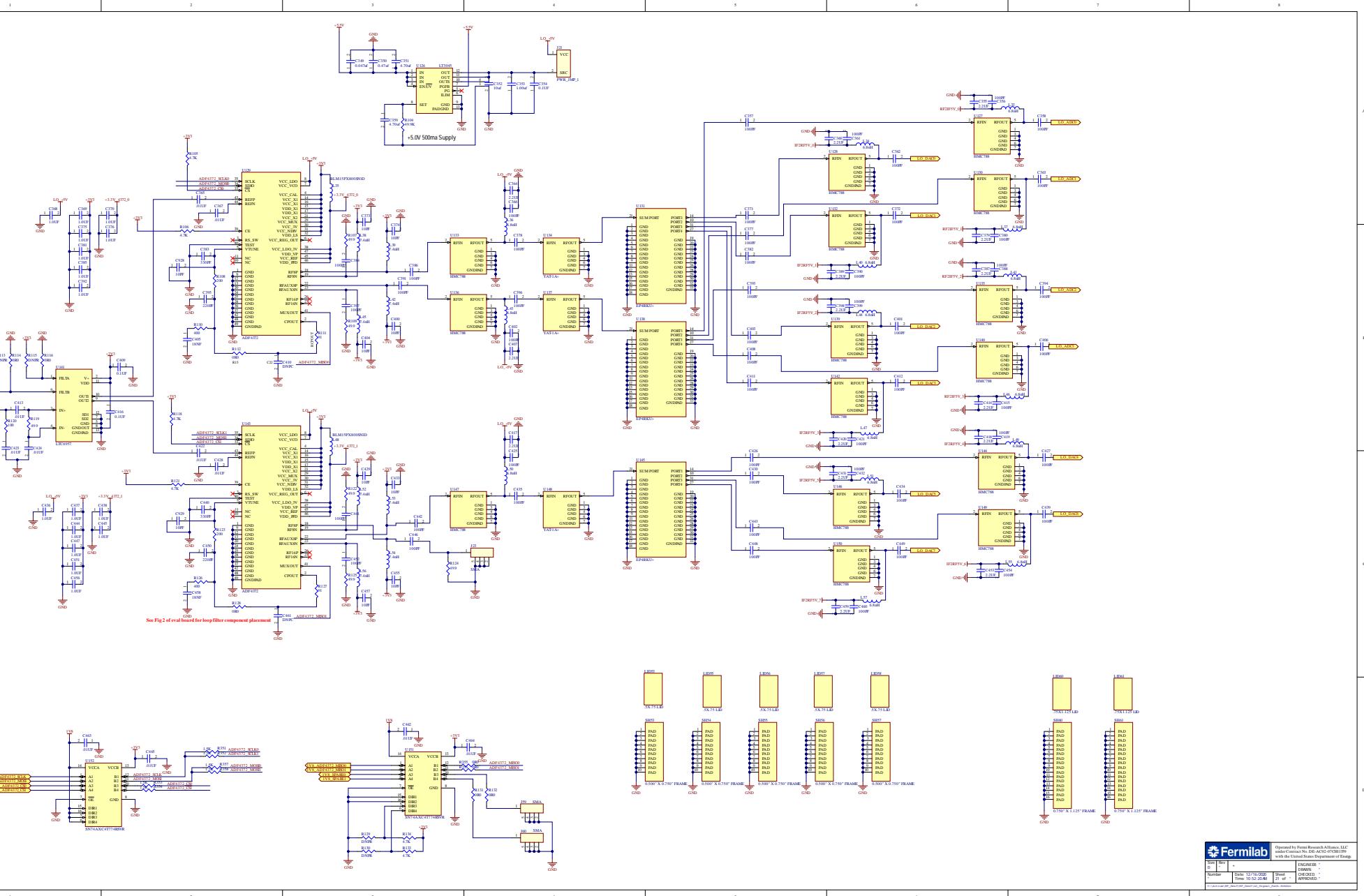


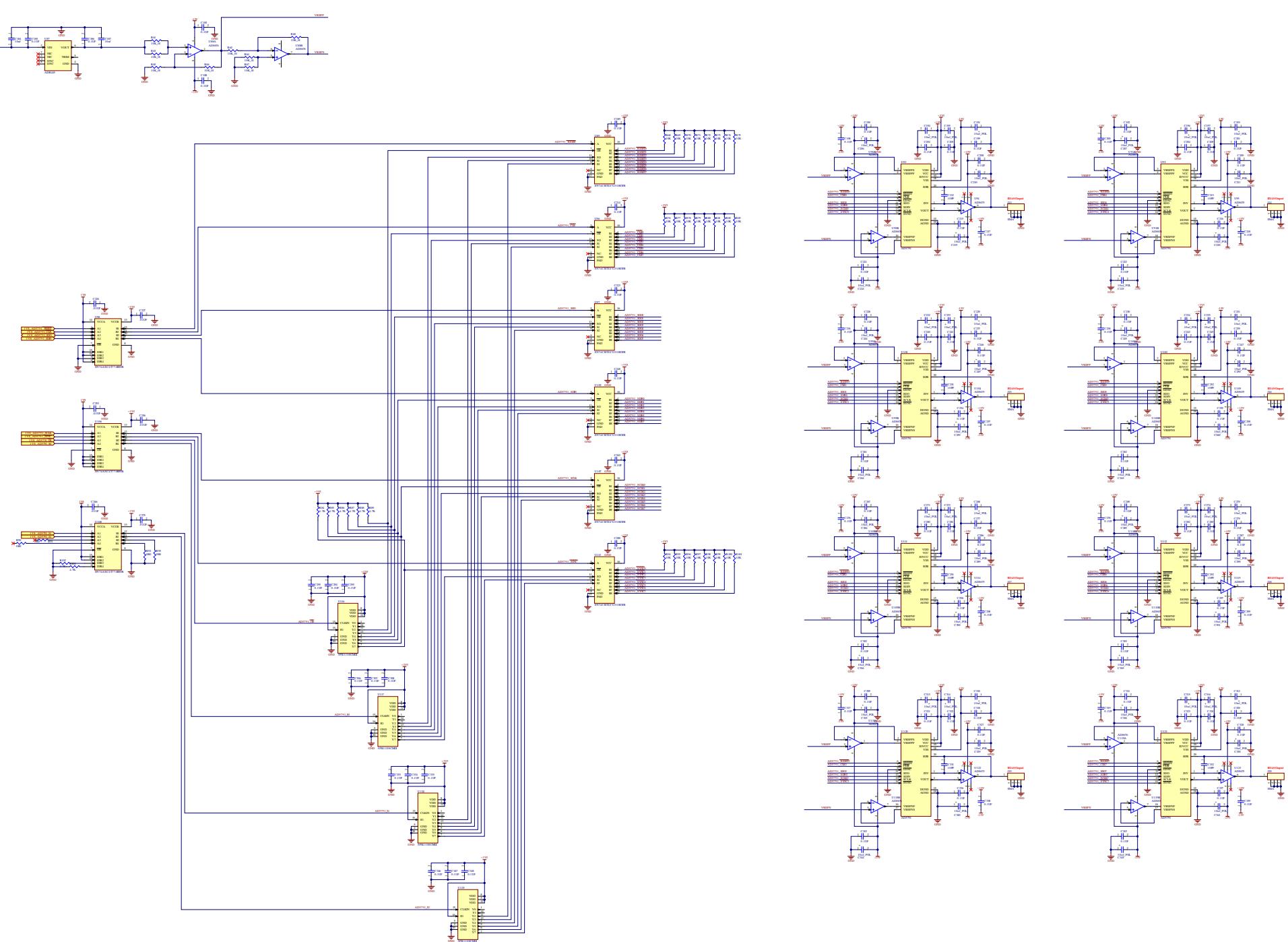
RFMC\_ADC\_04 - VCM01\_220  
RFMC\_ADC\_05 - VCM23\_220  
RFMC\_ADC\_06 - VCM01\_220  
RFMC\_ADC\_07 - VCM23\_220

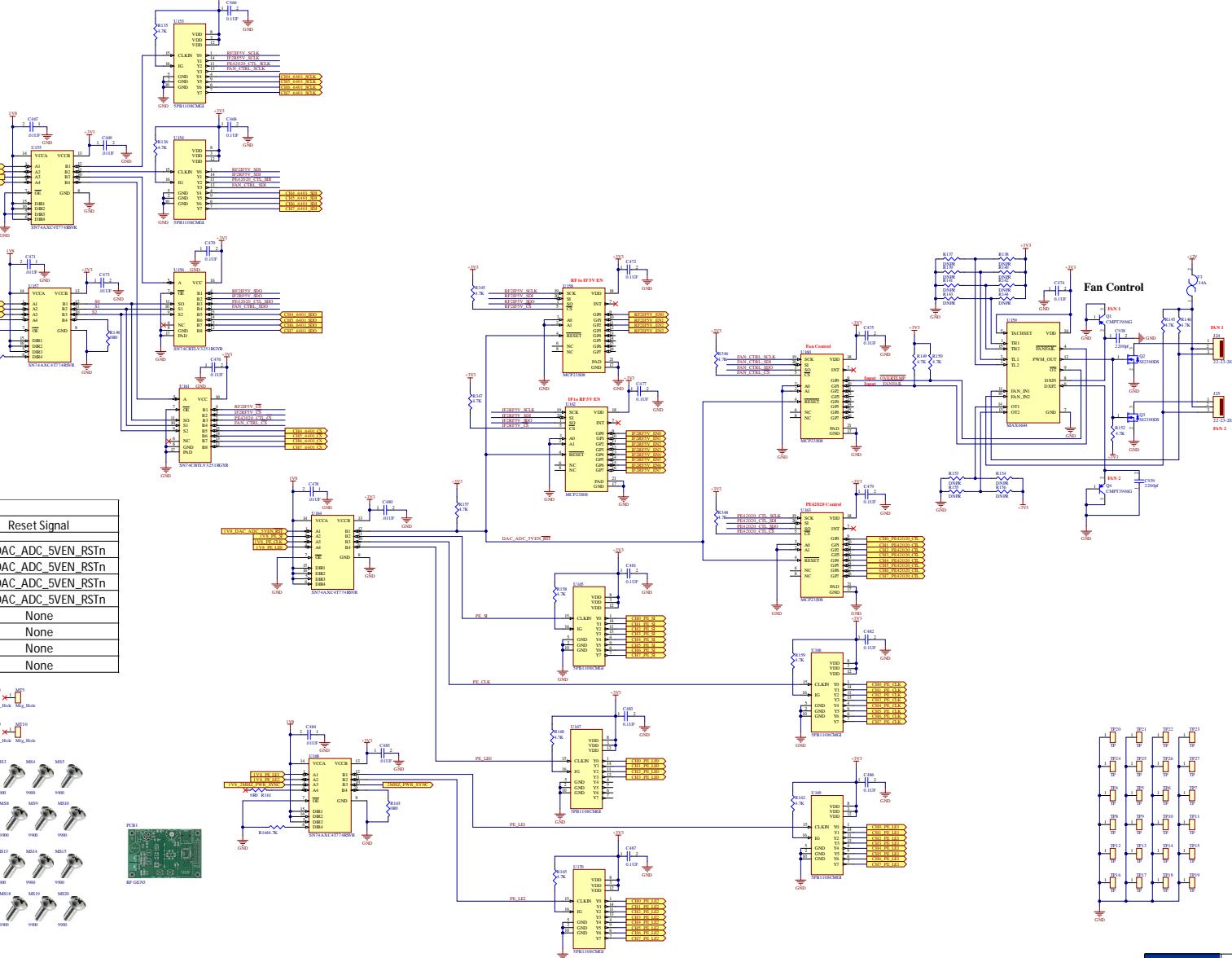












SPI Control		
Select Signals	SPI Device	Reset Signal
SO, S1, S2 = 0	RF2IF5V_EN	1V8_DAC_ADC_5VEN_RSTn
SO, S1, S2 = 1	IF2RF5V_EN	1V8_DAC_ADC_5VEN_RSTn
SO, S1, S2 = 2	PE42020_CTL	1V8_DAC_ADC_5VEN_RSTn
SO, S1, S2 = 3	FAN_CTRL	1V8_DAC_ADC_5VEN_RSTn
SO, S1, S2 = 4	CH4 6401	None
SO, S1, S2 = 5	CH5 6401	None
SO, S1, S2 = 6	CH6 6401	None
SO, S1, S2 = 7	CH7 6401	None

