

root
: Root

system
: System

system_port

membus
: SystemXBar
master slave

cpu1
: TimingSimpleCPU

cpu0
: TimingSimpleCPU

membus_dram
: CommMonitor
master slave

mem_ctrl
: DDR3_1600_8x8
port

dcache_port
dcache
: L1DCache
cpu_side mem_side
cpu_dcach
: CommMonitor
slave master
l1cache_tol2bus
: CommMonitor
slave master

icache_port
icache
: L1ICache
cpu_side mem_side
cpu_icach
: CommMonitor
slave master
l1cache_tol2bus
: CommMonitor
slave master

interrupts
: X86LocalApic
pio int_slave int_master

dcache_port
dcache
: L1DCache
cpu_side mem_side
cpu_dcach
: CommMonitor
slave master
l1cache_tol2bus
: CommMonitor
slave master

icache_port
icache
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cpu_side mem_side
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: CommMonitor
slave master
l1cache_tol2bus
: CommMonitor
slave master

interrupts
: X86LocalApic
int_slave pio int_master

l2cache
: L2Cache
tol2bus_l2cache
: CommMonitor
master slave
l2cache_membus
: CommMonitor
master slave
mem_side
cpu_side

l2bus
: L2XBar
slave master

