



**** 12/22/17 22:25:25 ***** PSpice Lite (March 2016) ***** ID# 10813 ****

** Profile: "SCHEMATIC1-part_b" [C:\Users\Sefa\Documents\PSpice\New Project\Demo\PartB\part_b-pspicefiles\schematic1\part_b.sim]

**** CIRCUIT DESCRIPTION

** Creating circuit file "part_b.cir"

** WARNING: THIS AUTOMATICALLY GENERATED FILE MAY BE OVERWRITTEN BY SUBSEQUENT SIMULATIONS

*Libraries:

* Profile Libraries :

.INC "C:\Users\Sefa\Documents\PSpice\New Project\Demo\PartB\part_b-pspicefiles\schematic1\part_b\part_b_profile.inc"

* Local Libraries :

* From [PSPICE NETLIST] section of C:\Users\Sefa\AppData\Roaming\SPB_Data\cdssetup\OrCAD_PSpice\17.2.0\PSpice.ini file:

**** INCLUDING part_b_profile.inc ****

.LIB ".\tl072.lib"

**** RESUMING part_b.cir ****

.lib "nomd.lib"

*Analysis directives:

.DC LIN V_V1 1.2 1.2 0.05

.WCASE DC V([VO]) MIN VARY DEV LOW

.OPTIONS ADVCONV

.PROBE64 V(alias(*)) I(alias(*)) W(alias(*)) D(alias(*)) NOISE(alias(*))

.INC "..\SCHEMATIC1.net"

**** INCLUDING SCHEMATIC1.net ****

* source PART_B

```
V_V1      N15700 N15800 1.2
R_R4      N15804 N15808 R_R4 1k TC=0,0
.model    R_R4 RES R=1 DEV=25% TC1=0 TC2=0
R_R2      N15736 V0 R_R2 2k TC=0,0
.model    R_R2 RES R=1 DEV=25% TC1=0 TC2=0
R_R1      N15808 N15736 R_R1 2k TC=0,0
.model    R_R1 RES R=1 DEV=25% TC1=0 TC2=0
R_R3      0 N15804 R_R3 1k TC=0,0
.model    R_R3 RES R=1 DEV=25% TC1=0 TC2=0
R_Rg      N15804 N15736 R_Rg 1k TC=0,0
.model    R_Rg RES R=1 DEV=25% TC1=0 TC2=0
X_U1      N15700 N15736 +VCC -VCC V0 TL072
X_U2      N15800 N15804 +VCC -VCC N15808 TL072
V_V2      -VCC 0 -10
V_V3      +VCC 0 10
```

**** RESUMING part_b.cir ****

.END



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**** Diode MODEL PARAMETERS

```
      X_U1.DX      X_U2.DX
IS  800.000000E-18  800.000000E-18
```



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**** Junction FET MODEL PARAMETERS

	X_U1.JX	X_U2.JX
	PJF	PJF
VTO	-1	-1
BETA	270.100000E-06	270.100000E-06
IS	15.000000E-12	15.000000E-12



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**** Resistor MODEL PARAMETERS

R	R_R4	R_R2	R_R1	R_R3
	1	1	1	1
R	R_Rg			
	1			

RUN MINIMUM VALUE

R_R4 R_R4 R -8.4776 at V_V1 = 1.2
 (0 % change per 1% change in Model Parameter)

R_R2 R_R2 R -8.4776 at V_V1 = 1.2
 (0 % change per 1% change in Model Parameter)

R_R1 R_R1 R -8.4776 at V_V1 = 1.2
 (0 % change per 1% change in Model Parameter)

R_R3 R_R3 R -8.4776 at V_V1 = 1.2
 (0 % change per 1% change in Model Parameter)

R_RG R_RG R -8.4776 at V_V1 = 1.2
 (0 % change per 1% change in Model Parameter)

↑
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**** UPDATED MODEL PARAMETERS TEMPERATURE = 27.000 DEG C

WORST CASE ALL DEVICES

Device	MODEL	PARAMETER	NEW VALUE	
R_R4	R_R4	R	1	(Unchanged)
R_R2	R_R2	R	1	(Unchanged)
R_R1	R_R1	R	1	(Unchanged)
R_R3	R_R3	R	1	(Unchanged)
R_Rg	R_Rg	R	1	(Unchanged)

↑
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**** SORTED DEVIATIONS OF V(V0) TEMPERATURE = 27.000 DEG C

WORST CASE SUMMARY

RUN MINIMUM VALUE

WORST CASE ALL DEVICES

-8.4776 at V_V1 = 1.2
(100 % of Nominal)

JOB CONCLUDED



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**** JOB STATISTICS SUMMARY

Total job time (using Solver 1) = .09

