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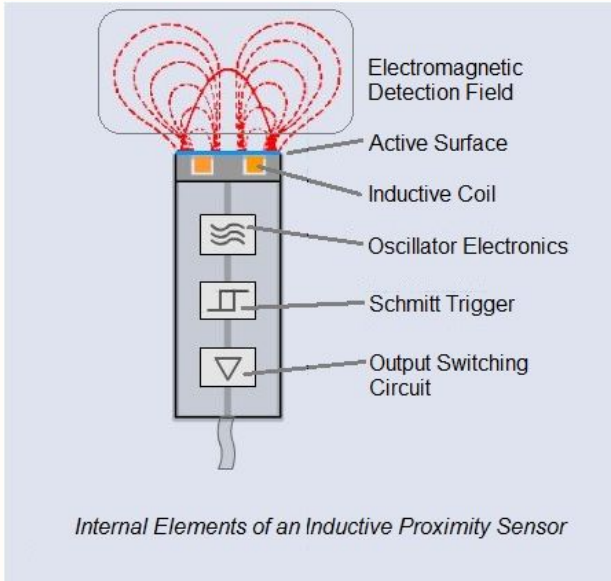
REVISION		
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CONTACT

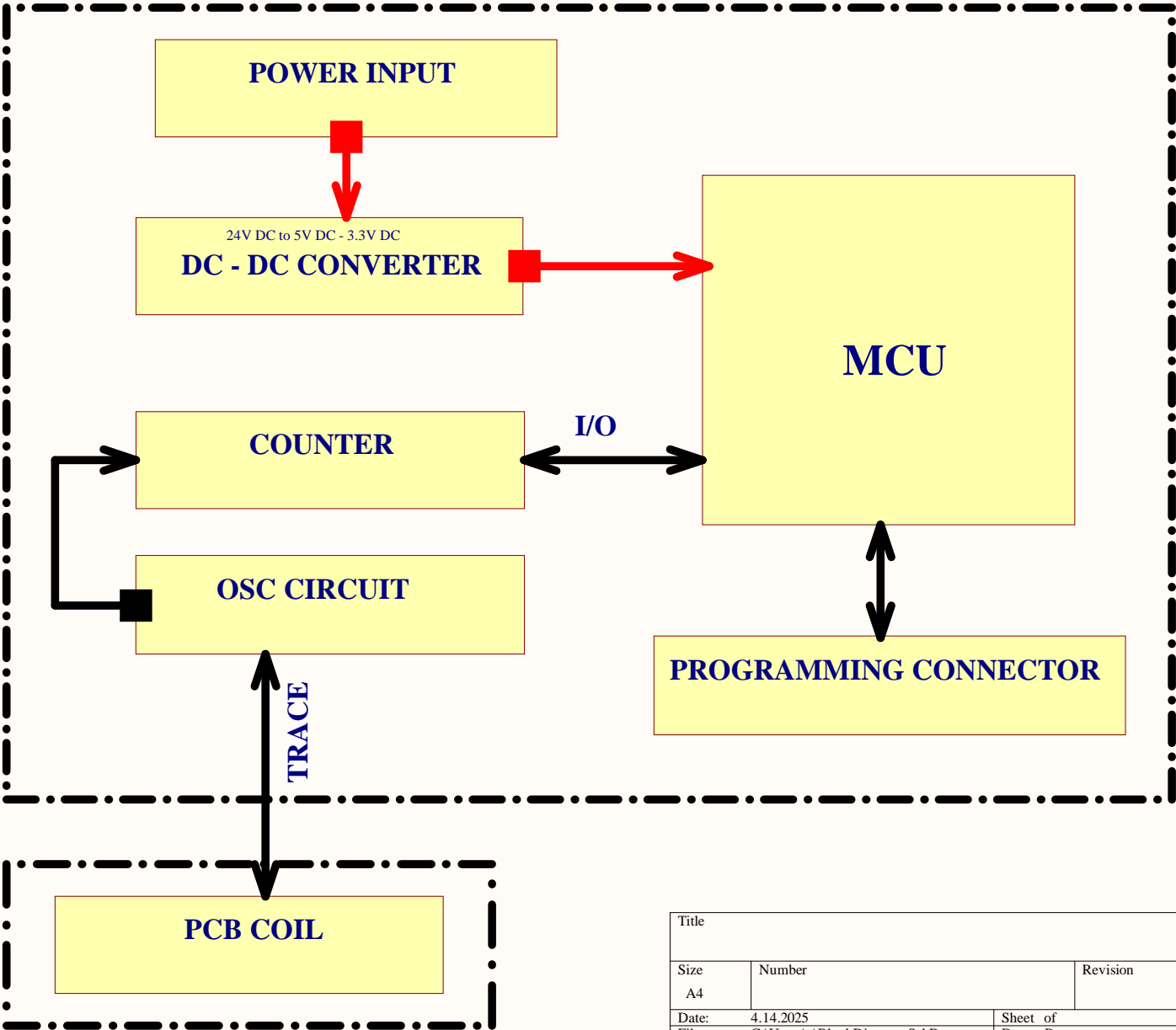
SEFA KARA
sefa.kara@konsens.com.tr
505 080 63 46

Company Name *			
PROJECT DC-DC Buck Conv.PrjPcb		FILE NAME Cover.SchDoc	
Engineer	*	Rel Date	*/
Checked By	*	Change Date	*/
Approved By	*	Rev Date	*/
Department		Revision	* Sheet 1 of 5



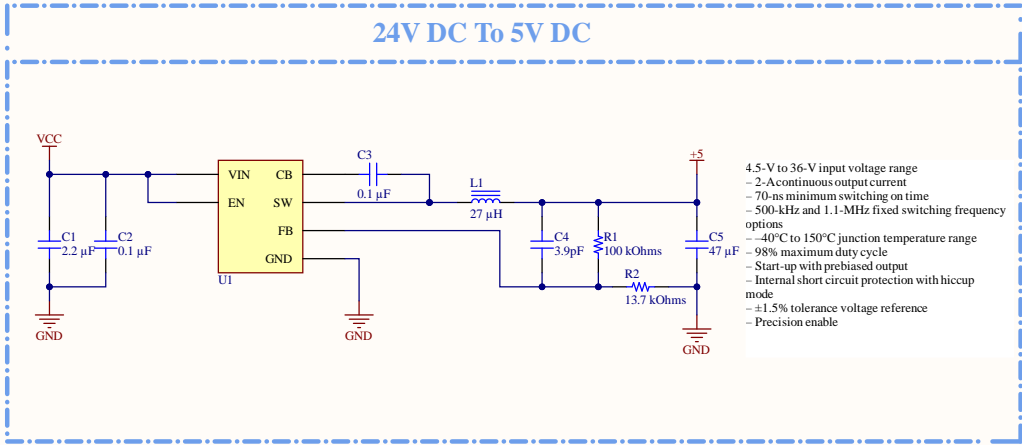
Type @ to refer to a designator or a net

BLOCK DIAGRAM

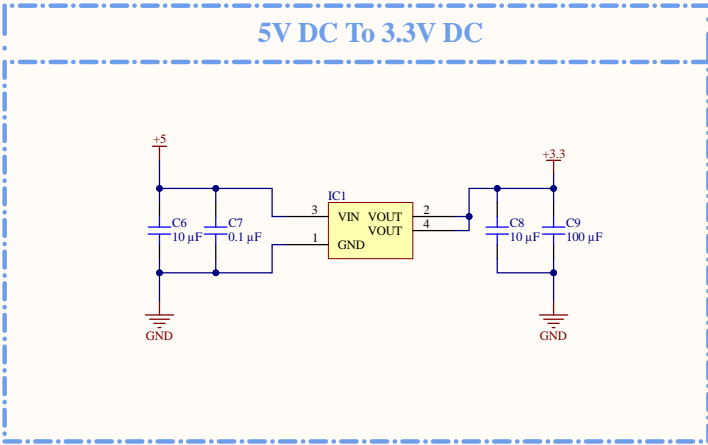


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File:	C:\Users\...\BlockDiagram.SchDoc	Drawn By:

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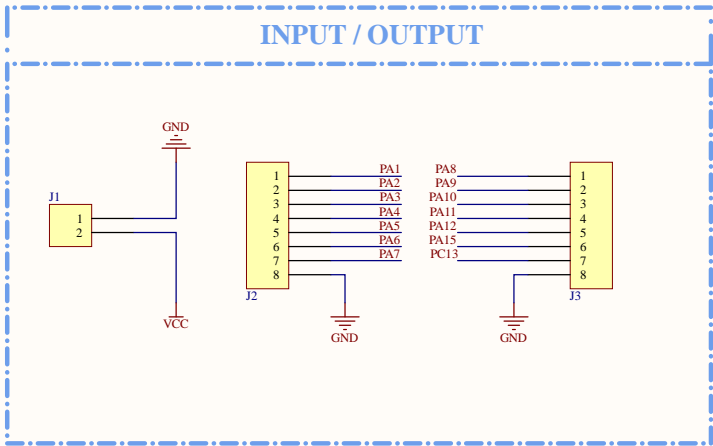


A



B

B



C

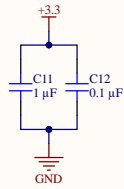
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D

D

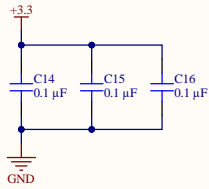
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PROJECT		FILE NAME	
DC-DC Buck Conv.PrjPcb		Power.SchDoc	
Engineer	*	Rel Date	*/
Checked By	*	Change Date	*/
Approved By	*	Rev Date	*/
Department		Revision	* Sheet 3 of 5

DECOUPLING CAP



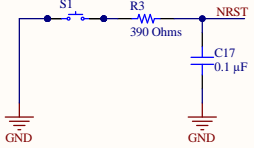
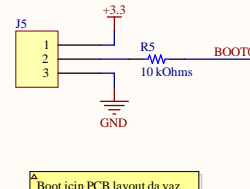
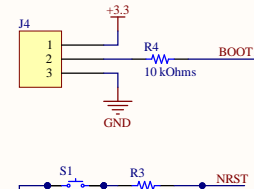
Decoupling for VDDA

If no external battery is used in the application, it is highly recommended to connect VBAT externally to VDD.



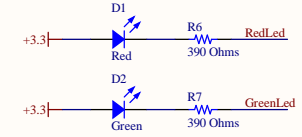
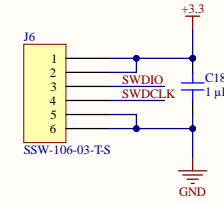
Decoupling capacitor for each VDD pin.

BOOT AND RESET

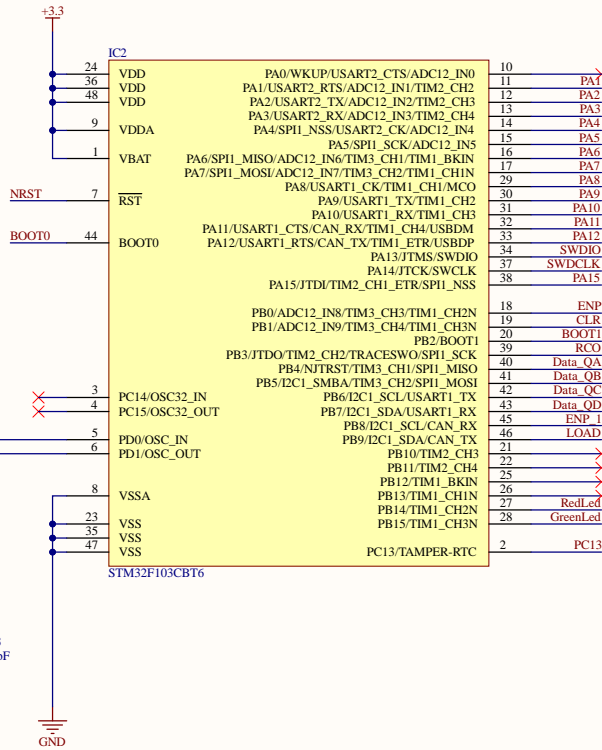


Boot için PCB layout da yaz Buraya doğruluk tablosun ekle

LED AND SWD



MCU



Company Name			
* PROJECT			
DC-DC Buck Conv.PrjPcb		FILE NAME	
		Mcu.SchDoc	
Engineer	*	Rel Date	*/
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Department		Revision	* Sheet 4 of 5

A

B

C

D

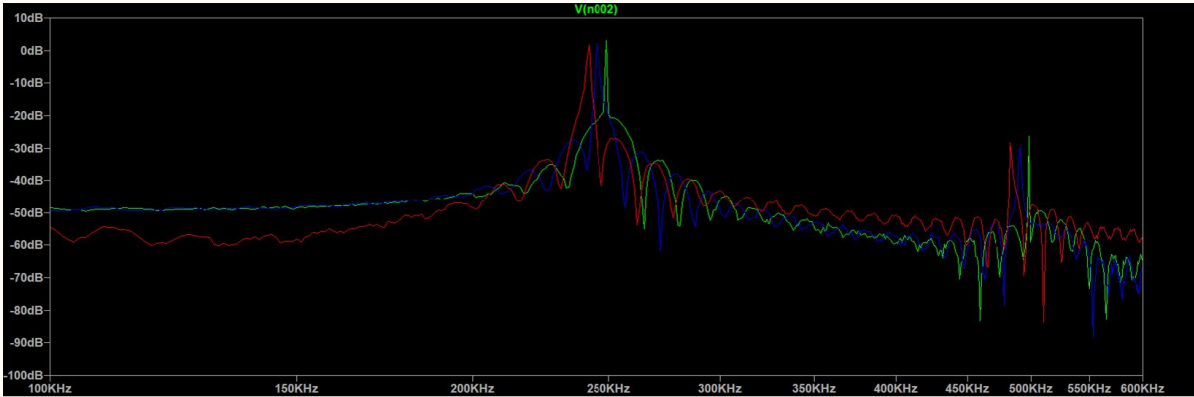
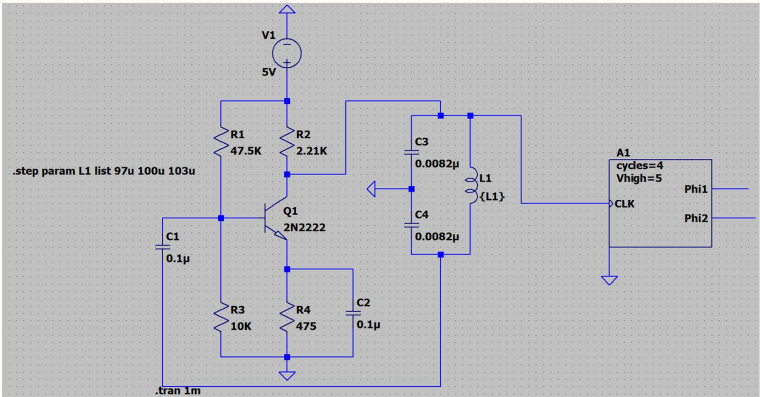
A

B

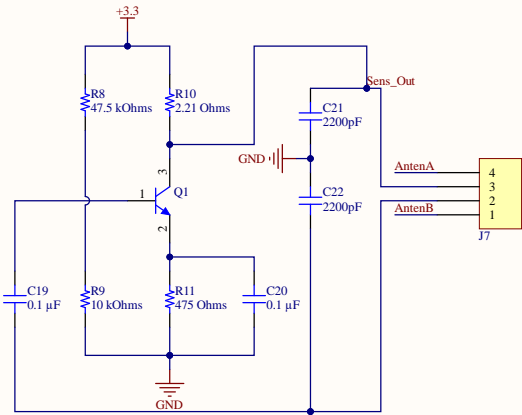
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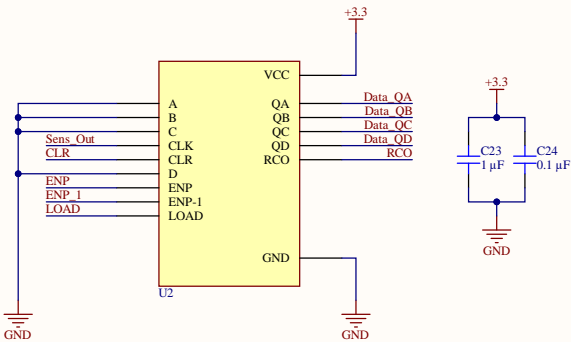
LTSPICE OUTPUT



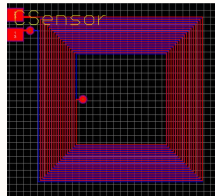
OSC CIRCUIT



COUNTER



COIL



Company Name			
PROJECT		FILE NAME	
DC-DC Buck Conv.PrjPcb		Measurement.SchDoc	
Engineer	*	Rel Date	10
Checked By	*	Change Date	10
Approved By	*	Rev Date	10
Department		Revision	* Sheet 5 of 5

