Guru Nanak Dev Engineering College, Ludhiana

Format for B.Tech. Major/ Minor Project Synopsis

Title page: same as given on page 4

1. Proposed Topic
2. Branch
3. Submitted by
4. Name of Student
5. University Roll No.
6. Class Roll No.
7. Batch

Table of contents:

Introduction (1 page) [Section]

The introduction part will include the brief introduction about the project to be developed, technology used, field of project (if specialized one), any special technical terms about the project.

Rationale: Justification, why needed?(1-2 paragraph)

Objectives (3-4 points)

Feasibility Study (should not exceed 1 page)

This will describe the very first step of software engineering i.e. feasibility study of the project that include the feasibility, need and significance of the project

Methodology/ Planning of work (should not exceed 1 page)

Research type, unit, methods, tools of data collection / analysis. Methodology will include the steps to be followed to achieve the objective of the project during the project development.

Facilities required for proposed work (1 paragraph)

Software/Hardware required for the development of the project.

References (IEEE format)

Here specify the description of the study material referred for the development of project.

SPECIFICATIONS FOR SYNOPSIS

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Note: Submission of Project Synopsis should be on github.
      Fork your Guide account and upload your file in the corresponding folder.
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1. The synopsis shall be computer typed in LaTex [use editors LyX] (English- British, Font -Times Roman, Size-12 point) and printed on A4 size paper.

2. The Synopsis shall be typed on one side only with double space with a margin 3.5 cm on the left, 2.5 cm on the top, and 1.25 cm on the right and at bottom. In Lyx editor click Document menu-> settings-> page margins and set Top: 2.5 cm; Bottom: 1.25 cm; Inner: 3.5 cm ; Outer: 1.25 cm and Line Spacing-> Double.

3. In the synopsis, the title page [Refer sample sheet (inner cover)] should be given first. This should be followed by index, notations/nomenclature.

4. The diagrams should be printed on a light/white background, Tabular matter should be clearly arranged. Decimal point may be indicated by full stop(.) The caption for Figure must be given at the BOTTOM of the Fig. and Caption for the Table must be given at the TOP of the Table.

5. Use *\thispagestyle{empty}* to avoid paging on First Page and use *\setcounter{page}{1}* to start paging from 3 page.

Following are the commands of LaTex to be used on Title page:

1. PROJECT TITLE (Select option Title)
2. MAJOR PROJECT SYNOPSIS (Select option Author)
3. BACHELOR OF TECHNOLOGY (Select option Standard; In paragraph settings Align: Center and Bold)
4. Information Technology (Select option Standard; In paragraph settings Align: Center)
5. SUBMITTED BY (Select Standard; In paragraph settings Align: Center)
6. VIJAY PRATAP SINGH (Select Standard; In paragraph settings Align: Center)
7. University Roll no. 1610776 (Select Standard; In paragraph settings Align: Center)
8. Class Roll no. 1510667 (Select Standard; In paragraph settings Align: Center)
9. August 2008 (Select Standard; In paragraph settings Align: Center)
10. Logo (Insert-> Float-> Figure; In paragraph settings Align: Center)
11. GURU NANAK DEV ENGINEERING COLLEGE, LUDHIANA (Select Standard; In paragraph settings Align: Center)
12. And at the end *(add LaTex command i.e \thispagestyle{empty} it avoids the paging on the Title Page.)*

PROJECT TITLE

MAJOR/Minor PROJECT SYNOPSIS

BACHELOR OF TECHNOLOGY

Information Technology

SUBMITTED BY

VIJAY PRATAP SINGH

University Roll no. 1610776

Class Roll no. 1510667

August 2008

GURU NANAK DEV ENGINEERING COLLEGE

LUDHIANA-141006, INDIA

[In the Insert option select List/TOC and click Table of Contents. Note: In TOC content are only fetched if the Chapters/ Topics names are choosen as Section.]

Contents

1 Introduction [Section command is used]

The introduction part will be of 1 Page include the brief introduction about the project to be developed, technology used, field of project (if specialized one), any special technical terms about the project.

Rationale: Justification, why needed?(1-2 paragraph)

[Use Latex Command To start Paging \setcounter{page}{1}]

2 Objectives [Section command is used]

Include 3-4 points of Objectives. [For numbering use Enumerate]

1. First Objective.
2. Second Objective.
3. Third Objective.
4. Fourth Objective.

3 Feasibility Study [Section command is used]

This should not exceed 1 page it describe the very first step of software engineering i.e. feasibility study of the project that include the feasibility, need and significance of the project

4 Methodology/ Planning of work [Section command is used]

It should not exceed 1 page. Research type, unit, methods, tools of data collection / analysis. Methodology will include the steps to be followed to achieve the objective of the project during the project development. See Figure 1 it shows the bitnami installation first step. [Click to see how to refer and label an image]

1. To add image Click Insert ->Float -> Figure. After adding block of figure again click insert -> Graphics attach the picture here.
2. To refer image in the paragraph place the cursor in the front of image name i.e Bitnami Image. Click on menu-bar Insert-> Label its shows automatic name click Ok then. Now add Reference click ->insert->Cross reference.Choose the label name to apply.]

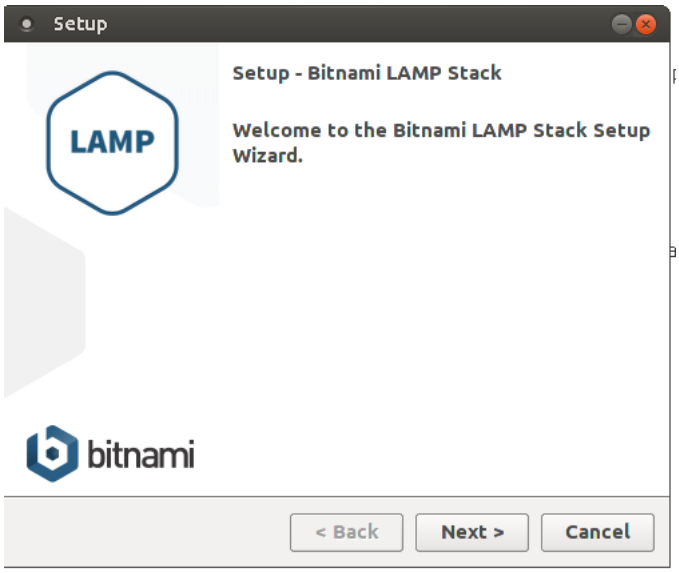

Figure 1: Bitnami Image

5 Facilities required for proposed work [Section command is used]

Software/Hardware required for the development of the project. It includes 1 paragraph.

6 References [Section command is used]

[Use Description for the following References]

[1] M. Shell. (2007) IEEEtran webpage on CTAN. [Online]. Available: http://www.ctan.org/tex-archive/macros/latex/IEEEtran/

[2] Y. Okada, K. Dejima, and T. Ohishi, "Analysis and comparison of PM synchronous motor and induction motor type magnetic bearings," IEEE Trans. Ind. Appl., vol. 31, pp. 1047–1053, Sep./Oct. 1995.

[3] S. Zhang, C. Zhu, J. K. O. Sin, and P. K. T. Mok, "A novel ultrathin elevated channel low-temperature poly-Si TFT," IEEE Electron Device Lett., vol. 20, pp. 569–571, Nov. 1999.