SeHAS
Secure Hardware, Architectures, and Software

January 21, 2025
HiPEAC Conference
Palau de Congressos
Barcelona, Spain

CALL FOR PAPERS

Join us for the *7th International Workshop on Secure Hardware, Architectures, and Software (SeHAS)* as we explore the forefront of security research and innovation. We invite leading researchers, practitioners, and administrators from security and related disciplines to contribute their expertise and insights.

SeHAS provides a dynamic platform for exchanging perspectives, sharing lessons learned, and gaining new insights into securing hardware, architectures, and software.

PAPER SUBMISSION

Paper Submission Guidelines for submitting original research articles

- ❑ **Length:** Up to 4 pages in an IEEE double-column, single-blind, 10-pt.
- ❑ **Review process:** The review is processed by IEEE Embedded Systems Letters (ESL).
- ❑ **Publication:** Accepted articles will be published in IEEE ESL.
- ❑ **Presentation:** Authors of accepted papers will have the opportunity to present their work at SeHAS@HiPEAC 2025 in Barcelona.
- ❑ **Registration:** One registration per presenter at the HiPEAC conference is mandatory.
- ❑ **Submission details:** https://sehas-workshop.github.io/
- ❑ Select the check-box in Step 6 in the ESL submission portal indicating that you would like your paper to be considered for presentation at a conference/workshop that has a tie-up with ESL.

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TOPIC AREAS

SeHAS 2025 invites original contributions in all areas of overlap between hardware and software security, including but not limited to:

- ❑ Hardware security
- ❑ EDA for security
- ❑ Architecture-level security
- ❑ System security
- ❑ Software security
- ❑ Embedded security
- ❑ Security measurement studies
- ❑ Emerging technologies and security
- ❑ Machine learning for security
- ❑ Security automation

SUBMISSION and DEADLINE

September 15, 2024
Manuscript submission

ESL reviews are typically done within 4 weeks
Notification