

CALL FOR PAPERS

Join us for the *8th International Workshop on Secure Hardware, Architectures, and Software (SeHAS)* as we explore the forefront of security research and innovation. We invite leading researchers, practitioners, and administrators from security and related disciplines to contribute their expertise and insights.

SeHAS provides a dynamic platform for exchanging perspectives, sharing lessons learned, and gaining new insights into securing hardware, architectures, and software.

Workshop webpage: <https://sehas-workshop.github.io>



PAPER SUBMISSION

Paper Submission Guidelines for Submitting Original Research Articles

- ☐ **Length:** Up to 4 pages in an IEEE double-column, single-blind, 10-pt.
- ☐ **Review process:** The review is processed by IEEE Embedded Systems Letters (ESL).
- ☐ **Publication:** Accepted articles will be published in IEEE ESL.
- ☐ **Presentation:** Authors of accepted papers will have the opportunity to present their work at SeHAS@HiPEAC 2026 in Kraków, Poland.
- ☐ **Registration:** One registration per presenter at the HiPEAC conference is mandatory.
- ☐ **Submission link:** <https://ieee.atyponrex.com/journal/les-ieee>
- ☐ **Article type:** Select **SeHAS** as the **Article Type** in the first step of the submission process. **Only submissions with this selection will be considered for the workshop.**

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TOPIC AREAS

SeHAS 2026 invites original contributions in all areas of overlap between hardware and software security, including but not limited to:

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| <input type="checkbox"/> Hardware security | <input type="checkbox"/> Embedded security |
| <input type="checkbox"/> EDA for security | <input type="checkbox"/> Security measurement studies |
| <input type="checkbox"/> Architecture-level security | <input type="checkbox"/> Emerging technologies and security |
| <input type="checkbox"/> System security | <input type="checkbox"/> Machine learning for security |
| <input type="checkbox"/> Security of LLMs | <input type="checkbox"/> LLMs for security |
| <input type="checkbox"/> FPGA security | <input type="checkbox"/> Malware detection & prevention |

SUBMISSION and DEADLINE

September 22, 2025 (extended)

Manuscript submission

Notification

ESL reviews are typically done within 4 weeks