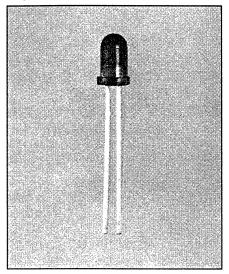
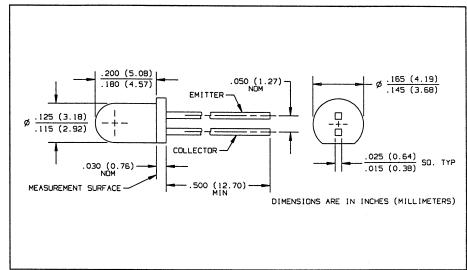


# Infrared Selected NPN Silicon Phototransistors Types OP505A, OP505B, OP505C, OP505D





#### **Features**

- Narrow receiving angle
- Variety of sensitivity ranges
- T-1 package style
- Small package size for space limited applications

## Description

The OP505 series devices consist of NPN silicon phototransistors molded in blue tinted epoxy packages. The narrow receiving angle provides excellent on-axis coupling. These devices are 100% production tested using infrared light for close correlation with Optek's GaAs and GaAlAs emitters.

### Replaces

K5500 Series

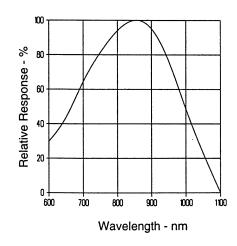
# **Absolute Maximum Ratings** (T<sub>A</sub> = 25° C unless otherwise noted)

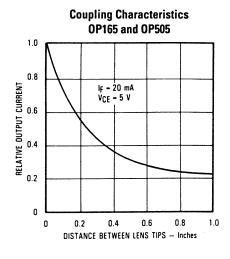
Collector-Emitter Voltage
Emitter-Collector Voltage
Storage and Operating Temperature Range
Lead Soldering Temperature [1/16 inch (1.6 mm) from case for 5 sec. with soldering
iron]
Power Dissipation
Notes:

- (1) RMA flux is recommended. Duration can be extended to 10 sec. max. when flow soldering. Max. 20 grams force may be applied to leads when soldering. (2) Derate linearly 1.33 mW/° C above 25° C.
- (3) Light source is an unfiltered GaAs LED with a peak emission wavelength of 935 nm and a radiometric intensity level which varies less than 10% over the entire lens surface of the phototransistor being tested.
- (4) To calculate typical collector dark current in  $\mu$ A, use the formula I<sub>CED</sub> =  $10^{(0.040 \text{ T}_A 3.4)}$  where TA is ambient temperature in ° C.

## **Typical Performance Curves**

### Typical Spectral Response





Carrollton, Texas 75006

# Types OP505A, OP505B, OP505C, OP505D

Electrical Characteristics (T<sub>A</sub> = 25° C unless otherwise noted)

SYMBOL	PARAMETER		MIN	TYP	MAX	UNITS	TEST CONDITIONS
Ic(on)	On-State Collector Current	OP505D OP505C OP505B OP505A	0.55 1.10 2.15 4.30		3.00 5.95	mA mA	$\begin{split} &V_{\text{CE}} = 5 \text{ V, E}_{\text{e}} = 0.50 \text{ mW/cm}^{2(3)} \\ &V_{\text{CE}} = 5 \text{ V, E}_{\text{e}} = 0.50 \text{ mW/cm}^{2(3)} \\ &V_{\text{CE}} = 5 \text{ V, E}_{\text{e}} = 0.50 \text{ mW/cm}^{2(3)} \\ &V_{\text{CE}} = 5 \text{ V, E}_{\text{e}} = 0.50 \text{ mW/cm}^{2(3)} \end{split}$
ΔΙς/ΔΤ	Relative IC Changes with Temperatu	re		1.00		1	$V_{CE} = 5 \text{ V, E}_{e} = 1.00 \text{ mW/cm}^{2}$ $\lambda = 935 \text{ nm}$
ICEO	Collector Dark Current				100	nA	V <sub>CE</sub> = 10.0 V, E <sub>e</sub> = 0 <sup>(4)</sup>
V <sub>(BR)</sub> CEO	Collector-Emitter Breakdown Voltage	)	30			٧	I <sub>C</sub> = 100 μA
V <sub>(BR)ECO</sub>	Emitter-Collector Breakdown Voltage	)	5.0			V	I <sub>E</sub> = 100 μA
VCE(SAT)	Collector-Emitter Saturation Voltage				0.40	٧	$I_C$ = 250 $\mu$ A, $E_e$ = 0.50 mW/cm <sup>2</sup> $\lambda$ = 935 nm

## **Typical Performance Curves**

