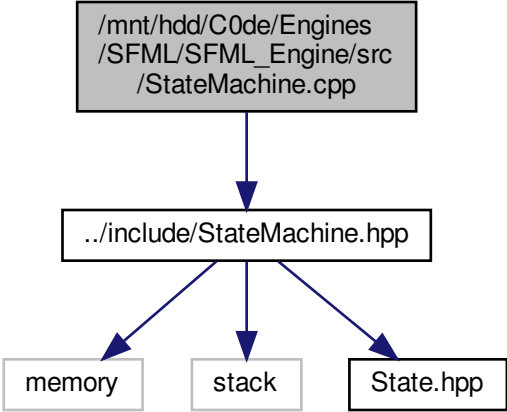


/mnt/hdd/C0de/Engines
/SFML/SFML_Engine/src
/StateMachine.cpp



```
graph TD; A["/mnt/hdd/C0de/Engines  
/SFML/SFML_Engine/src  
/StateMachine.cpp"] --> B["../include/StateMachine.hpp"]; B --> C["memory"]; B --> D["stack"]; B --> E["State.hpp"];
```

../include/StateMachine.hpp

memory

stack

State.hpp