```
rewrite(\alpha) = \alpha
                                                                                                                                                                                                                                                                                             where \alpha is an atomic proposition
                                            rewrite(deadlock) = deadlock
                                                                           rewrite(EG\varphi) = rewrite(\neg AFrewrite(\neg \varphi))
                                                                           rewrite(AG\varphi) = rewrite(\neg EFrewrite(\neg \varphi))
                                                                           rewrite(EX\varphi) = EX rewrite(\varphi)
                                                                        rewrite(AX\varphi) = AXrewrite(\varphi)
rewrite(\varphi_1 \wedge \cdots \wedge \varphi_n) = rewrite(\varphi_1) \wedge \cdots \wedge rewrite(\varphi_n)
                                                                 e(\varphi_{1} \vee \cdots \vee \varphi_{n}) = rewrite(\varphi_{1}) \vee \cdots \vee rewrite(\varphi_{n})
rewrite(\neg \varphi) = \begin{cases} \varphi' & \text{if } rewrite(\varphi) = \neg \varphi' \\ AX rewrite(\neg \varphi') & \text{if } rewrite(\varphi) = EX \varphi' \\ EX rewrite(\neg \varphi') & \text{if } rewrite(\varphi) = AX \varphi' \\ rewrite((\neg \varphi_{1}) \wedge \cdots \wedge (\neg \varphi_{n})) & \text{if } \varphi = \varphi_{1} \vee \cdots \vee \varphi_{n} \\ rewrite((\varphi_{1}) \vee \cdots \vee (\neg \varphi_{n})) & \text{if } \varphi = \varphi_{1} \wedge \cdots \wedge \varphi_{n} \\ \neg rewrite(\varphi) & \text{otherwise} \end{cases}
e(\varphi_{1} \vee \cdots \vee \varphi_{n}) = (\varphi_{1} \vee \cdots \vee \varphi_{n}) = (
rewrite(\varphi_1 \lor \cdots \lor \varphi_n) = rewrite(\varphi_1) \lor \cdots \lor rewrite(\varphi_n)
                 rewrite(A(\varphi_1 U \varphi_2)) = \begin{cases} \neg aeaalock & \text{if } rewrite(\varphi_2) = \neg deadlock \\ rewrite(\varphi_2) & \text{if } rewrite(\varphi_1) = deadlock \\ rewrite(AF \varphi_2) & \text{if } rewrite(\varphi_1) = \neg deadlock \\ EF \varphi_3 & \text{if } rewrite(\varphi_2) = EF \varphi_3 \\ AF \varphi_3 & \text{if } rewrite(\varphi_2) = AF \varphi_3 \\ rewrite((EF \varphi_4) \vee A(\varphi_1 U \varphi_3)) & \text{if } rewrite(\varphi_2) = \varphi_3 \vee EF \varphi_4 \\ A(rewrite(\varphi_1) U rewrite(\varphi_2) & \text{otherwise} \end{cases}
                  rewrite(E(\varphi_1 U \varphi_2)) = \begin{cases} \neg deadlock & \text{if } rewrite(\varphi_2) = \neg deadlock \\ rewrite(\varphi_2) & \text{if } rewrite(\varphi_1) = deadlock \\ rewrite(EF \varphi_2) & \text{if } rewrite(\varphi_1) = \neg deadlock \\ EF \varphi_3 & \text{if } rewrite(\varphi_2) = EF \varphi_3 \\ rewrite((EF \varphi_4) \vee E(\varphi_1 U \varphi_3) & \text{if } rewrite(\varphi_2) = \varphi_3 \vee EF \varphi_4 \end{cases}
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                otherwise
```