

NP-Completeness

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Last Lecture

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 - $x \in L_A \iff f(x) \in L_B$
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 - Sound and complete: $(\exists c. \text{check}(w, c)) \iff w \in L$
 - Example: solution of Boolean formula

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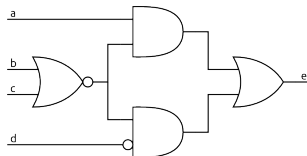
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- NP: problems with poly-time certificates
 - NP-hard: harder than any problem in NP
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- NP: problems with poly-time certificates
 - NP-hard: harder than any problem in NP
 - NP-complete = in NP + NP-hard
- How to show that problem is ...
 - ... in NP: show it has poly-time certificates
 - ... NP-hard: reduce another NP-hard problem to it

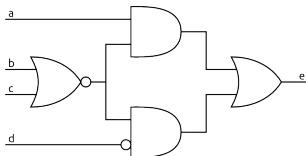
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- Given a combinational circuit with n gates, $m \leq 2n$ inputs, and one output
 - Let's restrict gate types to AND, OR, NOT



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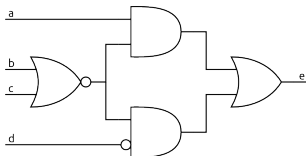
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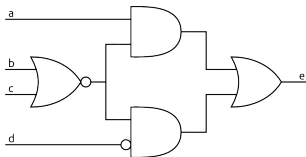
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 - given the inputs, simulate the circuit and check the output
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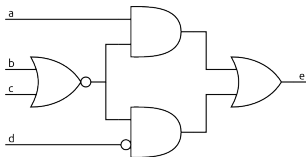
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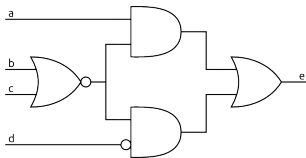
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 - precise proof: lot's of subtle technical details

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- Now: How to construct such a circuit

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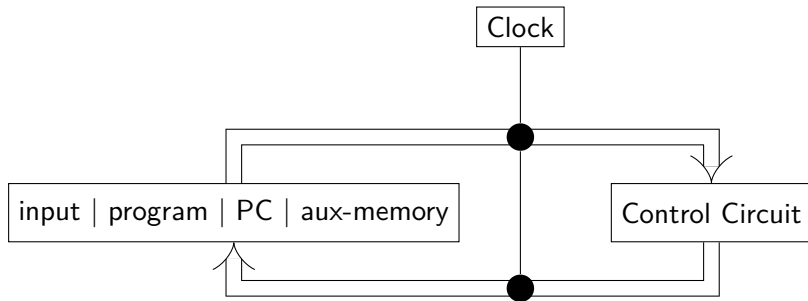
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 - easy enough to be simulated by standard computer
 - in $\text{poly}(n)$ time per cycle!

Our Computer



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- ignoring some outputs
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 - here: keep only the single bit that contains result

Circuit for n cycles

initial memory



Control Circuit (copy 1)



Control Circuit (copy 2)



...



Control Circuit (copy $n - 1$)



memory after n cycles

Outlook

- We have an initial NP-complete problem
- We now reduce it to other problems, to show that they are NP-complete, too!
- $\text{Circuit-SAT} \leq_p \text{SAT} \leq_p \text{3SAT} \leq_p \text{CLIQUE} \dots$

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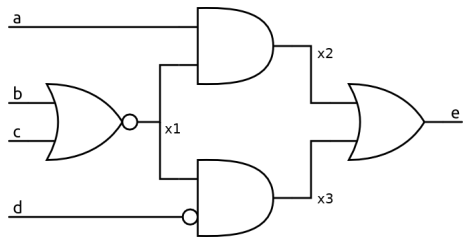
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 - where $x_1 \leftrightarrow x_2$ is short for $(x_1 \wedge x_2) \vee (\neg x_1 \wedge \neg x_2)$
 - can obviously be done in polynomial time
 - constant work per gate and variable!

Circuit-SAT \leq_p SAT



$$\begin{array}{l} \wedge \\ \wedge \\ \wedge \\ \wedge \end{array} \quad \begin{array}{l} e \\ \neg x_1 \leftrightarrow b \vee c \\ x_2 \leftrightarrow a \wedge x_1 \\ x_3 \leftrightarrow \neg d \wedge x_1 \\ e \leftrightarrow x_2 \vee x_3 \end{array}$$

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- Boolean formula in CNF, exactly 3 literals over different variables per clause
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 - one new variable y_i per node t_i .
 - for root node t_r , add clause y_r
 - for literals: use $y_i = x_i$
 - for node $t_i = \text{AND}(t_j, t_k)$, add clause $y_i \leftrightarrow y_j \wedge y_k$
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 - 3 eliminate duplicates:
 - remove clauses that contain both, x and $\neg x$
 - remove duplicate literals from clause

3SAT

- Boolean formula in CNF, exactly 3 literals over different variables per clause
 - E.g. $(x_1 \vee x_2 \vee \neg x_3) \wedge (\neg x_1 \vee x_2 \vee x_3) \wedge (\neg x_1 \vee \neg x_2 \vee x_3)$
- Obviously 3SAT is in NP.
- To prove it is NP-Hard: Show $\text{SAT} \leq_p \text{3SAT}$
- Converting SAT formula to 3SAT
 - 1 encode parse tree (same idea as for Circuit-SAT \leq_p SAT)
 - one new variable y_i per node t_i .
 - for root node t_r , add clause y_r
 - for literals: use $y_i = x_i$
 - for node $t_i = \text{AND}(t_j, t_k)$, add clause $y_i \leftrightarrow y_j \wedge y_k$
 - similar for OR, NOT
 - 2 convert to CNF
 - Convert clauses $x_i \leftrightarrow \dots$ to CNF
 - e.g. $x_i \leftrightarrow x_j \wedge x_k$ to $(\neg x_1 \vee x_2) \wedge (\neg x_1 \vee x_3) \wedge (x_1 \vee \neg x_2 \vee \neg x_3)$
 - 3 eliminate duplicates:
 - remove clauses that contain both, x and $\neg x$
 - remove duplicate literals from clause
 - 4 fill clauses to 3 variables
 - a $h_1 \vee h_2$ to $(h_1 \vee h_2 \vee p) \wedge (h_1 \vee h_2 \vee \neg p)$ for fresh variable p
 - b h_1 to $(h_1 \vee p) \wedge (h_1 \vee \neg p)$ for fresh variable p , then a)

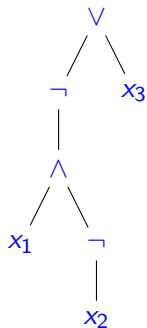
SAT \leq_p 3SAT — Example

SAT Formula: $\neg(x_1 \wedge \neg x_2) \vee x_3$

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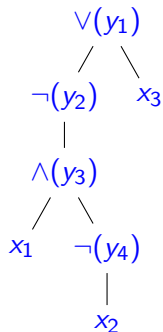
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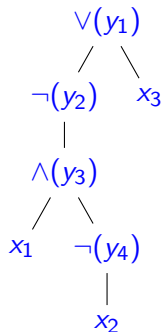


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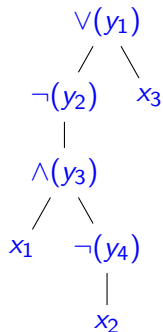
Formula for parse-tree:

$$y_1 \wedge (y_1 \leftrightarrow y_2 \vee x_3) \wedge (y_2 \leftrightarrow \neg y_3) \\ \wedge (y_3 \leftrightarrow x_1 \wedge y_4) \wedge y_4 \leftrightarrow \neg x_2$$

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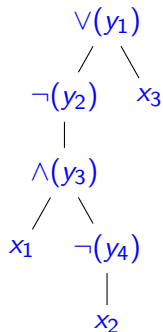
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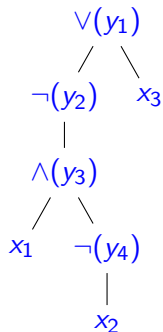
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fill clauses to 3 variables

$$(y_1 \vee p_1 \vee p_2) \wedge (y_1 \vee p_1 \vee \neg p_2) \\ \wedge (y_1 \vee \neg p_1 \vee p_2) \wedge (y_1 \vee \neg p_1 \vee \neg p_2) \\ \wedge (\neg y_1 \vee y_2 \vee x_3) \\ \wedge (y_1 \vee \neg y_2 \vee p_3) \wedge (y_1 \vee \neg y_2 \vee p_3) \\ \wedge \dots$$

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- Good, if we want to reduce 3SAT to a problem
 - Only need to encode very special clauses to problem

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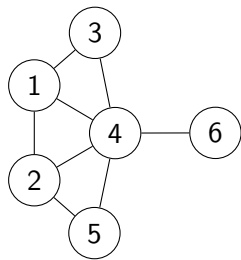
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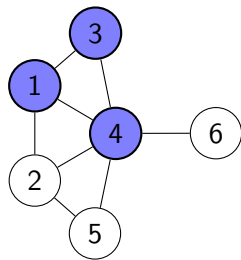
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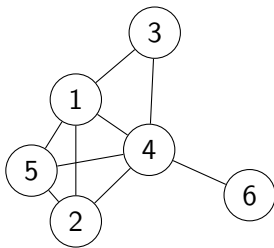
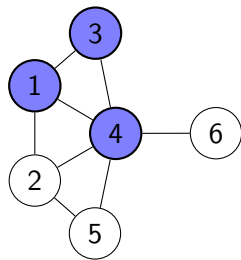
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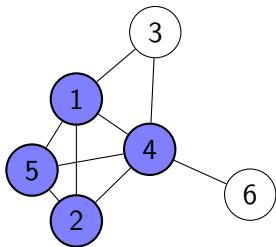
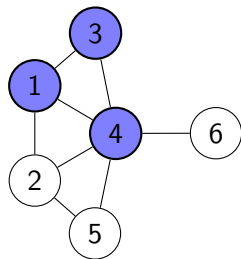
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 - construct graph:
 - one node per l_i^j : $V = \{l_i^j \mid i \leq n \wedge j \leq 3\}$
 - edges between non-contradicting literals of different clauses:
 $E = \{(l_i^j, l_{i'}^{j'}) \mid i \neq i' \wedge j \leq 3 \wedge (l_i^j, l_{i'}^{j'}) \text{ not contradicting}\}$
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 - claim: graph has n -clique, iff formula satisfiable!

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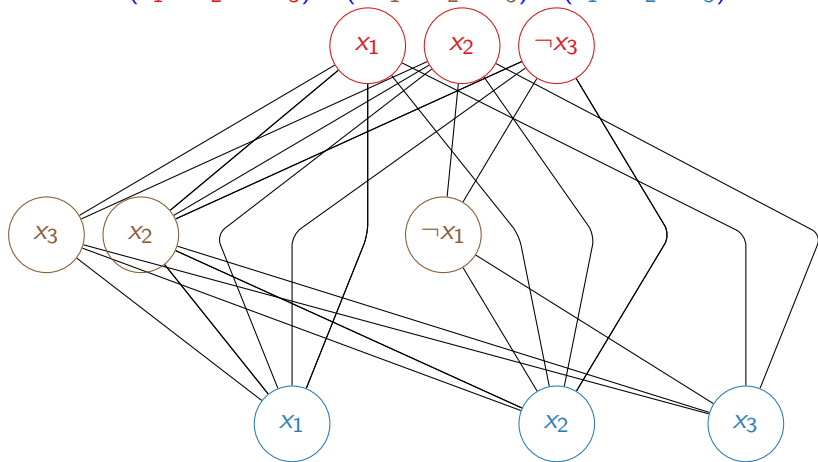
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- must involve literals of different clauses
- which are non-contradictory
- setting them to true yields solution

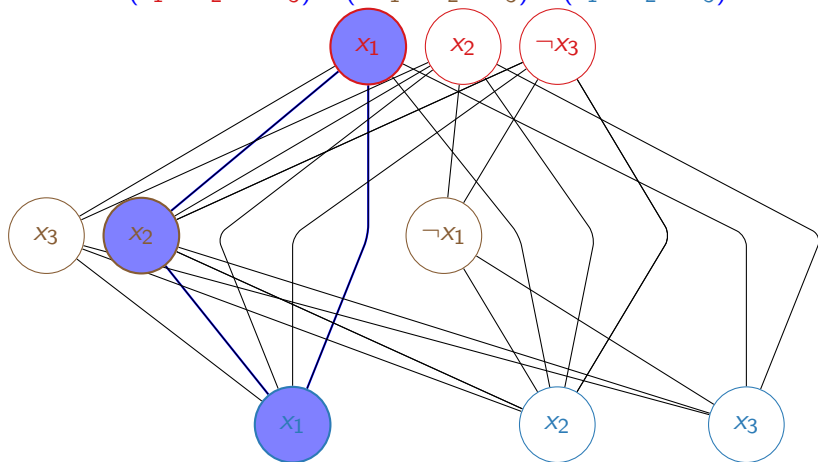
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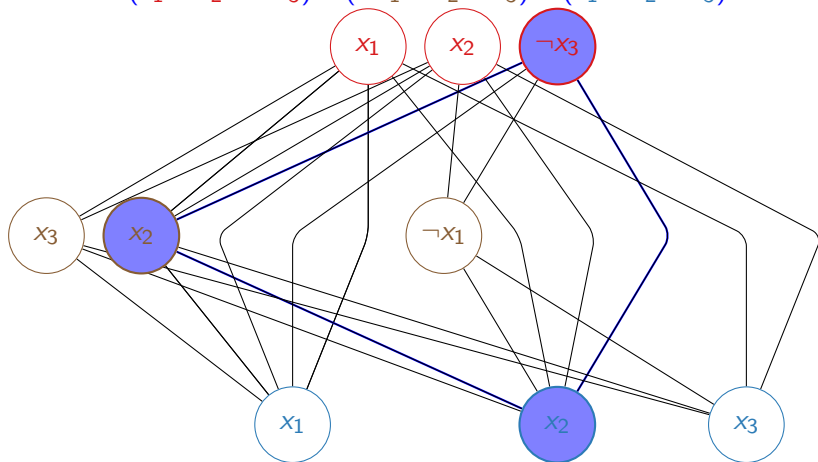
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Solution: $x_1 = \top, x_2 = \top, x_3 = ?$

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Formula: $(x_1 \vee x_2 \vee \neg x_3) \wedge (\neg x_1 \vee x_2 \vee x_3) \wedge (x_1 \vee x_2 \vee x_3)$



Solution: $x_1 = \perp, x_2 = \top, x_3 = \perp$

Conclusions

- NP and NP-complete problems
 - no poly-time algorithms known for NP-hard problems
 - if you encounter one: special case?, approximation?
- Prove that problem is NP-complete:
 - in NP: show poly-time certification
 - NP-hard: reduce other NP-hard problem to it
- Many realistic problems NP-hard
 - You'll come across a few more in this lecture
 - Knapsack, Integer Linear Programming, ...