MicroConverter®, Dual 16-/24- Bit ADCs with Embedded 62KB Flash MCU

Silicon Errata Sheet — ADuC834

A. This Errata sheet represents the known bugs, anomalies and work-arounds for the ADuC834 MicroConverter.

B. The Errata listed, apply to all ADuC834 packaged material branded as follows:

First Line: **ADuC834BS or ADuC834BCP**
Fourth Line: **D22**

C. Analog Devices Inc. is committed, through future silicon revisions to continuously improve silicon functionality. Analog Devices Inc. will use its best endeavors to ensure that these future silicon revisions remain compatible with your present software/systems implementing the recommended work-arounds outlined in this document.

D. ADuC834 Silicon Errata Sheet Revision History :

Revision	Date	Relevance	Silicon Status	# of Bugs Reported
D.0	Sept 2002	All Silicon branded ADUC834BS or ADuC834BCP Fourth Line: D22	Release	1 Errata

REV. D.0 Sept 02

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1 WAKEUP FROM POWERDOWN – 3V FUNCTIONALITY

Background: The ADuC834 can be put into power-down by setting the PCON.2 bit. If enabled power-down can be terminated in one of four ways, TIC interrupt, INT0 interrupt, SPI interrupt or a hardware reset (or power cycle). When TIC, INT0 or SPI wakeups occur the core will jump to the corresponding interrupt vector address in code space and continue executing code from there.

Issue: For DV_{DD} below 4.75V a power-on-reset may occur instead of a wakeup from powerdown. Hence code execution will resume from the start of the program (after a delay of 128ms approx) with all of the SFRs reset to their power-on-reset values.

Work-Around: If operating in powerdown at low voltages user code should save the any necessary configuration data before entering powerdown. This can be saved to RAM/XRAM as the RAM/XRAM will hold its data after a power on reset.

Related Issues: Planned for fix October 2002

ADuC834 Silicon Errata Revision History

Errata #	Description	Status
er1	WAKEUP FROM POWERDOWN	New