Design Rules Verification Report

Filename : C:\Users\a\Desktop\Rollmech_Hardware_Files\Rollmech_Design_Files_Rollmech__IoTProject_\V1.1\IoTProject_V1.1.CS PcbDoc

<div align="right">

Warnings 0
Rule Violations 0

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Warnings	
Total	0

Rule Violations	
Short-Circuit Constraint (Allowed=No) (All),(All)	0
Un-Routed Net Constraint ((All))	0
Clearance Constraint (Gap=0.2mm) (All),(All)	0
Power Plane Connect Rule(Relief Connect)(Expansion=0.5mm) (Conductor Width=0.3mm) (Air Gap=0.2mm)	0
Width Constraint (Min=0.2mm) (Max=3mm) (Preferred=0.3mm) (All)	0
Routing Layers(All)	0
Routing Via (MinHoleWidth=0.3mm) (MaxHoleWidth=0.6mm) (PreferredHoleWidth=0.3mm) (MinWidth=0.3mm)	0
Component Clearance Constraint (Horizontal Gap = 0.254mm, Vertical Gap = 0.254mm) (All),(All)	0
Height Constraint (Min=0mm) (Max=25.4mm) (Prefered=12.7mm) (All)	0
Pads and Vias to follow the Drill pairs settings	0
Hole Size Constraint (Min=0.3mm) (Max=3.5mm) (All)	0
Differential Pairs Uncoupled Length using the Gap Constraints (Min=0.254mm) (Max=0.254mm) (Prefered=0.254mm)	0
Hole To Hole Clearance (Gap=0.254mm) (All),(All)	0
Minimum Solder Mask Sliver (Gap=0mm) (All),(All)	0
Silk To Solder Mask (Clearance=0.254mm) (IsPad),(All)	0
Silk to Silk (Clearance=0.2mm) (All),(All)	0
Unpoured Polygon (Allow unpoured: False)	0
Total	0