

Birla Institute of Technology & Science, Pilani
Work-Integrated Learning Programmes Division
First Semester 2020-2021

ES ZG628T: Dissertation: Planned activity

Start date	End Data	Planned Activity
28/09/2020	4/10/2020	1) Verilog revision 2) FIFO implementation 3) Dissertation Document update
5/10/2020	11/10/2020	1) eCPRI praser Tx Implementation and testing 2) Dissertation Document update
12/10/2020	18/10/2020	1) eCPRI praser Rx Implementation and testing. 2) Tx and Rx path integration
19/10/2020	25/10/2020	1) eCPRI integration on the Xilinx Switch IP integration. 2) FIFO, eCPRI and Switch IP integration
26/10/2020	1/11/2020	1) eCPRI testing with Test JIG 2) Dissertation Document update
2/11/2020	8/11/2020	1) eCPRI integration with AXI bus 2) Dissertation Document update
9/11/2020	15/11/2020	1) OS bring up
16/11/2020	22/11/2020	1) Application implementation 2) Application integration
23/11/2020	20/11/2020	1) Dissertation Document update 2) Dissertation Checklist Update