SELVANATHAN T

TECHNICAL SKILLS

MATLAB
MAXWELL
PROTEUS design suit
C programming
Latex

PERSONAL PROFILE

S/O: THIYAGARAJAN
DOB : 10/04/1995
1/220, West street, Maniyakaranpatti, Pudur, Vilathikulam (TK), Tuticorin(Dt)- 628905.

LANGUAGES

TAMIL	R-W-S
ENGLISH	R-W-S
JAPANESE	R-W-S
TELUGU	S

R- Read, W- Write, S- Speak

ACADEMIC EXPERIENCE

Assistant Professor, Department of Electrical and Electronics Engineering, University VOC college of Engineering, Anna University, Thootukudi. Dec 2025 – Till date.

Guest Faculty at the Department of Electrical and Electronics Engineering, College of Engineering, Guindy, Anna University, Chennai.: 7 months 7 days.

EDUCATION

Pursuing full-time research in "Adaptive battery management system for EV application" Department of Electrical and Electronics Engineering, College of Engineering, Guindy, Chennai.

Master of Engineering in Power Electronics and Drives (CGPA 7.86) College of Engineering, Guindy, Chennai . Sep 2018 – Apr 2020.

Bachelor of Engineering in Electrical and Electronics Engineering (CGPA 7.79) First Class. MEPCO Schlenk Engineering College, Sivakasi. Aug2012 – Apr2016.

Higher Secondary (TN State Board, Percentage – 92.75%) Mar 2012
S.B.K Boys Higher Secondary School, Aruppukottai.

Senior Secondary (TN State Board, Percentage – 95.8%) Mar 2010
S.B.K Boys Higher Secondary School, Aruppukottai.

AREA OF INTEREST

Battery management system

Power Electronics and drives

Digital controllers and development.

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+91 9385455876

https://www.linkedin.com/in/selvanathan-thiyagarajan-918901b4/

FELLOWSHIP

AICTE DOCTORAL FELLOWSHIP:
2021-2023

GATE SCORE

2021- EE- Score: 392
2020- EE- Score: 379

PUBLICATIONS AND PROJECTS

Selvanathan Thiyagarajan, Kavitha Anbukumar, "Novel implementation method of Selective Harmonic Elimination (SHE) on a low cost FPGA controller for V2G application", *17th IEEE Energy Conversion Congress & Expo (ECCE)* 2025, May, 2025.

Selvanathan Thiyagarajan, Kavitha Anbukumar, "Experimental Validation of FPGA Based Battery Modelling and State of Charge Estimation of Li-ion Cell", *3rd International Conference on Power, Control and Embedded Systems (ICPCES-2024)*, September, 2024.

PG: Analysis of PV Based Active Balancing System for Electric Vehicle Application.

This project prototypes and analysis the feasibility of active cell balancing aided by solar energy in electric vehicles.

UG: Automatic Book-self Aligning and Accessing System

This Project prototyped to realize automation and remote handling of books in Library.

WORKSHOPS AND CONFERENCES ATTENDED

17th IEEE Energy Conversion Congress & Expo (ECCE) 2025, Bangalore May 2025.

"Electric Vehicles and Energy Storage Systems: Design, Challenges and Grid Integration Issues", faculty development program by NIT Warangal Mar 2025.

Training on SOLAR ENERGY by MSME-Technology development centre (PPDC), Coimbatore Feb 2024.

SERB sponsored Karyahala on E-mobility and electric vehicle engineering by NITTTR, Chennai June 2023.

DST PURSE supported faculty development programme on "Electric vehicle motors and drives:: Design, Analysis and validation" held on SRM institute of Science and Technology Jan 2023.