

	Jumper Table					
Dsg	Name	Description				
JP1	Vref	Select voltage referece				

rojectName: AD-SDIO_2_Click_v101.PrjPcb				MikroElektronika Batajnicki drum 23			
Design Engineer:	Zoran Jovanov	Revision: V100	Size: A3	11186 Zemun, Belgrade Serbia	=		
Date: 2/3/2020	Time: 9:33:51 AM	Sheet 1 of	1	www.mikroe.com			
Git: http://git	t: http://qitlab/click-boards						