BIRLA INSTITUTE OF TECHNOLOGY AND SCIENCE, PILANI DEPARTMENT OF COMPUTER SCIENCE AND INFORMATION SYSTEMS

Group Number

15

Compiler Construction (CS F363) II Semester 2021-22 Compiler Project (Stage-2 Submission) Coding Details (April 16, 2022)

Instruction: Write the details precisely and neatly. Places where you do not have anything to mention, please write NA for Not Applicable.

1.	IDs and Names of team member	S			
	ID:2019A7PS0088P	Name:Preetika Verma	Name:Preetika Verma		
	ID:2019A7PS1140P	Name:Pritika Ramu			
	ID:2019A7PS0097P	Name:Nandan Parikh			
	ID:2019A7PS0042P	Name:Sneha	<u>-</u>		
	ID:2019A7PS0077P	Name:Aadit Deshpande			
2.	Mention the names of the Submi 1) ast.c 2) ast.h	tted files (Include Stage-1 and Stage-2 9) parser.c 10) parser.h	both) 17) typeChecker.c 18) typeChecker.h		
	3) astDef.h	11) parserDef.h	19) grammar.txt, First.txt,		
	4) driver.c	12) predParseTable.c	Follow.txt		
	5) lexer.c	13) stack.c	20) TestCases: p1-p4.txt,		
	6) lexer.h	14) symbolTable.c	s1-s5.txt		
	7) II1Grammar.c	15) symbolTable.h			
	8) lookupTable.c	16) symbolTableDef.h			
	Group number)		nould be in ONE folder named exactly as		
4.		IDs of all team members at the top of ϵ without names will not be evaluated]	each file (and commented well)? (Yes/		
5.		as specified in the submission guideline	es? (yes/no)YES		
	Status of Code development: Ma				
6.	'No'.	ention 'Yes' if you have developed the c	code for the given module, else mention		
6.			code for the given module, else mention		
6.	'No'.	YES	ode for the given module, else mention		
6.	'No'. a. Lexer (Yes/No):	YES YES	code for the given module, else mention		
6.	'No'. a. Lexer (Yes/No): b. Parser (Yes/No):	YES YES 'No):YES	code for the given module, else mention		
6.	'No'. a. Lexer (Yes/No): b. Parser (Yes/No): c. Abstract Syntax tree (Yes/d. Symbol Table (Yes/ No):	YES YES 'No):YES	code for the given module, else mention		
6.	'No'. a. Lexer (Yes/No): b. Parser (Yes/No): c. Abstract Syntax tree (Yes/ d. Symbol Table (Yes/ No): e. Type checking Module (Yes/	YES YES /No):YES YES			

7. Execution Status:

	a.	Code generator produces code.asm (Yes/ No):NoNo					
	b.	code.asm produces correct output using NASM for testcases (C#.txt, #:1-11):No					
	c.	Semantic Analyzer produces semantic errors appropriately (Yes/No):Yes					
	d.	Static Type Checker reports type mismatch errors appropriately (Yes/ No):Yes					
	e.	Dynamic type checking works for variant records with tagged union and reports errors on executing code.asm (yes/no):No					
	f.	Symbol Table is constructed (yes/no)Yesand printed appropriately (Yes/No):Yes					
	g.	AST is constructed (yes/ no)Yesand printed (yes/no)Yes					
	h.	Name the test cases out of 17 as uploaded on the course website for which you get the segmentation fault (p#.txt; # 1-4, s\$.txt; \$ 1-5, and c@.txt; @:1-8):_If s2.txt gives Segmentation Fault in any Driver option, please execute it again (Please Select Option 5 first)					
8.		 ata Structures (Describe in maximum 2 lines and avoid giving C definition of it) a. AST node structure Has the Fields: AST Node Type, Grammar Symbol, Lexeme, Line number and isUnion. Implemented as an N-ary Tree Node (Parent, First Child, Sibling) 					
	b.	. Symbol Table structure :3 sub tables for Identifiers, Record/Union and Function, with hash-chained nodes (3 types corresponding to each type of table)					
	c.	Record type expression structure:					
	d.	d. Data structure for global variables:Boolean global in "Identifier Node"					
		. Variant record type expression structure: Boolean isVariant in the "Record/Union Node					
	f. g.						
	_	Structure for maintaining the three address code(if created) :					
	i.	Any other interesting data structures used :					
9.	words popula	ntic Checks: Mention your scheme NEATLY for testing the following major checks (in not more than 5-10) [Hint: You can use simple phrases such as 'symbol table entry empty', 'symbol table entry already foundated', 'traversal of linked list of parameters and respective types' etc.] Variable not Declared:Symbol Table Entry retrieved is empty					
	b.	Multiple declarations:Symbol Table Entry already for lexeme already exists					
	C.	Number and type of input and output parameters :Checked in Pass 4 of AST traversal, while making Function Table					
	d.	I. assignment of value to the output parameter in a function: _Matching type and number of outputs of function call with symbol table					
	e.	function call semantics: Type checking and number matching the input and output parameters in function call with the function node in symbol table.					
	f.	static type checking:Referred to Symbol Table to compare AST Types					
	g.	return semantics :_Checking if return parameters are assigned a value and the type and lexeme is the same as the function output parameters					
	h.	Recursion:No Recursion allowed in functions					
	i.	module overloading: implemented operator overloading					

j.	if-then-else semantics:Implemented in AST with correct syntactic structure					
k.	<pre>handling offsets for local variables (starting with 0, integer size =2, real size =4 for symbol table purpose):Each Function has local offset starting from 0, counting input/output parameters first then counting for local declarations of variables</pre>					
I.						
m.	handling global variable declaration over local variables and input-output parameters:_overwrite in symbol table entry with global variable declaration					
n.	Record semantics and static type checking:checking nested records attributes matching with the symbol table					
0.	Variant record semantics and dynamic type checking:(Not handled)					
p.	Scope of variables and their visibility:stored and handled with identifier table and function table					
q.	handling nesting depth of variables in Boolean expression in while loop for assignment of an expression to one of the guard variables: checking if the comparison in a while loop is performed for variables that have been assigned a value in symbol table entry					
traver	iler passes description (Mention the details of information collected/populated/worked upon at each sal of the whole AST):					
	Pass 1: _collect names of records and unions					
	Pass 2:map alias of constructed datatype to actual name					
C.	Pass 3:creates record and union table traversing fields of all the					
d.	records Pass 4:width calculation for record and union recursively and storing the identifiers					
11 Code (Generation: <u>(Not implemented)</u>					
	NASM version as specified earlier used (Yes/no):					
	Used 32-bit or 64-bit representation:					
C.	For your implementation: 1 memory word =(in bytes)					
	Mention the names of major registers used by your code generator:					
	For base address of an activation record:					
	for stack pointer:					
	• others (specify):					
e.	Mention the physical sizes of the integer and real data as used in your code generation module					
	size(integer):(in words/ locations),(in bytes)					
	size(real):(in words/ locations),(in bytes)					
-						
f.	How did you implement functions calls?(write 3-5 lines describing your model of implementation)					

	· 		(N.A.)	
g.	Specify	Specify the following:			
	• Caller's responsibilities:				
	•	Callee's responsibil	ities:		
h.	. How did you maintain return addresses? (write 3-5 lines):				
				(N.A.)	
i.				How were the statically computed offsets of the	
j. What have you included in the activation record size computation? (local variables, parameter					
k.			nanually selected he	uristic only)	
I.				n your code generation module?(Integer and real):	
m.	. Where are you placing the temporaries in the activation record of a function?				
n.	Write	your method of code	e generation for dyn	amic type checking for tagged union data type	
-	ilation D	Details : ile works (yes/No):_	YES		
b.	Code C	Compiles (Yes/ No):_	YES		
c.	. Mention the .c files that do not compile:None				
d.	Any specific function that does not compile:None				
e.		ed the compatibility o)YES		e specified versions [GCC, UBUNTU, NASM]	
	on (s1-s	5.txt), and code gen	eration (c1-c8.txt)] :	king (p1-p4.txt), semantic analyses including symbol tabl and (in seconds)0.006970	
				and (in seconds)0.007325	
				and (in seconds)0.026925	
				and (in seconds)0.027081	
				and (in seconds)0.032761	
				and (in seconds)0.030578	
				and (in seconds)0.028290	
				and (in seconds) 0.028380	

	ix.	s5.txt (in ticks)	30	and (in seconds)	0.029815
	х.	c1.txt (in ticks)		and (in seconds)	
	xi.	c2.txt (in ticks)		and (in seconds)	
	xii.	c3.txt (in ticks)		and (in seconds)	
				and (in seconds)	
				and (in seconds)	
				and (in seconds)	
				and (in seconds)	
				and (in seconds)	
(Al L5. Sp — L6. Ar	I 10, Excepted the later of the	t Code Generation) nguage features you de Generation, Varia ng the lifeline (Yes/ ommand you expec	ur compiler is not ant Records Dynam No):NO t to be used for execute NASM created or	ecuting the code.asm using	n one line) NASM simulator [We will use
go 19. An Ta	to stmts etc y other po i ble for hand	c) (g) modular (h) sp int you wish to mer dling nested records	pace and time efficention:We have in a contract of the con	cient mplemented comprehensing is very thorough and is c	rg style (indentation, avoidance of ve functionality in the Symbol capable of handling operations
20. De	claration: V	We, Preetika Verma,	Pritika Ramu, Nan	dan Parikh, Sneha, and Aad	it Deshpande declare that we have
pu	out our genuine efforts in creating the compiler project code and have submitted the code developed only by our				
gro	group. We have not copied any piece of code from any source. If our code is found plagiarized in any form or				
de	degree, we understand that a disciplinary action as per the institute rules will be taken against us and we will				
ac	accept the penalty as decided by the department of Computer Science and Information Systems, BITS, Pilani.				
[W	[Write your ID and names below]				
ID _.	2019A7P 2019A7P 2019A7P	a			
		S0042P		Name:Sneha	
ַטו	2019A/P	S0077P		Name:Aadit Deshpand	ie
Da	te:16 <i>A</i>	April 2022			
 Ch		reed 6 nages			

Should not exceed 6 pages.