

## VINAY K.B

Mobile: 9538657005

e-mail: vkb5488@gmail.com

### OBJECTIVE

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Seeking a creative, competitive, challenging and growth oriented career to optimally utilize and enhance my skills and to excel in it by hard work and determination.

### EMPLOYMENT DETAILS

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Having overall 2.5 years of Experience in EDA Tools and Designing.

#### 1. Currently Working at FTD Automation Pvt Ltd, Bangalore

**Designation:** Application Engineer

**Duration:** August 2011 to Till Date

**Tools:** Cadence-Allegro 16.3 to 16.6, OrCAD 9.2 to 16.6 (Front to Back flow)

**Mentor Graphics-**PADS, CADSTAR

#### **Description:**

- ❖ Designing & Modification of Printed Circuit Board (PCB) designs using Cadence 16.6 tool as per Client requirements & delivering.
- ❖ Complete Printed Circuit Board (PCB) Design which Includes Schematic parts creation, Schematic draw, Footprints creation as per IPC standards, Netlist creation, Importing Netlist, setting up constraints, placement and Board Design.
- ❖ Schematic design and re-design (conversion from other tools like Dx-Designer, PADS etc) to Orcad and linking with the board.
- ❖ Creation of BOM for PCB's.
- ❖ Library management and development of footprints, Schematic symbols and attributes of components like IC's, Connectors, Discrete components using *End User tool* called *EMA Design Automation – Component Information Portal (CIP)*.
- ❖ Technical Documents for IC's, Resistors, Capacitors etc. & uploading it to PLM.
- ❖ Materials Creation in PLM.
- ❖ Release the Designs, BOM & related Documents.
- ❖ Converting OrCAD files to other formats.
- ❖ Exporting layout files to IPC-2581 and IPC-356 formats.
- ❖ Expertise on Valor tools and ODB++ formats.

### **Key Activities:**

- ❖ Library development of footprints for layout & Schematic symbols.
- ❖ Providing training to the customers across all over India and helping out for the designs.
- ❖ Having expertise on Signal Integrity tools Like OrCAD and Allegro crosstalk and reflection.
- ❖ Having provided training to more than 60 to 70 customers and also public programs like seminars.
- ❖ Experience in PSpice model creation and editing the models provided by the customers.
- ❖ Experience in PreSales and PostSales area.
- ❖ Prepared BKM (Best Known Methods) documents which helps to implement ideas/methods of several procedures through tool and end user tools.
- ❖ Finding and suggesting alternate components of different packages with similar attributes for hardware circuit design in order to make the circuit effective and reduce space usage.

### **Responsibilities:**

- ❖ Handling government and private customers all over India.
- ❖ Helping them in the tools and supporting them in their designs.
- ❖ Providing training and onsite customer support.
- ❖ Having experience in Pre and Post Sales.

### **Tools Proficiency:**

- ❖ Cadence – Allegro, OrCAD
- ❖ OrCAD Capture CIS
- ❖ OrCAD PSpice A/D
- ❖ OrCAD PSpice Advanced Analysis
- ❖ OrCAD PCB Professional and Allegro PCB Designer
- ❖ OrCAD Signal Integrity
- ❖ Mentor Graphics – PADS
- ❖ Cadstar CR-5000
- ❖ C, C++ and Python
- ❖ Saber, LTSpice
- ❖ Linux(Fedora and Ubuntu and Redhat)
- ❖ Embedded Systems with IAR and RTOS.
- ❖ EICAS Lab
- ❖ EMI Stream
- ❖ EMC Analysis with Termal tool.

**Technical Interests:**

- ❖ Engineering & Design, R&D
- ❖ Designing Hardware circuits for specific functionality.
- ❖ Having knowledge on Signal Integrity analysis, power analysis.
- ❖ Knowledge in C C++ and networking areas and Python programming.

**Organizational Interests:**

- ❖ Felicated by IIT Madras and NIE College Mysore
- ❖ Involved in a project going on in IIT Bombay
- ❖ Actively participated in many sports and cultural events at organization and won prizes.

***PROFESSIONAL EDUCATION***

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**Engg. Degree:**

- **Course** B.E. Computer Science
- **Institute** National Institute of Engineering, Mysore, Karnataka.
- **University** Visveswaraya Technological University,  
Belgaum, Karnataka
- **Year of passing** 2010
- **Degree** 63.55%

***POST Graduation***

BITS Pilani MS Software Systems

<b>Class</b>	<b>School</b>	<b>Board</b>	<b>Result</b>
2 <sup>nd</sup> PUC (Electronics)	Sadvidya College Mysore	Pre University Board, Karnataka	72%
Class X	SVEI Mysore	Karnataka State Secondary Education Board	84.84%

## **PERSONAL PROFILE**

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<b>Father's Name</b>	K.R Baburao
<b>Mother's Name</b>	Shamantaka
<b>Date of Birth</b>	05-04-1988
<b>Permanent Address</b>	No 1834 12 <sup>th</sup> main Vijaynagar 2 <sup>nd</sup> stage, Mysore
<b>Current Address</b>	No 1519 13 <sup>th</sup> main 10 <sup>th</sup> cross BTM 2 <sup>nd</sup> stage, Bangalore
<b>Languages known to Speak</b>	English, Hindi, Kannada.
<b>Strength</b>	Fast learner & hard worker, Confidence, Consistent Worker.
<b>Leisurely activity</b>	Professional Singer, Reading Books.

**Reference:**  
**Mahendra C**  
**FTD Automation Pvt Ltd**  
**Senior Application Engineer**  
**Bangalore**

I hereby declare that the information given above is true to the best of my knowledge

**Place: Bangalore**

**VINAY K.B**

**Date:**