**CURRICULUMVITAE**

Suresh Kumar T Mail id: [suresh.vlsi46@gmail.com](mailto:suresh.vlsi46@gmail.com)

+91 9743224365

**SUMMARY:**

* Have 2.8 years of experience as a verification engineer at Arasan Chip Systems Pvt Ltd
* Hands on experience in C, Verilog HDL, System Verilog, UVM, Perl
* Developed verification environment for MIPI DSI using verilog
* Hands on experience in test plan and writing the test scenarios
* Developed Verification environment for AHB protocol using UVM methodology.
* Experience in debugging and running the regressions.
* Good understanding of design logic design and AHB, APB protocols
* Experience in working with tools Modelsim, NCsim, Questasim, ICCR, Spyglass

**WORK EXPERIENCE:**

**Company:** Arasan Chip System Pvt Ltd

**Designation:** Verification Engineer

**Period:** Aug 2011 to till date

**EDUCATIONAL QUALIFICATION:**

M.Tech in Electronics and Instrumentation June 2009 to June2011

National Institute Of Technology, Warangal CGPA: 7/10

B.Tech in Electronics and Communications Sept 2004 to May 2008

Prakasam Engineering College Percentage: 67.84

Intermediate in MPC July 2002 to April 2004

Pratibha Junior college Percentage: 86.1

SSC July 2001 to Mar 2002

Nagarjuna Vidhyalayam High School Percentage: 77.33

**PROJECTS**:

**Project: AMBA AHB slave verification using UVM methodology**

**Duration:** Sept 2013 to till date

**Description:** An AMBA-based microcontroller typically consists of a high-performance system backbonebus, able to sustain the external memory bandwidth, on which the CPU, on-chip memory and other Direct Memory Access(DMA) devices reside. This bus provides a high-bandwidth interface between the elements that are involved in the majority of transfers. AHB implements the features required for high-performance, high clock frequency systems including burst transfer, single clock edge operation and wide data bus configurations, 32, 64, 128, 512, 1024 bits.

**Roles & Responsibilities:**

* Developed test cases for the different scenarios.
* Developed test environment for AHB slave using UVM methodology
* Simulation and debugging
* Implementing functional and code coverage

**Project**: **MIPI DSI (Display Serial Interface)**

**Duration:** Dec 2011 to Aug 2013

**Description:** The Display Serial Interface Specification defines protocols between a host processor and peripheral devices using a D-PHY physical interface. The DSI specification builds on existing specifications by adopting pixel formats and command set defined in MIPI Alliance specifications for Display Bus Interface 2 (DBI-2), Display Pixel Interface 2 (DPI-2) and Display Command Set (DCS).

**Roles & Responsibilities:**

* Understood the full verification flow of Display Serial Interface
* Developed test environment for DSI receiver using verilog
* Developed test cases for DSI Receiver
* TB plan creation, simulation, debugging.
* Implementing Code coverage.
* Setting regression environment.

**Project: APB Protocol verification**

**Duration:** Aug 2011 to Nov 2011

**Description:** The APB is part of the AMBA protocol family. It provides a low-cost interface that is optimized for minimal power consumption and reduced interface complexity. The APB interfaces to any peripherals that are low-bandwidth and do not require the high performance of a pipelined bus interface. All signal transitions are only related to the rising edge of the clock to enable the integration of APB peripherals easily into any design flow. AMBA APB bus transfers are a read or write operation of a data object, which always requires two bus cycles.

**Roles & Responsibilities:**

* Developed test environment for APB using verilog
* Developed test cases for the different scenarios.
* Simulation and debugging

**Project: Interfacing ADC0804 to Parallel Port:**

**Duration:** Jan 2011 to March 2011

**Description**: The aim of the project is to measure the temperature and to displays it in PC. A sensor is being connected to an Analog-to-Digital Converter (ADC) and it is interfaced to pc parallel port. The two major areas that need to be addressed are– hardware and software. The hardware consists of the parallel port, an ADC, and an analog signal source or sensor LM35.

**Roles & Responsibilities:**

* Programmed C code for parallel port interfacing

**Achievements:**

* Secured **903** All India rank in GATE 2009.
* Received prizes for cricket competitions conducted by various colleges.