**Sreevatsa Joshi**

**10/1, Mallikarjuna Residency, 10th Cross, Bhuvaneshwarinagar, BSK 3rd stage, Bangalore 560085**

**Mobile: +91 9980032475; Mail-ID: sreevatsajoshi@gmail.com**

A qualified, trained and seasoned IT Professional with over **12+** years of rich and professional experience, involved in Project Participation/Leadership of Integration, Testing and Design Validation of Embedded Products for Multimedia, DSP, Audio/Video Decoders, Set Top Box domain on multiple platforms (UNIX, Linux, Windows, RTOS), Based Remote Data Acquisition Platform gaining experience on Project Management, Integration, Software Testing Life Cycle and Team Management

Seeking leadership challenges in **Project Management/ Software Testing.**

**Competence**

**Functional Skills**

* **Core Strength** in Supervision of Project Functions like planning, scoping, estimation, tracking and implementation; Testing of Multiple Embedded Products (Audio/Video Codec, Device Driver, Set Top Box, DTV) as well as Solutions (API, Network Protocols, Middleware); All phases of Software Testing Cycle and Defect Management
* **Special Competence**  includes Technical Expertise on MPEG /DVB/ATSC standards, Test Tools like Quality Centre/Rational Robot/ClearQuest, Network/Streaming protocols, End-to-end Testing of Set Top Box; Multiple platforms (RTOS - Psos/Mucos/Embedded Linux, Windows, UNIX, Linux); Manual Testing and Test Automation; Team Management & Development
* Knowledgeable of all functions of **Project Life Cycle** from Business Function Study, Requirement Finalization, Project Scoping, Task Break down, Effort & Time Estimation, Costing & Project Budgeting, Resource Allocation, Scheduling, Risk Management to Execution Control Planning/Monitoring
* Hands-on expertise of **Project Coordination,Integration and build process** including Stake Holder Interaction, Project Progress Reporting, Project Level Issues Resolution, Expectation Management, Technology Conflict Resolution, Cross-functional Process Coordination
* Extensive expertise of **Software Quality Assurance (SQA)** involving Definition & Documentation of standards, regulations and procedures to produce, verify, evaluate and confirm products during the software development lifecycle; Incorporation of knowledge base of best practices; Incorporation and use of off-the-shelf software tools; Fault Tracking and Resolution Support
* Equal proficiency of **Software Testing Life Cycle(10+ yrs)** by Development of High Level Test Plan, Detailed Test Plan/Functional Validation Matrix/Test Cases' Review of Test Cases & selection for Automation, Scripting of Test Cases; Complete Testing Cycles execution (including Functional & Performance Tests; Test Result Documentation; Evaluation of testing processes
* Adept in **Team Management(4+yrs)** skills including tasking, delegation, monitoring, feedback, skill upgrade, competence management, career planning, motivation & mentoring of the team
* Cohesive **Team Player** with Fast Learning Curve along with strong analytical, problem solving, innovation, planning, organizational, communication & interpersonal skills
* **Good fundamentals of Linux, Kernel Architecture and Java.**

**Computing Skills**

* Protocols: TCP/IP, Wi-Fi, RTSP, IPv4, IPv6,SIP,GPRS,GSM
* Middleware: MHC (NDS Middle ware), Fusion OS
* Operating Systems: Win NT6.0/98/2K/XP, Linux, UNIX
* Embedded OS: Embedded Linux, Psos, VXworks, Mucos
* Architectures : 8051,ARM7/9,MIPMS32
* Language: C, C++,Java
* Scripting Tools: Perl, TCL/TK, Python, JavaScript,Ruby
* Database: MS SQL Server 2000
* Test/Debugging Tools: Clear Quest, Hp Quality Center
* CM Tools: VSS, CM Synergy,**GIT**
* Project Tool: MPP(Microsoft Project plan)
* Automation tools QTP,Loadrunner,Selenium,Jmeter

**Training & Certifications**

* Attended training on MPEG Standards, RTOS concepts conducted at Philips Semiconductors.
* Further trained on Rational ClearCase and on Java.

**Project Summary**

Participated/Managed **over 9 projects** involving testing and design validation of embedded solution as well as business solutions and the following project is a snap shot of experience:

**iTriangle Infotech Pvt. Ltd., Bangalore**

**Team Lead (Integration & Validation) (Jun 2012 to July 2014)**

Reporting to the Management, with the specific responsibilities of providing Technical Leadership to Test Teams for multiple product evaluations

**Responsibilities:**

* Primarily engaged with Integration and validation of software components that build the Remote Data Acquisition Platform over which different GSM,GPS, Sensor & RFID based applications are developed.
* Managing all phases of Testing Life Cycle including requirement analysis, designing Test plans, Test cases and Test Specifications.
* Responsible for both Manual Testing as well as Test Automation.
* Contributing in process improvement by designing and implementing Templates for use across projects.
* Performing various Team Development initiatives like recruitment interview, training & competence development

*Please refer to Annexure for further details on projects*

**Career Profile**

**Previous Engagements**

|  |  |  |
| --- | --- | --- |
| **Organization** | **Duration** | **Designation** |
| Mirafra Software Technologies Pvt. Ltd., Bangalore | July 2011 to May 2012 | Member of Technical Staff |
| Corpus Software Pvt.Ltd., Bangalore | Feb 2011 to June 2011 | Test Analyst |
| NDS Pay TV Technologies Pvt. Ltd., Bangalore | Jun 2007 to Jan 2011 | Sr. Engineer QC |
| RelQ Software Pvt. Ltd., Bangalore | Oct 2004 to Jun 2007 | Sr. Software Engineer |
| NIC, Government of India, Bangalore | Jul 2002 to Oct 2004 | Software Engineer |

**Education**

**EDUCATION**

* **B. E. (Computer Science)**, from Adichunchanagiri Institute of Technology Kuvempu University (2001) with **First Class (67.5%)**

**Personal Details**

Date of birth: 03-02-1980

Languages known: Kannada, English, Hindi and Sanskrit

Location: Bangalore

**ANNEXURE**

**PROJECT DETAILS**

|  |  |
| --- | --- |
| **Project** | GPS-GSM-RFID Based Remote Data Acquisition Platform |
| **Client** | ITRIANGLE InfoTech |
| **Duration** | July 2012 till May 2014 |
| **Team Size** | 7 |
| **Environment and Tools** | C/C++ and Embedded firmware development, ARM7/9, GCC, Eclipse,  SVN, CVS, VISO, Crystal Revs |
| **Details** | GPS-GSM-RFID Based Remote Data Acquisition Platform is designed and developed over which many applications are developed based on the requirements of the client and will supply the product by manufacturing them at our facility. |
| **Role** | Team Lead |
| **Responsibilities** | * Responsible for the integration of different components in the software stack which form the core of the RDAP software. * Responsible for work status, root cause analysis, result reports, preparation of test matrix etc., * Writing the test plan, developing a new testsuite (or harnessing an existing test suite), and validating the software using the same. * Involved in Design, development and execution of reusable and maintainable automated scripts. |

|  |  |
| --- | --- |
| **Project** | HDL compiler for hardware emulation & hardware-software co-simulation |
| **Client** | Qualcomm |
| **Duration** | June 2011 till May 2012 |
| **Team Size** | 11 |
| **Environment** | System Verilog and C |
| **Details** | Customer is a leader in emulation solutions for hardware verification and hardware-software co-verification. Its solutions are used by most of the leading semiconductor companies to verify their SOC's. |
| **Role** | Technical Lead Testing |
| **Responsibilities** | * Responsible for work status, root cause analysis, result reports, preparation of test matrix etc., * Writing the test plan, developing a new testsuite (or harnessing an existing test suite), and validating the software using the same. * Involved in Design, development and execution of reusable and maintainable automated scripts. |

|  |  |
| --- | --- |
| **Project** | **STB Software for MultiChoice(Hybrid STB Cable+IP)** |
| **Client** | MultiChoice SouthAfrica |
| **Duration** | February 2011 till June 2011 |
| **Team Size** | 10 |
| **Environment** | BCM 7335 Chipset (JDK for EPG Development), Java, ClearCase, ClearQuest, Quality Center, DVB Stream Analyser |
| **Details** | Project for design & performance validation of end-to-end solutions for Set top boxes including the Head end till the software deliverables, comprising of different components such as EPG, Middleware, Driver and loader and is based on the DVB Cable standard which provides the information to the EPG based on the request made by the middleware IRDETO and are tested with multiple configurations |
| **Role** | Technical Lead Testing |
| **Responsibilities** | * Responsible for work status, root cause analysis, result reports, preparation of test matrix etc., * Performing peer review of test cases to ensure adherence to standards and ensures test case completeness. Report test cases failures via Quality Center. * Creating issues pertaining to functionalities, Test Environment setup,Stream Analysis flashing different chipsets and follow-up of the same until resolution. * Completely involved in development of Frame Work for Automating the application using STORM TEST tool. * Involved in Design, development and execution of reusable and maintainable automated scripts using Python. * Involved in the test automation of Scripts for the project. |

|  |  |
| --- | --- |
| **Project** | **STB Software for (HDPVR, SDPVR, HDZapper, IPPVR)** |
| **Client** | Airtel |
| **Duration** | July 2009 till January 2011 |
| **Team Size** | 28 |
| **Environment** | BCM 7402 &BCM 7401 Chipset & Mucos/Embedded Linux (RH) with Microchai (JDK for EPG Development), Java, ClearCase, ClearQuest, Quality Center, DVB Stream Analyser, IEX |
| **Details** | Project for design & performance validation of end-to-end solutions for Set top boxes including the Head end till the software deliverables, comprising of different components such as EPG, Middleware, Driver and loader and is based on the DVB Cable standard which provides the information to the EPG based on the request made by the middleware (MHCORE) and are tested with multiple configurations |
| **Role** | Test Lead |
| **Responsibilities** | * To take ownership of testing activities of two SET Top boxesTravelled onsite to Delhi to support all the STB’s and played a major role in the launch of different set top boxes. * Responsible for work status, root cause analysis, result reports, preparation of test matrix etc., * Performing peer review of test cases to ensure adherence to standards and ensures test case completeness. Report test cases failures via Quality Center. * Creating issues pertaining to functionalities, Test Environment setup,Stream Analysis flashing different chipsets and follow-up of the same until resolution. * Completely involved in development of Frame Work for Automating the application * Involved in Design, development and execution of reusable and maintainable automated scripts using IEX(Ird Exerciser) and VB Script. * Involved in the test automation of Scripts for the project. |

|  |  |
| --- | --- |
| **Project** | **STB Software for (HDPVR, SDPVR, HDZapper, IPPVR)** |
| **Client** | GET, Norway |
| **Duration** | July 2008 till July 2009 |
| **Team Size** | 18 |
| **Environment** | BCM 7402 &BCM 7401 Chipset & Mucos/Embedded Linux (RH) with Microchai (JDK for EPG Development), Java, ClearCase, ClearQuest, Quality Center, DVB Stream Analyser, IEX |
| **Details** | Project for design & performance validation of end-to-end solutions for Set top boxes including the Head end till the software deliverables, comprising of different components such as EPG, Middleware, Driver and loader and is based on the DVB Cable standard which provides the information to the EPG based on the request made by the middleware (MHCORE) and are tested with multiple configurations |
| **Role** | Sr. Software Engineer |
| **Responsibilities** | * To take ownership of testing activities of two SET Top boxes to support all the STB’s and played a major role in the launch of VOD over IP feature on 2 Cable set top boxes. * Responsible for work status, root cause analysis, result reports, preparation of test matrix etc., * Performing peer review of test cases to ensure adherence to standards and ensures test case completeness. Report test cases failures via Quality Center. * Creating issues pertaining to functionalities, Test Environment setup,Stream Analysis flashing different chipsets and follow-up of the same until resolution. * Completely involved in development of Frame Work for Automating the application * Involved in Design, development and execution of reusable and maintainable automated scripts using IEX(Ird Exerciser) and VB Script. * Involved in the test automation of Scripts for the project. |

|  |  |
| --- | --- |
| **Project** | **SET Top box Software for (SDZapper)** |
| **Client** | Tatasky, India |
| **Duration** | July 2007 till July 2008 |
| **Team Size** | 5 |
| **Environment** | BCM7324/5, ST5202/11,BCM 7335/7407 Chipsets & Mucos/Embedded Linux (RH) with Microchai (JDK for EPG Development), Java, ClearCase, ClearQuest, Quality Center, DVB Stream Analyser, IEX |
| **Details** | Project To test and validate the design & performance of Set Top Boxes with various Chipsets and functionalities, based on the DVB S2 standard which provides the information to the EPG based on the request made by the middleware (MHCORE) |
| **Role** | Sr. Software Engineer |
| **Responsibilities** | * Ownership of testing activities of 5 different SET Top boxes. * Reporting all activities of project through various matrices. * Performing peer review of test cases to ensure adherence to standards and ensures test case completeness. Report test cases failures via Quality Center. * Completely involved in development of Frame Work for Automating the application * Involved in Design, development and execution of reusable and maintainable automated scripts using IEX (Ird Exerciser) and VB Script. * Supervise and support test automation team pertaining to the project. |

|  |  |
| --- | --- |
| **Project** | **Nexperia Development Kit software (PNX1500/PNX1700/Pnx0108)** |
| **Client** | Philips Semicondutcors, Sunnyvale, USA |
| **Duration** | Jan 2005 till June 2007 |
| **Team Size** | 6 |
| **Environment** | Psos (Embedded RTOS) with C, Psos, Trimedia Streaming Architecture |
| **Details** | Project for development of Software Platform for software developm,ent for various Media Processors with the functionality of design & performance validation and testing |
| **Role** | Sr Software Engineer Testing |
| **Responsibilities** | * Led a team of 2 members during the complete test cycle of this project. * Involved in preparing Test Case development, Publish and review of Test Cases of the other team members. * Involved in setting up the Test Setup for the project migrated to Offshore at Bangalore * Reported the defects in Bugzilla found during System Testing of the application and prepared test metrics. * Reviewed the defects reported by team members and assigned the defects to the development team and coordinated with the Onsite team members for clarification. * Involved in training team members on different Audio Video decoders. * Involved in **Software Integration of different software components** provided by various sites and generate the build for the PNX1500 PNX 1700 and PNX0108 chipsets. * Reporting progress of project to the client through weekly reports and various matrices. |

|  |  |
| --- | --- |
| **Project** | **Divx 6.0 Codec for Windows and MAC** |
| **Client** | Divx Networks USA |
| **Team Size** | 4 |
| **Duration** | Oct 2004 till Jan 2005 |
| **Environment** | Windows XP/MAC with DIVX Application on Windows and MAC. |
| **Details** | Project for testing and validation of Video Codec for multiple platform and conforming to the requirements of the client |
| **Role** | Software Engineer |
| **Responsibilities** | * Day to day communication with on-site team. * Understanding client’s requirements, Integration Data flow and Implementing. * Prioritizing the task and coordinating with team members to meet the client’s deliverables and support to resolve technical issues. * Create test scenarios, writing and executing test cases in for each application. * Documentation and knowledge sharing. * Prepare Test machine, Perform Certification Test, Software Delivery testing and Failure escalations. * Manual Test Execution and Defect logging using Bugzilla and ClearQuest. |

|  |  |
| --- | --- |
| **Project** | **File Monitoring System and Letter Monitoring System (sachivalavahini)** |
| **Client** | Govt. of India (e-Governance) |
| **Team Size** | 4 |
| **Duration** | Sept 2003 till Sept 2004 |
| **Environment** | JAVA, JSP, Servlets, MS SQL Server2000 |
| **Details** | Project for development of a comprehensive solution to help the organization to monitor the movement and pending of files/letters and assist in easy tracking and disposal of files/letters and gives administrative support right from opening of new files/letters, file attachments, various movements of files/letters, dispatch of files/letters, and record management along with Search facility based on any parameter of a file/letter makes retrieval of files/letters in a user friendly and intuitive manner |
| **Role** | Software Engineer |
| **Responsibilities** | * Participated in analysis of a user's needs, preliminary investigation, gathering information, design, coding , Integration ,testing, Deployment of the software * Creation of Test cases and Ad-hoc testing of system * Review of existing test plans to accommodate changing needs |
| **Project** | **Bangalore One (B1)** |
| **Client** | Govt. of India (e-Governance) |
| **Team size** | 4 |
| **Duration** | July 2002 till August 2003 |
| **Environment** | VB.NET, ASP.NET, SQL Server2000 |
| **Details** | Bangalore One, the prestigious e-governance project developed in co-ordination with the Government of Andhra Pradesh, and tested at the NIC centre, e-governance. The vision of B1 is to eventually bring all the G2C and G2B services within the purview of B1 project so as to obviate the need for citizens and business people to visit the Government offices except for specialized and complex services. |
| **Role** | Sofwtare Engineer |
| **Responsibilities** | * Manually tested the Life cycle of the System. * Involved in functionality testing & user acceptance test. |