

Heads-Up Display for a Manufacturing Microscope

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1 Introduction

Honeywell technicians assemble miniature mechanisms under a microscope. They face repetitive movements and eye strain while checking work instructions which are displayed on a nearby computer. A Kansas State design team outlined several solutions to this problem; a small tablet mounted next to the microscope, a virtual environment using technology similar to the Oculus Rift, and a heads up display inside the microscope's field of view. We are exploring the last of these options.

2 Project Statement

Optically inserting a virtual screen inside the microscope's field of view allows the operator to seamlessly refer to work instructions during assembly. This setup could potentially improve workplace ergonomics and reduce manufacturing time while ensuring all components meet Honeywell's high quality expectations.

3 Design Requirements

- Allows users to interact with PDFs that contain embedded pictures, videos, and links to other work instructions.
- Does not obstruct the 15" vertical workspace below the objective lens.
- Does not restrict airflow in the workspace to maintain ISO Class 7 clean room standards.
- Remains in focus during normal microscope operation.

4 Deliverables

- Design working prototype of heads-up display system.
- Provide all design documents, schematics, and code.
- Operations Manual to demonstrate how to setup and use the system.
- Future design manual that outlines improvements and ideas for development down the line.

5 Functional Overview

To meet all the requirements, our group designed a DLP projector with custom optics, hardware, and firmware. Our design is centered around the Texas Instruments' DLP3010 chipset and uses the DLP3010 EVM optical engine and DLP controller board. The entire system can be broken down into three main parts: main control board, DLP control board, and the optical system. These subsystems work together to display the final image inside the microscope. Figure 1 shows the block diagram of the final design.

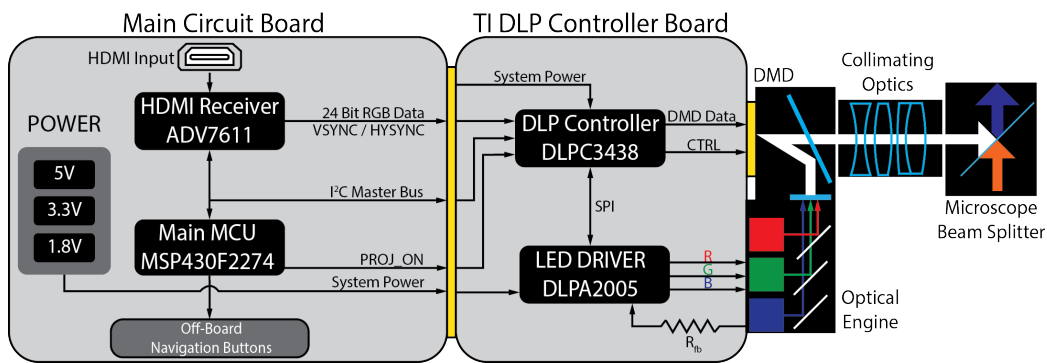


Figure 1: Block diagram of the main board, DLP controller, and Optical system

5.1 Main Control Board

The main control board was designed to take an HDMI input and convert the data to 24 bit RGB format which is fed directly to the DLP3438 display controller. The on-board microcontroller, MSP430F2274, serves as both a user interface hub and system coordinator. The power-on switch and navigation buttons are connected GPIOs. These inputs are configured in software trigger certain events in the projector. For example, the left and right buttons can be used to change screen size, zoom, and cropping while the power switch simply triggers all the initialization code to run. The MCU communicates with both the ADV7611 and the DLPC3438 over I²C. All the power for the system is routed through the main board. The 5V input is stepped down to 3.3V and 1.8V using buck regulators.

5.2 DLP Controller

The DLP controller board was part of an evaluation kit from Texas Instruments. The board consists of the DLPC3438 display controller and the

DLPA2005 PMIC/ LED driver. These two integrated circuits work in tandem to control the DLP3010, digital micro-mirror device.

5.3 Optical System

This optical engine uses three LEDs, red, green, and blue, to project a full color image. The light is combined and reflected off the DMD and out through the custom optical system. The optics we designed take the projected light, magnify, correct for aberrations, and finally collimate the image before it is sent into the infinity space of the microscope. The EMZ-8TRU has a camera port attachment that was removed and modified to change the direction of the beam splitter. This allowed the image to be reflected up through the eyepiece where the user can view the virtual screen.

6 Detailed Hardware Design

6.1 Main Control Board

6.1.1 HDMI Receiver - ADV7611

The ADV7611 receives HDMI signals from the input source, converts the data format, and provides display information to the source over I2C. Before any pixel data can be transferred, the source must know some basic information about the display. The External Device ID is 128 bytes of data that holds manufacturer information, serial number, display dimensions, data speeds, and color information. The final byte holds a check-sum of the data block. Once this information has been transferred over and a confirmation has been sent, both devices are ready to communicate.

The image data is transmitted over four differential pair wires, three for data and one for clock, at 60 frames per second for 1080p displays. This corresponds to a data rate of 3.4Gbps. Routing high speed signals on a circuit board requires special attention. The propagation delay of the signal is directly proportional to the dielectric of fiberglass and length of trace. This becomes incredibly crucial for differential pairs as a mismatch in timing can cause excess power draw on the lines or even corrupt data.

$$V_{signal} = \frac{3 \times 10^8}{\sqrt{\epsilon_r}}, \text{ where } \epsilon_r = 4.6 \text{ for FR4 PCB material.} \quad (1)$$

The ADV7611 converts the video into 24 bit RGB format. This parallel interface uses 8 bits per color per pixel. Each pixel is clocked in like a typewriter, each pixel placed to right of the previous. At the end of each

line, the HSYNC line is pulsed, notifying the receiver to start the next line. The last row of pixels is clocked in and followed by a pulse on the VSYNC line. The receiver sets up for the next frame in the same manner as before, starting in the corner and clocking across.

6.1.2 Microcontroller - MSP430F2274

The on-board MSP430F2274 acts as the main coordinator for the system. The DLP controller and ADV7611 are both controlled via I2c. All settings and events are routed through the microcontroller and sent to the respective chip.

6.1.3 Power

Finally, the main board acts as the power distribution center. The 5V system power is converted to 1.8V and 3.3V using two switching regulators. All three control voltages are necessary for normal operation. When power is first applied, the system waits for the 5V and 3.3V power to stabilize before turning on the 1.8V supply. This power-up sequence is necessary for a clean program start and ensures none of the components are damaged.

6.2 DLP Controller Board

6.3 DMD Controller - DLPC3438

6.4 LED Controller - DLPA2005

The DLPA2005 uses a feedback resistor on the cathode side of the LEDs to track current. An internal DAC is set to limit LED current, the output is compared to the voltage across the shunt resistor and the channel is pulsed off until the current comes back within range. The original value of 39m Ω was intended for current control between 1.5 and 2.1 amps per channel. This device was originally designed to produce 150 lumens and was not intended for direct line of sight out of the box. Replacing the resistor with 100 Ω allowed us to test the LEDs down to a few milliamps.

The DLPC3438 is the direct

7 Concept Comparison

Add content to figure out

8 Testing

Talk about the cuddly bricks

9 Recommendations

Tell engineers to keep moving forward

- Start software for DLPC3438
- Redesign considerations
 - Develop other interface for computer
 - Develop better interface for projector to use zoom and other functionality
- easier focusing -

10 Conclusion

A Operation Manual

step 1: go home.

B Alternative Designs

B.1 Objective Lens Integration

Our design process began by brainstorming various methods of inserting a projection into the microscope system. With our intentions focused only on providing the instructions directly to the user, we predicted three potential points of insertion into the microscope system. These projection possibilities ranged from the area immediately in contact with the user - the eyepiece, to a portion of the optics tangential to the overall system – the camera port, and also the portion of the microscope already being used to capture images – the objective lens.

We decided to pursue a strategy that utilized the objective lens as the insertion point of the projected image for our first design. This was a result of the compactness of the eyepiece component, which had no tolerance for customization, and also that the default position of the camera port system allowed only for image capture. Later it became apparent that the camera port could be changed to display an image to the eyepiece, which we took advantage of at a point farther along in the design process. Because the

objective lens' purpose is to capture light and display it to the user, pursuing this design allowed us to focus more on the projection system as a first priority. We began by purchasing a commercial projector for testing of the various lens and component setups to be used in conjunction with an image source. This was chosen as a way of getting immediate feedback on the methods of focusing the image as it traveled to the objective lens. It is relevant to note that as a result of our project being almost completely hardware oriented, progressing through suggested concepts requires actual testing, and so many of our designs are focused on allowing us to get results on feasibility and quality in a very short time frame.

As part of experimenting with the various lens arrangements and several different beam splitters, it was necessary to disassemble and adapt the purchased pocket projector to better fit our needs. We removed all of the casing and added a small circuit board attachment to optimize the brightness setting of the projector. We were then able to find the correct beam splitter alignment in conjunction with the projector's position so that the image would be viewed properly in the eyepiece. This was very important because as the light travels through the microscope system, the image is reflected off of many surfaces and is displayed in a different orientation than was initially projected at the beginning of the process.

The final part of our design required us to build a physical structure to not only hold each of the components in their particular positions, but also to attach them securely to the microscope itself. The resulting model is shown in 2, where the beam splitter is placed just below the objective lens with the projector and its controlling circuitry held to the side. The projector and beam splitter are placed at specific angles so that the image is reflected directly into the objective lens, but while the power is off the beam splitter allows for the view below the system to enter into the lens unobstructed.

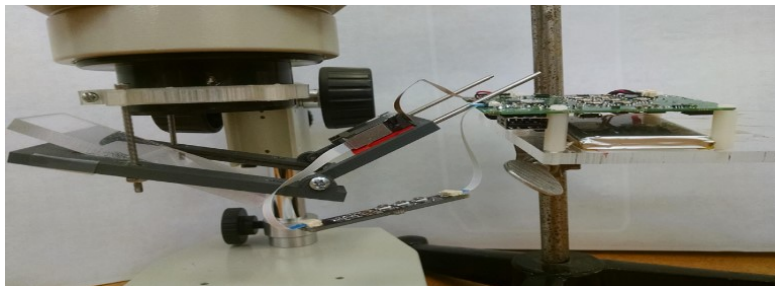


Figure 2: Design using modified commercial projector underneath the objective lens.

This was a good result for our first design, but there were several dis-

advantages that we wanted to optimize as our project developed. The main issue with the objective lens integration setup was that the angle of reflection required the beam splitter and projector to be placed in the user's workspace. We built our model in such a way so that the components were held off to the side, but this design still posed a considerable obstruction to the workstation. Another topic of concern was that the distance between the projector and beam splitter needed to be adjusted each time the zoom of the microscope was changed. This is because the focal point of the projection system needed to be changed in accordance to the magnification being viewed by the user. Finally, the projected image presented a double image when viewed (shown in 3), which was a result of the beam splitter having two reflective surfaces.



Figure 3: Image produced by the objective lens design as seen through eyepiece.

B.2 Camera Port Attachment

As the project progressed, we were able to spend more time learning about the internal optics used in the microscope and to get more comfortable adjusting various parts of the system. At one point we experimented with manipulating the set of mirrors used by the camera port in such a way so that they reflected light in a direction of our choosing. This allowed us to change that part of the microscope which was only initially used for capturing images so that

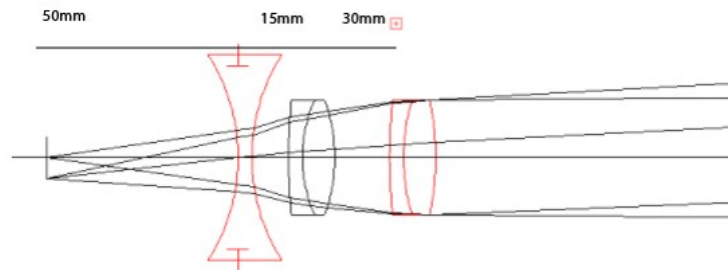


Figure 4: Lens array schematic with dimensions optimized by WinLens 3D software.

it could actually input a light source to be viewed by an eyepiece. Once this development was made, we decided to pursue it as our second method of design for the project. At this point we determined all of the necessary changes to be implemented for the new system. This list included adjusting the camera port mirrors so that the image could be sent in a direction of our choosing, redesigning the camera port hardware itself to allow for easier input of images as well as to hold the project and other components being used. We also knew that we would be able to change our beam splitter implementation completely, swapping in a lens array for focusing because we only needed to consider inserting the image as the camera port attachment does not obstruct the central optics used by the microscope. In order to enact these changes we employed a few programs in conjunction with the usual testing and experimenting that is the staple of this project. One program which was vital to determining the optimal distances used for the lens array that focused our image was WinLens 3D. This optical simulation program not only allowed us to find those distances to be used, but also to test the impact that various types of lenses and optical components had on our projected image. As shown in 4, we went with a three lens system after completing the testing process with that program. We utilized one biconcave and two biconvex lenses with achromatic lenses to correct for color aberration.

C Other Considerations

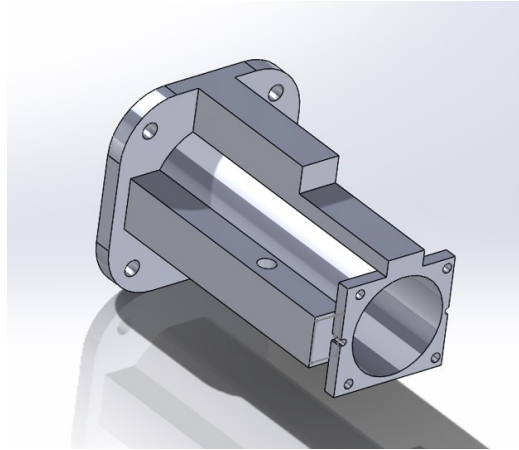


Figure 5: Custom lens mounting bracket to support the lenses used for focusing the image.

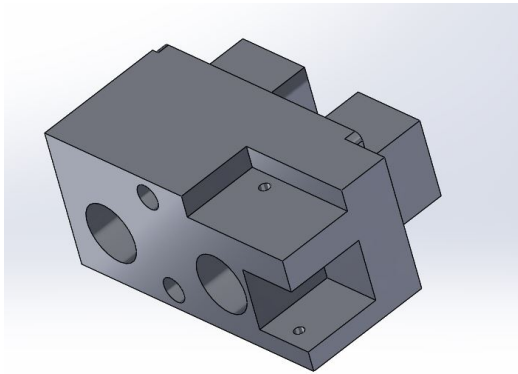


Figure 6: (Custom camera port design to support the projector and house the lens array)