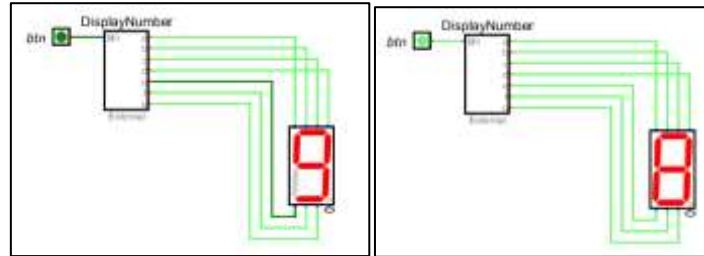


Student Name:

Student ID:

EEE 213 Introduction to Logic Term Project -2024

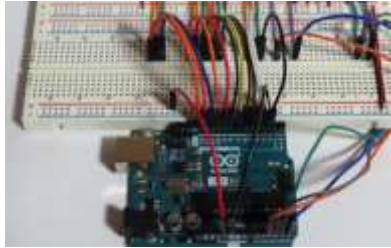
1. (50 points) Design a logic circuit using Digital Logic Circuit simulation which has one input button can become 1 or 0 and has 7 output a, b, c, d, e, f, g connected to a 7-segment display. Output will toggle between the last 2 digits of your student ID. Example ID=123456789, btn=1 display=8, btn=0 display=9. You can only use ONLY logic gates AND, OR, NOT



/***/ Copy Paste the pictures of the Circuit simulation for btn=0 and btn=1 *****/

2. (50 points) Select a logic IC according to your Student ID's last digit: OR=0,2,4 XOR=1,3,5 Decoder=6,7 MUX=8,9. Connect input/output pins to an Arduino. Change input pins A,B,C etc for all possible values and observe output "Y" values. Print Your name, Student ID and the truth table using Arduino UART port and observe in Serial Monitor windows

/** Copy Paste the picture of your setup showing Arduino, breadboard, logic IC etc **/



/** Copy Paste the Arduino code inside a text box **/

```
//Senol Gulgonul ID=123456789
//EEE 213 Introduction to Logic 2024 Term Project

int inputA = 5;
int inputB = 6;
```

/** Copy Paste the picture of Serial Monitor showing your ID and truth table **/

```
Senol Gulgonul ID=123456789
AND Gate Testing
A: 0 B: 0 Y: 0
A: 0 B: 1 Y: 0
A: 1 B: 0 Y: 0
A: 1 B: 1 Y: 1
```