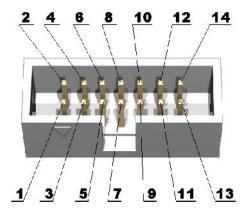
## EXTERNAL CONNECTORS DESCRIPTION: <a href="https://doi.org/10.1007/jtag:">JTAG:</a>



Pin #	Signal Name	Pin #	Signal Name
1	TDO	2	JTAG power supply
3	TDI	4	VCC
5	TMS	6	Not Connected
7	TCK	8	TEST/SWBTCK
9	GND	10	Not Connected
11	#RST/SWBTDIO	12	BSLTX/UCA0TXD
13	Not Connected	14	BSLRX/UCA0RXD

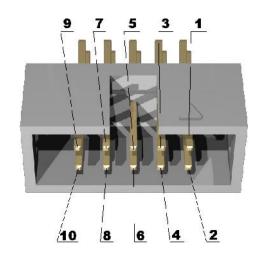
## PWR:

Pin #	Signal Name		
1	VIN		
2	GND		



## <u>UEXT:</u>

Pin #	Signal Name		
1	VCC		
2	GND		
3	TX		
4	RX		
5	SCL		
6	SDA		
7	UCB0_MISO/SC L		
8	UCB0_MOSI/SD A		
9	UCB0_CLK		
10	UCB0_STE		



## **Pin holes:**

	Pin #	Signal Name	Pin #	Signal Name	PJ.6 P2.6
	PJ.0	TDO	P2.6	P2.6	DWT RST; never pull
	PJ.1	TDI	P2.7	P2.7	high (open drain for
	PJ.2	TMS	P3.0	P3.0	normal, down for reset
	РЈ.3	TCK	P3.1	P3.1	WAKEUP: high for 0.5ms to wake up
	TEST	TEST/SWBTCK	P3.2	P3.2	SPI CS internally pulled
	RST	#RST/SWBTDIO	P3.3	P3.3	up, pull down to start (owakeup - 0.5ms)
	3.3V	VCC	P3.4	P3.4	
	GND	GND	P3.5	P3.5	
	P2.5	P2.5	P3.6	P3.6	
	P2.4	P2.4	P3.7	P3.7	
	P2.3	P2.3	P1.0	LED	PZ3
IRQ (active high)	P2.2	P2.2	P1.1	BUT	
	P2.1	SW_SDA	P1.2	UCB0_MISO/SCL	SP MISO 2
	P2.0	SW_SCL	P1.3	UCB0_MOSI/SDA	SP MOSI (pulled dowr
	P1.7	UCB0_STE	P1.4	UCB0_CLK >	SP CLK (pulled down)
	P1.6	BSLTX/UCA0TX D	P1.5	BSLRX/UCA0RXD	