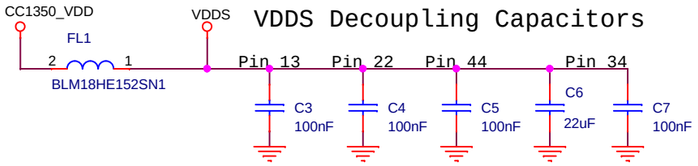
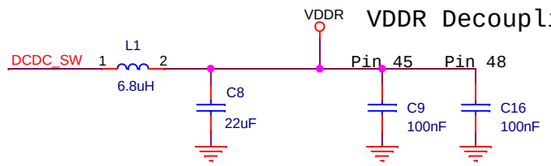
CC1350 RF



VDDS Decoupling Capacitors

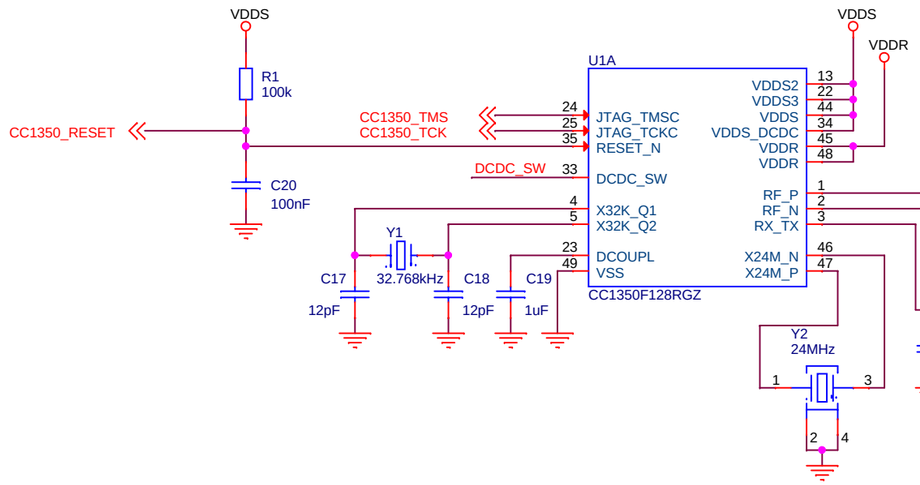
Pin 13 Pin 22 Pin 44 Pin 34



VDDR Decoupling Capacitors

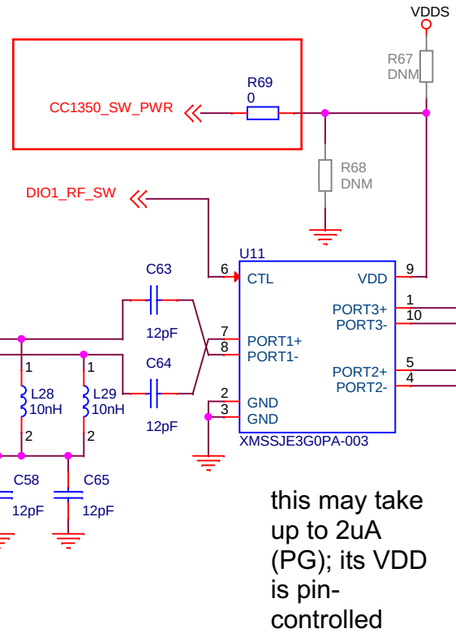
Pin 45 Pin 48

Place L1 and
C8 close to pin 33



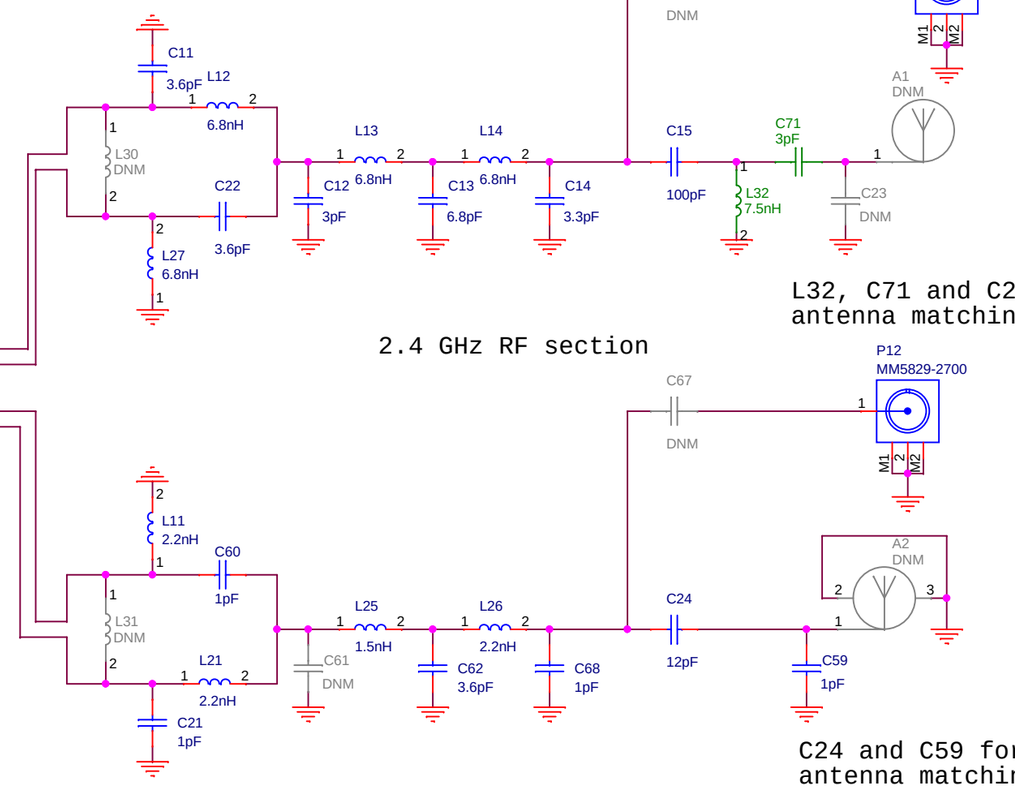
CC1350 IO block
placed on page 2.

R67, R68, R69 select
source of power for RF switch;
only mount one of them



this may take
up to 2uA
(PG); its VDD
is pin-
controlled

Sub-1 GHz RF section



L32, C71 and C23 for
antenna matching

2.4 GHz RF section

C24 and C59 for
antenna matching

915 MHz

Title: LAUNCHXL-CC1350	Texas Instruments
Drawn: TER	
Checked: NN	
Size: A3 Rev: 1.1.1 Sheet: 1 of 4	
Date: Monday, April 11, 2016	

BoosterPack Headers and Peripherals

5V and 3V header

P1 — BB02-HC031-KB1-603000
P2 — BB02-HC031-KB1-603000

Jumpers M13-M14 to be mounted on header P6

P6 — BB02-HJ041-KB1-603000

CC1350 IO block

J1 — SSQ-110-23-L-D
J2 — SSQ-110-23-L-D
U1B — CC1350F128RGZ

External flash

U4 — MX25R8035FZUIL0

Title:	LAUNCHXL-CC1350
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915 MHz

XDS110 Debugger Interface

P10 selects the voltage source for the level shifters
When powering CC1350 from the XDS suppl, connect jumper between pins 1 and 2.
When powering CC1350 from the external supply, connect jumper between pins 2 and 3.

Jumpers M12 to be mounted
between pins 1 and 2 on P10

M12

P10
XDS_VCC
CC1350_VDD
1
2
3
BB02-HC031-KB1-603000

V-Sense

Jumpers M1-M11 to be mounted
on header P4

M1 M2 M3 M4 M5 M6 M7 M8 M9 M10 M11

P4
CC1350_SWO
DIO16_TDO
DIO17_TDI
CC1350_TCK
CC1350_TMS
CC1350_RESET
CC1350_TXD
CC1350_RXD
CC1350_VDD
+5V
XDS_VCC
USB_VBUS
BB02-HJ221-KB1-603000

XDS_RX_LS
XDS_TX_LS
XDS_SWO_LS
XDS_TDO_LS
XDS_TDI_LS
XDS_RESET_LS
XDS_TMS_LS

R61 100k
R62 100k
R63 100k
R65 100k
R57 100
R58 3.3k

C57 100nF
C52 100nF
C53 100nF
C54 100nF
C55 100nF
C56 100nF

U8
U9
U10
VCCB
VCCA
1DIR
2DIR
1nOE
2nOE
1B1
1B2
2B1
2B2
1A1
1A2
2A1
2A2
GND
GND
SN74AVC4T245RSV

XDS_TX
XDS_RX
XDS_SWO
XDS_TCK
XDS_TDI
XDS_TDO
XDS_TMS_DIR
XDS_RESET
XDS_TMS

SW3
1188E-1K2-V

CC1350_VDD

P5
FTSH-105-01-F-DV-K

CR8
TPD6E004RSER

Use P5 for debugging
CC1350 with an
external debugger
(requires that all
jumpers on P4 be
removed)

P7
FTSH-105-01-F-DV-K

CR7
TPD6E004RSER

Use P7 for debugging
external targets
(requires that all
jumpers on P4 be
removed)

XDS-RST = 0 -> output = 0
XDS-RST = 1 -> output = Hi-Z

TMS signal is bidirectional.
TMS_DIR used to control
direction of level shifter

DIR = H: A -> B
DIR = L: B -> A

OE = H: output = Hi-Z

915 MHz

Title:	LAUNCHXL-CC1350
Drawn:	TER, KHT
Checked:	NN
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Texas Instruments

XDS110 Debugger