

Resonant Inverter Stage in Modular Converter for Electrical Vehicle Charger



A PROJECT REPORT

Submitted by

RISHIARAVIND N. (1503122)

SENTHILKUMAR S. (1503130)

THEENADHAYALAN G. (1503142)

in partial fulfillment for the award of the degree

of

BACHELOR OF ENGINEERING

in

ELECTRICAL AND ELECTRONICS ENGINEERING

SRI RAMAKRISHNA ENGINEERING COLLEGE (AN AUTONOMOUS INSTITUTION) COIMBATORE-641022

ANNA UNIVERSITY:: CHENNAI 600 025

APRIL 2019

ANNA UNIVERSITY: CHENNAI 600 025

BONAFIDE CERTIFICATE

Certified that this project report "Efficient Single Phase Transformerless Inverter for Grid Tied PV System With Reactive Power Control" is the bonafide work of "RISHIARAVIND N., SENTHILKUMAR S. and THEENADHAYALAN G." who carried out the project work under my supervision.

SIGNATURE	SIGNATURE		
Dr.S.U.Prabha	DR.C.S.Ravichandran		
HEAD OF THE DEPARTMENT	SUPERVISOR		
Professor,	Professor,		
Electrical and Electronics Engineering,	Electrical and Electronics Engineering,		
Sri Ramakrishna Engineering College,	Sri Ramakrishna Engineering College,		
Vattamalaipalayam,	Vattamalaipalayam,		
Coimbatore- 641022.	Coimbatore- 641022.		
Submitted for the Project Viva-Voce E	xamination held on		
INTERNAL EXAMINER	EXTERNAL EXAMINER		

ACKNOWLEDGEMENT

We express our sincere and deepest gratitude to our respected Managing Trustee, **Sri.R.VIJAYAKUMHAR** and Joint Managing Trustee, **Sri.D.LAKSHMINARAYANASWAMY** for providing the necessary facilities for carrying out our project work in the institution.

We have great pleasure in thanking **Dr.A.EBENEZER JEYAKUMAR** Director (Academics), SNR Sons Charitable Trust. We would like to thank our beloved Principal, **Dr.N.R.ALAMELU** for her continuous motivation towards the development of the students.

We are very grateful to **Dr.S.U.PRABHA**, Professor and Head, Department of Electrical and Electronics Engineering, who has encouraged and gave us valuable suggestions for the completion of this project.

We also take this opportunity to thank our Project Coordinators, **Dr.S.U.PRABHA**, Professor & Head and **Mr.P.SEBASTIAN VINDRO JUDE**, Assistant Professor, Department of Electrical and Electronics Engineering for their positive criticism and support to complete this project.

We express our sincere thanks to our Supervisor, **Dr.C.S.RAVICHANDRAN**, Professor, Department of Electrical and Electronics Engineering for his valuable guidance for the implementation of this project.

We extend our thanks to other Teaching and Non-Teaching Staff Members of Electrical and Electronics Engineering Department for their invaluable help during the project.