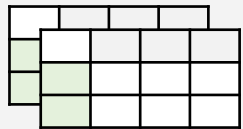


System Trace



Pre-processing

Trace to Image

Hyper-config
Generator

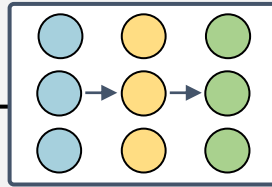
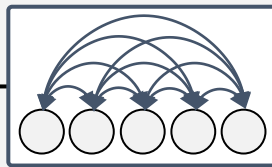


\updownarrow *CHIP Objective*



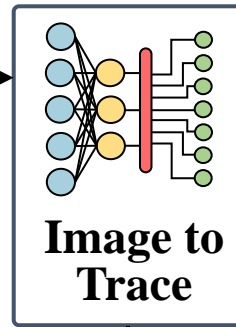
Text Encoder

{ } **Hyper-config**



Prior

Decoder



Generated Trace

