



# Data Sheet

## PLL MODULE

Design

Check

Approval

Customer

VIETTEL Corp.

Part No.

HC1-25MT6G

Lot No.

Date

2016-05-11

### ■ Features & Housing

Low Phase Noise

SMD Type

Small Size

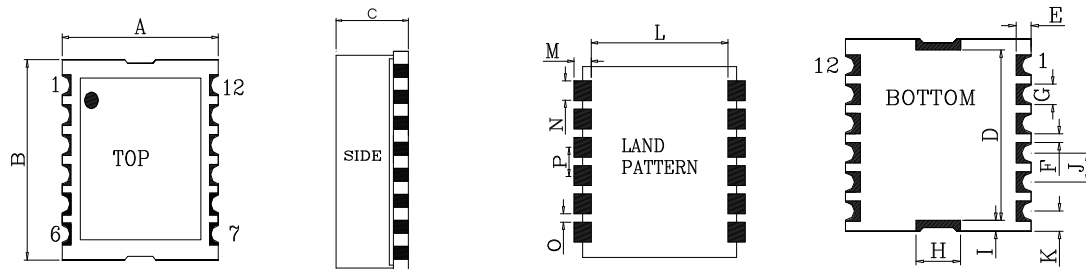
Hybrid Integrated Circuit

Programming

### ■ Specifications

No.	Item	Test Condition (at temp.25℃)	UNIT	Secification		
				Min.	Typ.	Max.
1	Frequency Range		MHz	25		6000
2	Supply Voltage	Pin7	Vdc	3.135	3.3	3.465
		Pin 11		4.74	5.0	5.25
3	Output Power		dBm	+6~-12		
4	Reference Frequency	Recommend Freq.	MHz		10	
5	Reference Input Level	(External)	dBm			
6	Channel Step		kHz		10	
7	2'nd Harmonics		dBc			-15
8	Spurious Level		dBc			-65
9	Current Consumption	Pin 7	mA			60
		Pin 11				180
10	Operating Temp. Range		℃	-40		+80
11	Frequency temp. stability	-30~+80℃	ppm			
12	RF Device			hmc833 (Analog devices)		
13	Phase Noise (C/N)	1kHz Offset (25MHz)	dBc/Hz		-106	
		1kHz Offset (30Hz)			-92	
		1kHz Offset (60Hz)			-88	

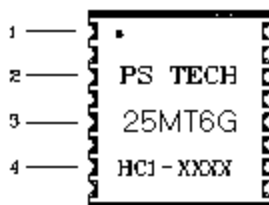
## ■ Dimensions



Dimension (Tolerance $\pm 0.2\text{mm}$ )						
A	B	C	D	E	F	G
12.7	12.9	3.0	12.1	1.2	0.7	1.1
H	I	J	K	L	N	M
4.2	0.4	1.80	1.95	10.3	1.1	1.2
O	P					
0.7	1.8					

Pad Connection					
NO.	Function	NO.	Function	NO.	Function
1	Clock	4	Osc In.	11	Vcc(PLL IC)
2	Data	7	Vcc (VCO)	12	Lock Detect
3	Enable	9	RF Out	All are the pin are Gnd	

## ■ Marking



No.	Printed	No.	Printed
1	#1 Index	4	Model / Date Code
2	Logo		
3	Frequency		