

9/8/11.13

سر

این SR-Latch را در جدولی خلاصه کنید

A	B	C	نتیجه
0	0	0	0
0	0	1	1
0	1	0	0
0	1	1	1
1	0	0	0
1	0	1	1
1	1	0	0
1	1	1	1
1	0	0	0
1	0	1	1
1	1	0	0
1	1	1	1
1	1	0	0
1	1	1	1

Hold

set

~~Reset~~ Hold

Reset

Hold

Not allowed

Not allowed

A	B	C	نتیجه
0	0	1	0
0	1	1	1
1	0	1	0
1	1	1	1
X	X	0	X

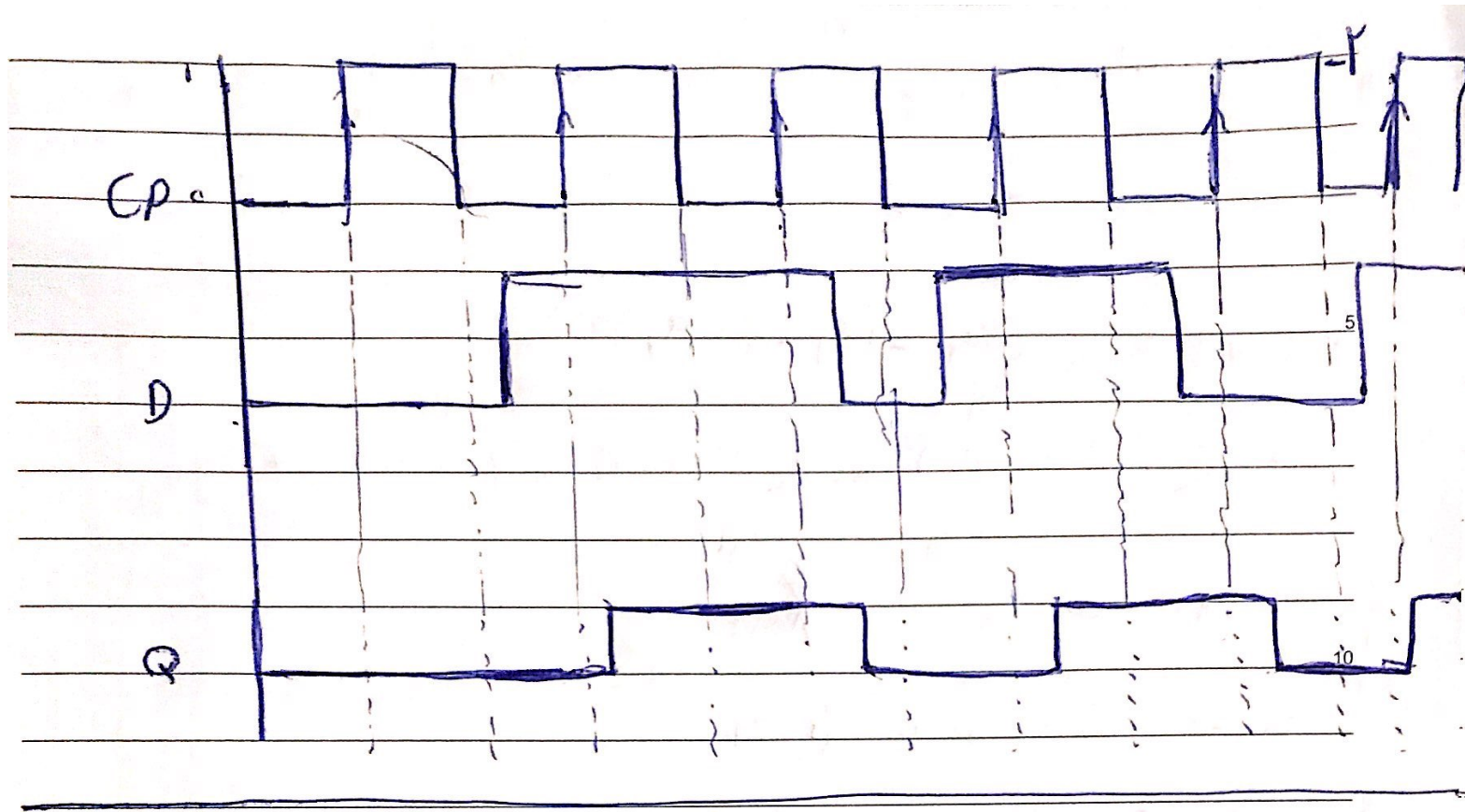
Hold

reset

set

Not allowed

hold



Month: _____

Date: () _____

Subject: _____

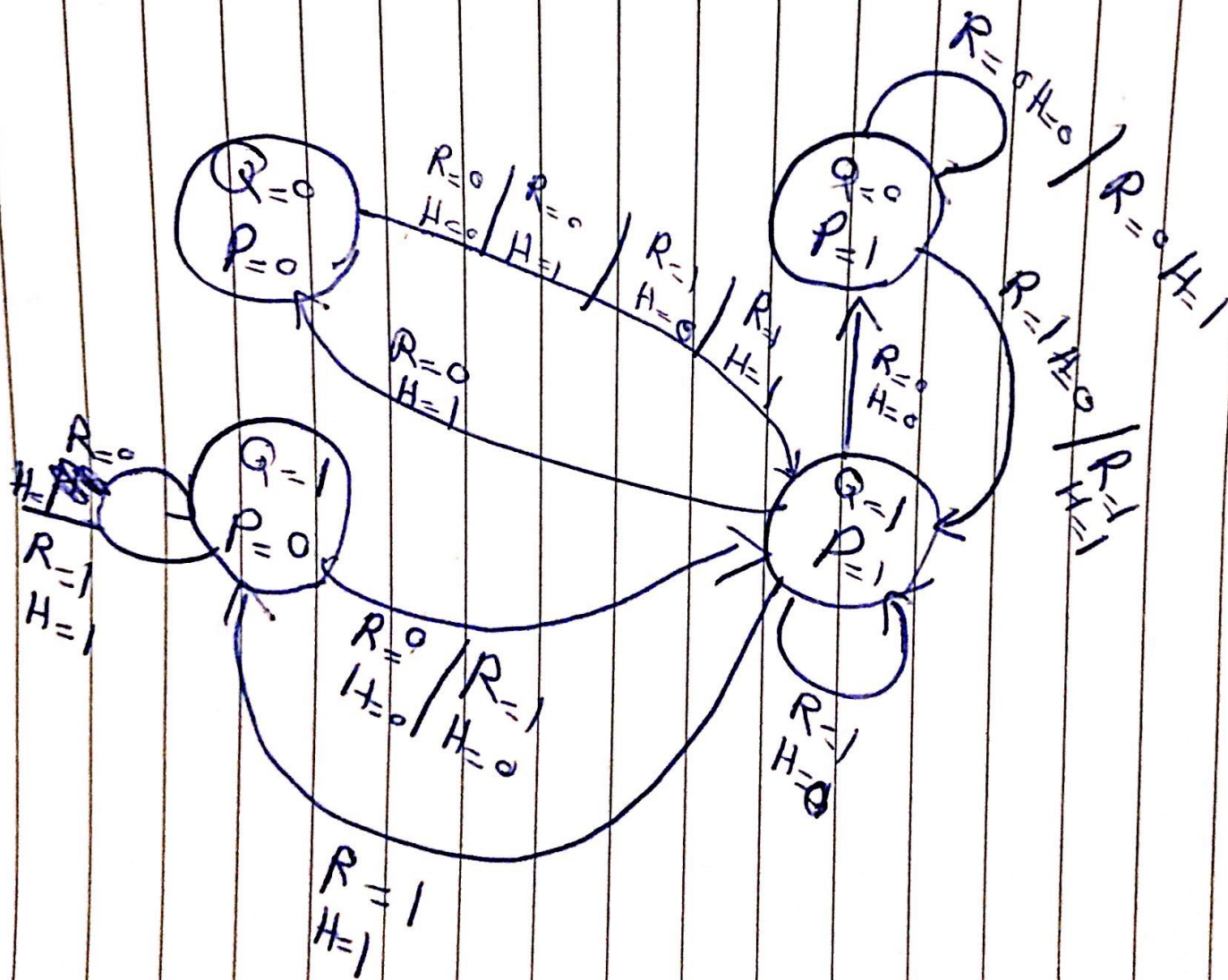
R	H	Q ₊	P ₊
0	0	P'	Q
0	1	P'Q	
1	0	1	1
1	1	1	Q'

$$Q_+ = P' + R$$

$$P_+ = Q' + H' = (QH)'$$

R	H	00	01	11	10
00		1	1	1	1
01		1	1	1	1
11		0	0	1	1
10		0	0	1	1

P	Q	00	01	11	10
00		1	1	1	1
01		1	0	0	1
11		1	0	0	1
10		1	1	1	1



Year: Month: Date: ()

ع. ق. ب. ا. و. س. ج.

$$\begin{array}{r} A_1 \quad A_0 \\ B_1 \quad B_0 \\ \hline A_1 B_0 \quad A_0 B_0 \end{array}$$

$$A_1 B_1 \quad B A_0$$

$$\begin{array}{r} A_1 B_1 \quad A_1 B_0 \quad A_0 B_0 \\ B A_0 \end{array}$$

module multiplication (

Input [1:0] A, [1:0] B,

Output [2:0] sum, Cout);

wire C;

assign sum[0] = A[0] & B[0];

assign sum[1] = (A[1] & B[0]) ^ (A[0] & B[1]);

assign C = (A[1] & B[0]) & (A[0] & B[1]);

assign sum[2] = (A[1] & B[1]) ^ C;

assign Cout = (A[1] & B[1]) & C;

end module.