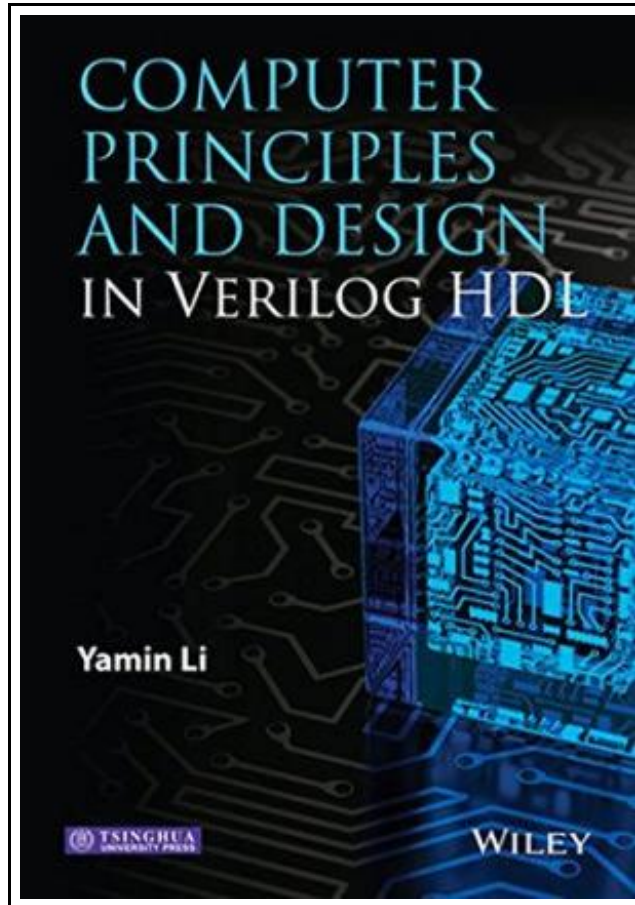# Computer Principles and Design in Verilog HDL

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Reviews

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COMPUTER PRINCIPLES AND DESIGN IN VERILOG HDL

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John Wiley & Sons Inc. Hardback. Book Condition: new. BRAND NEW, Computer Principles and Design in Verilog HDL, Tsinghua University Press, Yamin Li, The best way to learn what is important in computer architecture and microprocessor design is to see the logic designed in detail and simulated directly both in an explained text with the chance to duplicate those simulations, or your own ideas, yourself. This text succeeds in this regard. This book aims to teach fundamental and more advanced level Computer Architecture and Microprocessor Design, using an industrially important Hardware Description Language (HDL) to illustrate each concept. Use of an HDL will allow the student to readily simulate and adjust the operation of each design, and thus build industrially relevant skills. It introduces the computer principles, computer design, and how to use Verilog HDL (Hardware Description Language) to implement the design. The contents include: - 1) computer fundamentals and performance evaluation; 2) digital circuits and their design in Verilog HDL; 3) computer arithmetic algorithms (addition, subtraction, multiplication, division, and square root, as well as the Wallace Tree for multiplication, Newton-Raphson algorithm and Goldschmidt algorithm for division and square root) and their implementations in Verilog HDL; 4) instruction set architecture, ALU design, and register file design in Verilog HDL; 5) single-cycle CPU, multiple-cycle CPU, pipelined CPU designs in Verilog HDL; 6) interrupts, exceptions, and precise interrupts/exceptions; 7) floating-point algorithms, FPU design, and pipelined CPU/FPU design in Verilog HDL; 8) multithreading CPU design in Verilog HDL; 9) memory, cache, and virtual memory management; 10) design of CPU/FPU with cache and TLB in Verilog HDL; 11) Multi-core CPU design in Verilog HDL; 12) UART (Universal Asynchronous Receiver Transmitter), PS/2 keyboard and mouse, VGA (Video Graphics Array), I2C series bus and PCI parallel bus interface design in Verilog HDL; and 13) high-performance computers...

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