Top-Down Digital VLSI Design

TOP-DOWN DIGITAL VLSI DESIGN

From Architectures to Gate-Level Circuits and FPGAs

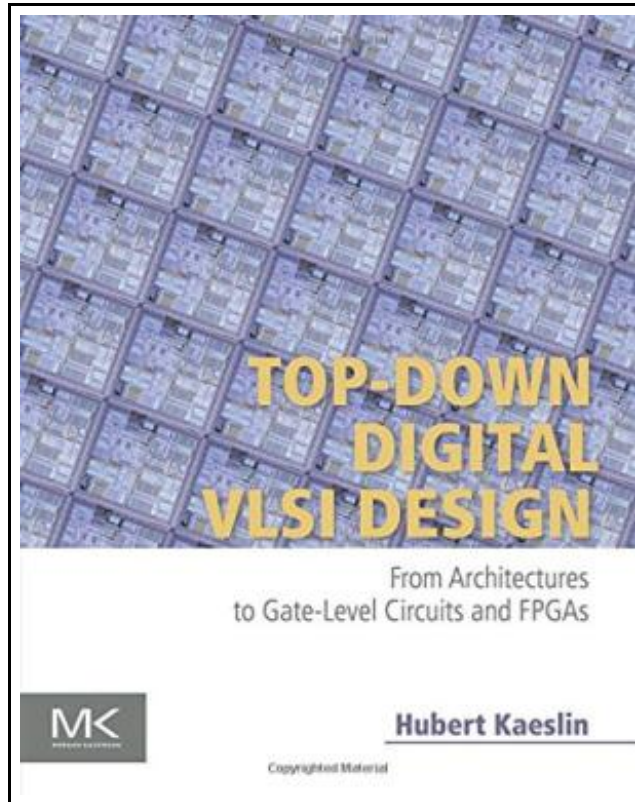Hubert Kaeslin

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