



# Gowin FOC Current Loop Control Light IP

## **Release Note**

RN770-1.0E, 03/16/2021

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### Revision History

Date	Version	Description
03/16/2021	1.0E	Initial version published.

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# 1 About This Guide

This release includes Gowin FOC Current Loop Control Light IP User Guide.

Gowin FOC Current Loop Control Light IP User Guide is available at [Gowin website](#).

# 2 Overview

Gowin FOC Current Loop Control Light IP can realize FOC current loop and control the motor with less resources. It can be used with the position loop and speed loop of MCU.

Gowin FOC Current Loop Control Light IP	
IP Core Application	
Delivered Doc.	
Design Files	Verilog (encrypted)
Test and Design Flow	
Synthesis Software	SynplifyPro/ GowinSynthesis
Application Software	Gowin Software

# 3 Documents

This software release includes Gowin FOC Current Loop Control Light IP documents, which are shown listed below:

Documents	Instructions
<a href="#">IPUG770</a> , Gowin FOC Current Loop Control Light IP User Guide.pdf	PDF
<a href="#">RN770</a> , Gowin FOC Current Loop Control Light Release Note.pdf	PDF

# 4 IP Support

Since the requirements of user design and the highest frequency are different, the reference design does not include FPGA specific location (.cst) and timing constraints (.sdc) documents, and you can determine the location and timing constraints on his own.

For the customized IP design and support, please contact Gowin sales and support line.

Email: [support@gowinsemi.com](mailto:support@gowinsemi.com)

Official Website: <http://www.gowinsemi.com>



