



Sergio Giménez Antón

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WORK EXPERIENCE

[15/09/2020 – Current] **Research engineer**

i2CAT Foundation

City: Barcelona

Country: Spain

Main activities and responsibilities:

- Implementing [RINA](#) (Recursive InterNetwork Architecture) stack from scratch based on high performance packet processing framework ([Netmap](#)).
- Working with and extending virtualization technologies such as QEMU/KVM, Docker and Kubernetes.
- Implemented an Intent Engine for verticals in Self-Organizing Networks.
- Design of the interconnectivity for the federated testbeds.

[14/02/2020 – 29/07/2020] **Research engineer**

Aristotle University

Address: Thessaloniki, Greece

City: Thessaloniki

Country: Greece

Main activities and responsibilities:

Working within the Wireless Communications Systems Group developing the diploma thesis: Development of a low-cost prototype to evaluate the performance of Transdermal Optical Links

[08/2019 – 13/01/2020] **Research Engineer Intern**

i2CAT Foundation i2cat.net

Address: Barcelona, Spain

City: Barcelona

Country: Spain

Main activities and responsibilities:

Working within the RINA architecture background developing a P4 implementation of the RINA data transfer and data transfer control elements.

[08/2013 – 01/2020] **Head Basketball Coach**

UE Claret

Address: Barcelona, Spain

City: Barcelona

Country: Spain

Main activities and responsibilities:

Basketball coach of different teams, gender and ages

[08/2018 – 13/01/2020] **Android Developer**

UPC - ETSETB Electrical Engineering Department <http://bpup.eu>

Address: Barcelona, Spain

City: Barcelona

Country: Spain

Main activities and responsibilities:

Working in a biomedical research project ("[Blood Pressure Under Pressure](#)") for preventing cardiovascular diseases in Africa.

EDUCATION AND TRAINING

[14/09/2020 – 19/06/2022] **Master's degree in Advanced Telecommunication Technologies**

UPC - ETSETB <https://matt.masters.upc.edu/>

Address: Campus Nord UPC 1-3, 08034, Barcelona, Spain

[14/02/2020 – 29/07/2020] **Diploma Thesis**

Aristotle University of Thessaloniki (AUTH)

Address: Thessaloniki, Greece

Level in EQF: EQF level 6

Main subject / occupational skills covered:

Subject: The subject of the current thesis will be the hardware development of the carotid tracking system for early detection of stenosis. This device will house several light-emitting-diodes and photo-detectors. The latter will collect data from the scattered light and transmit them to a smartphone.

[08/2015 – 07/2020] **Bachelor degree, Computer Systems Networking and Telecommunications**

UPC - ETSETB

Address: Barcelona, Spain

Level in EQF: EQF level 6

LANGUAGE SKILLS

Mother tongue(s): Spanish, Catalan

Other language(s):

English

LISTENING C1 READING C1 WRITING C1

SPOKEN PRODUCTION C1 SPOKEN INTERACTION C1

DIGITAL SKILLS

My Digital Skills

Linux System | RINA | netmap | P4 | SDN | Docker | QEMU/KVM | C | Python | Java

PUBLICATIONS

[2022]

RINA-based virtual networking solution for distributed VNFs: Prototype and Benchmarking

<https://ieeexplore.ieee.org/abstract/document/9815620>

2022 Joint European Conference on Networks and Communications & 6G Summit (EuCNC/6G Summit)

[2020]

A Proof of Concept implementation of a RINA interior router using P4-enabled software targets

<https://ieeexplore.ieee.org/document/9059486>

Reference: 2020 - IEEE; 2020 23rd Conference on Innovation in Clouds, Internet and Networks and Workshops

S Gimenez, E Grasa, S Bunch.

[2020] **A P4-Enabled RINA Interior Router for Software-Defined Data Centers**

<https://www.mdpi.com/2073-431X/9/3/70>

Reference: 2020 - MDPI; Computers 9 (3), 70

C Fernández, S Giménez, E Grasa, S Bunch

[2020]

Development of a low-cost prototype to evaluate the performance of Transdermal Optical Links

<https://upcommons.upc.edu/handle/2117/329137>

Reference: 2020 - AUTH & UPC

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