

DNX First Customer Shipment Checklist

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Revision History

<i>Revision</i>	<i>Date</i>	<i>Change Description</i>
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About This Document

This document provides a list of commands for checking DNX devices. Each command has a description.

Run these commands routinely during First Customer Shipment (FCS). For more details, see the Knowledge Management (KM) article [FCS Checklist for DNX Customers](#).

String of Commands

For quick execution use the strings of commands listed in **Table 1: Commands**.

Table 1: Commands

#	String of Commands
1.	<pre>diag cosq non; diag egr max; fabric reachability; fabric connectivity; fabric mesh_topology; cosq conn ingress; cosq conn egress; Fabric queues; Fabric reachability; Fabric connectivity; d IRR_DESTINATION_TABLE; d IRR_QUEUE_IS_OCB_COMMITTED; d IRE_TDM_CONFIG; d IDR_QUEUE_IS_DRAM_ELIGIBLE; d IDR_QUEUE_IS_OCB_ELIGIBLE; d IDR_DRAM_BUFFER_TYPE; d IQM_CRDTDIS; d IQM_GRSPRM; d IQM_OCBPRM; d IQM_PACK_MODE; d IQM_PG_TC_BITMAP; d IQM_PQDMS; d IQM_PQRED; d IQM_VS_QA_QSZ; d IQM_VS_QB_QSZ; d IQM_VS_QC_QSZ; IQM_VS_QD_QSZ; d IQM_VS_QE_QSZ; d IQM_VS_QF_QSZ; d IQM_CNG_QUE_SET; d IPST_CRVS; IPS_QPRI; d IPS_CRWDTH; d IPS_EMPTYQCRBAL; d IPS_SFTH; d IPS_QPM_1_NO_SYS_RED; d IPS_QPM_1_SYS_RED; d IPS_QPM_2_NO_SYS_RED; d IPS_QPM_2_SYS_RED; d IPS_CRBALTH; d IPS_FLWID; d IPS_QDESC; d IPS_QSZTH; d IPS_CR_BAL_TABLE; d IPT_IPT_0_DEST_PIPE_MAPPING_2_FDT; d IPT_IPT_1_DEST_PIPE_MAPPING_2_FDT; d IPT_PRIORITY_BITS_MAPPING_2_FDT; d IPT_TDM_BIT_MAPPING_2_FDT; d EQO_PCT; d EQO_PPCT; EQO_DWM; d EQO_DWM_8P; d EQO_PDCT_TABLE; d EQO_PDCMAX; d EQO_PQST_TABLE; d EQO_PQSMAX; d EQO_QDCT_TABLE; d EQO_QDCMAX; d EQO_FDCMAX; d EQO_QOST_TABLE; d EQO_QOSMAX; d EQO_FQSMAX; d EQO_TC_DP_MAP; d EQO_MAP_PS_TO_IFC; d EQO_MC_SP_TC_MAP; d EQO_EGRESS_SHAPER_CONFIGURATION; d EQO_PER_IFC_CFG; EQO_EPS_PRIO_MAP; d EQO_DSP_PTR_MAP; d SCH_FC_MAP_FCM; d SCH_PORT_ENABLE_PORTEN; d SCH_ONE_PORT_NIF_CONFIGURATION_OPNC; d SCH_PIR_SHAPERS_STATIC_TABLE_PSST; d SCH_CH_NIF_CALENDAR_CONFIGURATION_CNCC; d SCH_CH_NIF_RATES_CONFIGURATION_CNRC; d SCH_CIR_SHAPERS_STATIC_TABLE_CSST; d SCH_DEVICE_RATE_MEMORY_DRM; d SCH_SHARED_DEVICE_RATE_SHARED_DRM; d SCH_SLOW_FACTOR_MEMORY_SFM; d SCH_TOKEN_MEMORY_CONTROLLER_TMC; d SCH_FLOW_INSTALLED_MEMORY_FIM; d SCH_FLOW_STATUS_MEMORY_FSM; d SCH_DSP_2_PORT_MAP_DSPP; d FDT_IPT_MESH_MC; mRTP_RMHMT; set rcerror=off; tm -stoponerror +progress; g * debug; diag dump; diag pp last; diag pp rpi; diag pp pi; diag pp termi; diag pp ive; diag pp pkttm; diag pp dblif; diag pp fdt; diag pp trapsi; diag pp encap; diag pp fli; kbp kaps_show; kbp kaps_db_stats; diag pp dblem; diag pp mode; diag prge_info; diag prge_last; diag template all; diag pp vtt; diag pp vtt last=1; diag pp eve; diag pp ipv4_mc; stg show; diag pp flp; diag pp flp last=1; diag alloc all; diag dbal tif; diag dbal lp; diag pp cc; diag pp occ; diag pp cos; diag pp ECMP_load_balancing; diag pp fec all; l2 show; diag field res; diag pp klkp; diag pp dbglem; diag pp rif; diag pp edrop; ps; show c; diag oam ep; diag oam lu; diag oam prge; diag oam count oamp</pre>
2.	<pre>diag cosq print_flow_and_up is_voq=1/0 print_status=1 dest_id=flow_id/dest_id;</pre>



Note: Enter the correct values for **is_voq** and **dest_id**. See [row 9](#) in **Table 2: //FAP TM Diag Commands**

Traffic Management Diag Commands

This section lists the Traffic Management (TM) diag commands for Fabric Access Processor (FAP) and Fabric Element (FE) devices.

Table 2: //FAP TM Diag Commands

#	Command	Description
1	diag cosq non	Displays the currently non-empty queues in the IQM.
2	diag egr max	Displays the maximum occupancy of EGQ Queue Pairs.
3	fabric reachability %modid	Displays which links are used to reach the given Module ID.
4	fabric connectivity	Displays the connectivity map of all available fabric links.
5	fabric mesh_topology	Internal.
6	cosq conn ingress	Displays the configuration of the ingress TM.
7	cosq conn egress	Displays the configuration of the egress TM.
8	diag cosq qpairs egq map	Displays the queue-pair mapping of the EGQ.
9	diag cosq print_flow_and_up is_voq=1/0 print_status=1 dest_id=flow_id/dest_id	Displays the credit flow information for a given destination. <ul style="list-style-type: none"> If is_voq = 1, dest_id = [flow_id] If is_voq = 0, dest_id = [dest_id]

Table 3: //FE TM Diag Commands

#	Command	Description
1	fabric queues	Displays MAX(WM) queue status of the DCH/DCM/DCL blocks.
2	fabric reachability %modid	Displays which links are used to reach the given Module ID.
3	fabric connectivity	Displays the connectivity map of all available fabric links.

Dump Tables

This section lists the dump tables and their descriptions.

Table 4: Dump Commands

#	Table	Description
1	IRR_DESTINATION_TABLE	Mapping system port ID to VOQ.
2	IRR_QUEUE_IS_OCB_COMMITTED	Per queue, defines if it is committed to OCB resources.
3	IRE_TDM_CONFIG	TDM configuration for each TDM context.
4	IDR_QUEUE_IS_DRAM_ELIGIBLE	Per queue, display DRAM resources eligibility.
5	IDR_QUEUE_IS_OCB_ELIGIBLE	Per queue, display OCB resources eligibility.
6	IDR_DRAM_BUFFER_TYPE	Per traffic type, define which buffer type to use.
7	IQM_CRDTDIS	Credit Discount. Per credit class of a queue, displays a value that is discounted from the packet size on packet dequeue.
8	IQM_GRSPRM	Guaranteed Space Parameters (static). Contains the guaranteed space defined for each queue in bytes, buffers and BDBs. Queues below these thresholds accept packets regardless of WRED decisions.
9	IQM_OCBPRM	Eligible Queue Parameters (static). Defines the report to the IDR regarding queues eligibility to use OCB/DRAM buffers.
10	IQM_PACK_MODE	Packing Mode. Packing mode per destination ID. Each entry holds packing modes of 16 devices.
11	IQM_PG_TC_BITMAP	In-PP-Port to NifPort (static). Maps incoming PP-Port to VSQs.
12	IQM_PQDMS	Statistics Reports Definitions. Define the format and behavior of the statistics report presented on the statistics interface.
13	IQM_PQRED	Packet Queue RED Parameters. RED parameters per queue rate class and Drop-p.
14	IQM_VS_QA_QSZ	Instantaneous size memory. <ul style="list-style-type: none"> VSQs 0-3: MIXED VSQs 4-7: OCB-Only

Table 4: Dump Commands

#	Table	Description
15	IQM_VS_QB_QSZ	Instantaneous size memory. <ul style="list-style-type: none"> VSQs 8-39: MIXED VSQs 40-71: OCB-Only
16	IQM_VS_QC_QSZ	Instantaneous size memory. <ul style="list-style-type: none"> VSQs 72-135: MIXED VSQs 136-199: OCB-Only
17	IQM_VS_QD_QSZ	Instantaneous size memory. <ul style="list-style-type: none"> VSQs 200-455: MIXED VSQs 456-711: OCB-Only
18	IQM_VS_QE_QSZ	Instantaneous size memory. <ul style="list-style-type: none"> VSQs 712-839: MIXED VSQs 840-967: OCB-Only
19	IQM_VS_QF_QSZ	Instantaneous size memory. VSQs 968-1479: MIXED/OCB
20	IQM_CNG_QUE_SET	Congested Queue Set (Dynamic). The array of 8 values presents the 8 most congested queues and their queue size. The array can be set to monitor queues congestion based on buffers (Dbuffs or BDs) by asserting CngqOnBuffEn. Reading starts from offset 0 (freeze the 8 values, and end at offset 7, release the set for further updates). Notes: <ul style="list-style-type: none"> This is a status array During reading the IQM continues to monitor the queues congestion
21	IPST_CRVS	Each source device is associated with one of two possible CreditValues. Each bit in this bitmap determines if CreditValue0 or CreditValue1 is to be used. This table is enabled by CrValSelEnable register.
22	IPS_QPRI	The values in this table determine the following: <ul style="list-style-type: none"> The priority of the queue (high/low) Whether it is OCB-Only (low latency) This affects the DQCQ context selection for this Q-type. Accessing this table is according to Queue Type.

Table 4: Dump Commands

#	Table	Description
23	IPS_CRWDTH	<p>The values in this table hold thresholds for the credit watchdog. If a long time has passed since the last credit for an active queue was received, a Flow Status Message can be sent or the queue can be marked to be deleted.</p> <p>A deleted queue drops all the packets in the queue. If a packet arrives when the queue is in a delete state the new packet is dropped. Queues in delete state return to normal, when a new credit arrives for the queue or when the queue is emptied.</p> <p>If a new credit arrives and several of the packets in the queue were deleted, the other packets are sent out.</p> <p>The values are given in WD full cycle units. A value of 0x0 disables the relevant threshold.</p> <p>Access to this table is according to Queue Type.</p> <p>Queue type is defined per VOQ queue at IPS Queue Type Lookup Table</p>
24	IPS_EMPTYQCRBAL	<p>The values in this table relate to the credit balance of an empty queue and affect its latency and credit waste.</p> <p>Accessing this table is according to Queue Type.</p> <p>Note: Values given here are in two's complement.</p>
25	IPS_SFTH	<p>Slow-Factor (SF) Thresholds.</p> <p>SlowFactor is used when exact scheduling is desired.</p> <p>When Flow Status is SLOW, the SCH sends SLOW rate credits which ensure that only 1 credit is in flight. This allows the IPS to respond in time to stop the SCH and ensure that no excess credits accumulate in the IPS once the queue is emptied.</p> <p>The SlowFactor reflects the number of missing credits (Qsize-CrBal) and allows the SCH to use higher SLOW rates where more than 1 credit is in flight but not too many in flight credits exist, so the IPS is able to respond in time to stop the SCH without credits accumulating in the IPS.</p> <p>Thresholds represent (Qsize-CrBal) and are given in 256B resolution.</p> <ul style="list-style-type: none"> Value of 12'hfff disables the threshold. SfTh12 must be bigger than SfTh 01 and so on. SfTh10 must be smaller than SfTh 21 and so on. SfTh10 must be smaller than SfTh 01 and so on (hysteresys). <p>Access to this table is according to Queue Type.</p>
26	IPS_QPM_1_NO_SYS_RED	Queue Port Map 1 (under no system RED mode).
27	IPS_QPM_1_SYS_RED	Queue Port Map 1 (under system RED mode).
28	IPS_QPM_2_NO_SYS_RED	Queue Port Map 2 (under no system RED mode).
29	IPS_QPM_2_SYS_RED	Queue Port Map 2 (under system RED mode).

Table 4: Dump Commands

#	Table	Description
30	IPS_CRBALTH	<p>The values in this table affect the credit request state (CRS) according to the credit balance (or the difference between the credit balance and the queue size).</p> <p>Values for all thresholds are given in terms of mantissa and exponent. The value of the threshold is $(M \times 2^E)$ where $M=TH7:4$ and $E=TH3:0$. All values are in 1B resolution.</p> <p>Access to this table is according to Queue Type.</p>
31	IPS_FLWID	<p>Queue to egress base flow mapping.</p> <p>Every 4 queues are mapped to a single base flow and subflow mode. For more details see the InterdigitatedMode register.</p>
32	IPS_QDESC	<p>This memory holds most of the queue descriptor variables.</p> <p>Important:</p> <p>This table is for debug purposes. Use it with discretion as it can affect the IPS operation.</p>
33	IPS_QSZTH	<p>The values in this table affect the credit request state (CRS) according to the difference between the queue size and the credit balance.</p> <p>Values for all thresholds are given in terms of mantissa and exponent with a sign bit.</p> <p>The value of the threshold is $(M \times 2^E)$ where $M=TH9:4$ $E=TH3:0$ and TH10 is the sign bit.</p> <p>All values are in 1B resolution.</p> <p>Access to this table is according to Queue Type.</p>
34	IPS_CR_BAL_TABLE	<p>Holds most of the queue descriptor variables.</p> <p>Important:</p> <p>This table is for debug purposes. Use it with discretion as it can affect the IPS operation.</p>
35	IPT_IPT_0_DEST_PIPE_MAPPING_2_FDT	<p>This table maps the incoming index, build from various packet parameters, to the destination pipe send to the FDT.</p> <p>The fields building the input of the table are:</p> <ul style="list-style-type: none"> • iqm2ipt_hp, iqm2ipt_mc • ftmh2ipt_dp(2bits) • ftmh2ipt_trfcls(3bits) • is_pkt_tdm(q_sig). <p>The output is 2 bits of the destination context number (0,1,2), per each key (above).</p>

Table 4: Dump Commands

#	Table	Description
36	IPT_IPT_1_DEST_PIPE_MAPPING_2_FDT	<p>This table maps the incoming index, build from various packet parameters, to the destination pipe send to the FDT.</p> <p>The fields building the input of the table are:</p> <ul style="list-style-type: none"> • iqm2ipt_hp, iqm2ipt_mc • ftmh2ipt_dp(2bits) • ftmh2ipt_trfcls(3bits) • is_pkt_tdm(q_sig). <p>The output is 2 bits of the destination context number (0,1,2), per each key (above).</p>
37	IPT_PRIORITY_BITS_MAPPING_2_FDT	<p>This table maps the incoming index, build from various packet parameters to 2 priority bits send to the FDT.</p> <p>The fields building the input of the table are:</p> <ul style="list-style-type: none"> • iqm2ipt_hp, iqm2ipt_mc • ftmh2ipt_dp(2bits) • ftmh2ipt_trfcls(3bits) • is_pkt_tdm(q_sig)</html>
38	IPT_TDM_BIT_MAPPING_2_FDT	<p>This table maps the incoming index, build from various packet parameters to 1 bit TDM send to the FDT.</p> <p>The fields building the input of the table are:</p> <ul style="list-style-type: none"> • iqm2ipt_hp, iqm2ipt_mc • ftmh2ipt_dp(2bits) • ftmh2ipt_trfcls(3bits) • is_pkt_tdm(q_sig)</html>
39	EGQ_PCT	<p>Per Port Configuration Table.</p> <p>This table contains 256 entries.</p> <p>The address to this table is Q-Pair number.</p> <p>If SpecialFlowControl is set, then the address is as follows:</p> <ul style="list-style-type: none"> • Entries 0-7: Corresponding to OTM-Port 0 UC queues • Entries 8-15: Corresponding to OTM-Port 8 UC queues • Entries 16-23: Corresponding to OTM-Port 0 MC queues • Entries 24-31: Corresponding to OTM-Port 8 MC queues • Entries 32-255: Corresponding to Q-Pairs 32-255
40	EGQ_PPCT	<p>Per OTM-Port Configuration Table.</p> <p>Allows configuring port-related features for each one of the 256 ports.</p> <p>Each address corresponds to a port with identical fields definitions per entry.</p>

Table 4: Dump Commands

#	Table	Description
41	EGQ_DWM	<p>The table consist of 32 entries, one entry per Port Scheduler (group of 8 Q-Pairs).</p> <p>Defines the weights of the 8 traffic class groups (TCG) connected to this scheduler.</p> <p>Enables WFQ and SP relation between the 8 TCGs.</p> <ul style="list-style-type: none"> Weight of 0 for a class indicates that this class has SP over the other classes. When all classes have equal weights, simple RR is implied. Maximum weight difference is 255:1. The lower the rate the higher the bandwidth. Rate Calculation: TCG(i) rate = (WFQ Total Rate) / (TCG(i) weight * (1/Weight0 + 1/Weight1 + ... + 1/Weight7)).
42	EGQ_DWM_8P	<p>The table consists of 32 entries, one entry per Port Scheduler (group of 8 Q-Pairs).</p> <p>Defines the weight of the 8 traffic class groups (TCG) connected to this scheduler.</p> <p>Enables WFQ and SP relation between the 8 TCGs.</p> <ul style="list-style-type: none"> Weight of 0 for a class indicates that this class has SP over the other classes. When all have equal weights, this implies simple RR. Maximum weight difference is 255:1. The lower the rate the higher the bandwidth. Rate Calculation: TCG(i) rate = (WFQ Total Rate) / (TCG(i) weight * (1/Weight0 + 1/Weight1 + ... + 1/Weight7)).
43	EGQ_PDCT_TABLE	This memory defines per OTM-Port-Profile the thresholds related to packet descriptor tests in CGM.
44	EGQ_PDCMAX	Maximum unicast and multicast packet descriptors per OTM-Port Memory.
45	EGQ_PQST_TABLE	<p>Data buffers thresholds per Otm-Port-Profile table.</p> <p>This memory defines per OTM-Port-Profile the thresholds related to data buffers tests per OTM-Port in CGM.</p>
46	EGQ_PQSMAX	Maximum unicast and multicast DBs per OTM-Port Memory.
47	EGQ_QDCT_TABLE	<p>PDs thresholds per queue table.</p> <p>The index to this table is CGM-PORT-PROFILE(4) Egress-Tc(3).</p> <p>The result is a set of thresholds corresponding to per queue resources.</p>
48	EGQ_QDCMAX	Maximum packet descriptors per queue Memory.
49	EGQ_FDCMAX	Maximum packet descriptors per Interface.
50	EGQ_QQST_TABLE	Data buffers thresholds per queue table.

Table 4: Dump Commands

#	Table	Description
51	EGQ_QQSMAX	Maximum data buffers per queue Memory.
52	EGQ_FQSMAX	Maximum data buffers per interface Memory.
53	EGQ_TC_DP_MAP	<p>Lookup table used to map:</p> <ul style="list-style-type: none"> • Port-COS-Map-Profile(3) • Egress-MC(1) • SYS-TC(3) • SYS-DP(2) <p>To the following parameters:</p> <ul style="list-style-type: none"> • Egress-TC(3) • CGM-MC-DP(2) <p>The Egress-TC + Base-Queue-Num determines the final queue number the packet is en-queued to.</p>
54	EGQ_MAP_PS_TO_IFC	<p>Mapping for each egress interface the Port Schedulers (queue group of 8 Q-Pairs) belonging to it.</p> <p>The index to this table is the egress interface number according to the following encoding:</p> <ul style="list-style-type: none"> • 0-27 - NIF ports 0-27 • 28 - Cmic • 29 - OLP • 30 - OAM • 31 - RCY <p>The result is a 32 bits bitamp representing the Port Schedulers that are mapped to the corresponding interface.</p>
55	EGQ_MC_SP_TC_MAP	<p>RQP should drop MC packets based on data buffers consumption.</p> <p>The decision whether to drop a packet or forward it is resolved by the CGM block that passes the indication to the RQP.</p> <p>The RQP maps SYS-TC2:0SYS-DP1:0 to CGM-SP(1) CGM-SE(2) CGM-TC(3) and forwards them to the CGM.</p>
56	EGQ_EGRESS_SHAPER_CONFIGURATION	<p>Egress Shaper Configuration.</p> <ul style="list-style-type: none"> • Entries 0-31 refer to Shaper Calendars 0-31 • Entry 32 refers to Non-channelized Shaper Calendar Memory • Entry 33 refers to Q-Pairs Shaper Calendar Memory • Entry 34 refers to TCGs Shaper Calendar Memory
57	EGQ_PER_IFC_CFG	<p>Per EGQ Interface Configuration in FQP.</p> <p>The FQP (Transmit queues) block maintains a queue per interface (124 NIF ports Cmic OLP OAM and RCY).</p>

Table 4: Dump Commands

#	Table	Description
58	EGQ_EPS_PRIO_MAP	Mapping each priority (Q-pair) to TCG. This table contains 32 entries. One entry per Port Scheduler (a queue group of 8 Q-Pairs). Each queue group is configured by PsMode register as either: One Eight-Priority ETM-Port (8P-Port) Four Two-Priority ETM-Ports (2P-Port) Eight One-Priority ETM-Ports (1P-Port) This register is relevant only when the corresponding queue group is working in 8P-Port mode. In this mode, this register defines for each priority (Q-Pair) to which SP scheduler (TCG) it is mapped.</html>
59	EGQ_DSP_PTR_MAP	The PP-DSP parameter for unicast packet is received from the FTMH header. For the egress multicast copy the PP-DSP parameter is generated by the multicast replication machine. This table maps the PP-DSP field to the following parameters: <ul style="list-style-type: none"> • Out-TM-Port • Out-PP-Port • Destination-System-Port
60	SCH_FC_MAP_FCM	Maps each of the 128 EGQ interface FCs to the 128 scheduler interface FCs. If more than one EGQ interface FC is mapped to one scheduler interface FC, they are bit-wise ORed.
61	SCH_PORT_ENABLE_PORTEN	Enables HR scheduler to function as a Scheduler-Port.
62	SCH_ONE_PORT_NIF_CONFIGURATION_OPNC	
63	SCH_PIR_SHAPERS_STATIC_TABLE_PSST	Shaper define per each TC (HR). holds Quanta to add and max-burst per each HR.
64	SCH_CH_NIF_CALENDAR_CONFIGURATION_CNCC	Holds the length and weight of the active calendar. Note that the maximum valid value for calendars 31:4 is 255 (not 1023).
65	SCH_CH_NIF_RATES_CONFIGURATION_CNRC	
66	SCH_CIR_SHAPERS_STATIC_TABLE_CSST	Shaper define per each PG. holds Quanta to add and max-burst per each PG.
67	SCH_DEVICE_RATE_MEMORY_DRM	The selection of the current active rate is determined by the number of current active links (0 36) and the current status of fabric congestion as reflected in the RCI bucket level (0 7). The address is a concatenation of NumActiveLinks and RCILevel.
68	SCH_SHARED_DEVICE_RATE_SHARED_DRM	This memory defines the total BW of the two schedulers in case shared-DRM is used. $\text{RateGbps} = (\text{Credit_Size} * 8 * 128) / (\text{clk_cycle_in_nano} * \text{SharedDrm})$.
69	SCH_SLOW_FACTOR_MEMORY_SFM	Slow factor memory.

Table 4: Dump Commands

#	Table	Description
70	SCH_TOKEN_MEMORY_CONTROLLER_TMC	The token memory holds the number of tokens currently available for a sub-flow/flow/scheduler. Updating the initial token count is done by updating the MaxBurst and MaxBurstUpdate fields in the SHDS (consult API regarding update of initial values in this memory).
71	SCH_FLOW_INSTALLED_MEMORY_FIM	Each entry holds data of 16 flows.
72	SCH_FLOW_STATUS_MEMORY_FSM	This memory holds the status of flows/schedulers. Each entry contains 8 bits which states the status of 4 flows. For a flow the entry reflects the status of the ingress queue. For a scheduler the entry reflects whether it has a flow attached. A flow may be at one of 3 states: <ul style="list-style-type: none"> • OFF • ON-SLOW • ON-NORMAL
73	SCH_DSP_2_PORT_MAP_DSPP	Maps each of the 256 DSPs to each of the 256 HR schedulers.
74	FDT_IPT_MESH_MC	<ul style="list-style-type: none"> • In mesh mode: IPT MC replication. Address is bits 15:5 of MCID. • In fabric mode: Packing control and minimum number of links. The address is the destination of the device.
75	RTP_RMHT	

Program Processing (PP) Commands

This section lists the Program Processing commands and their descriptions.

Table 5: PP Commands

#	Command	Description
1	set rcerror=off	
2	tm -stoponerror +progress	
3	g * debug	Displays registers dump.
4	diag dump	Displays signals.
5	diag pp last	Displays last packet sent into the device.
6	diag pp rpi	Displays last packet received information.
7	diag pp pi	Displays parsing information.
8	diag pp termi	Displays termination information.
9	diag pp ive	Displays ingress VLAN information.
10	diag pp pkttm	Displays information processed by the TM blocks.
11	diag pp dblif	Displays the lookup keys and results from SEM DB.
12	diag pp fdt	Displays last packet forward decision trace.
13	diag pp trapsi	Displays whether the packet was trapped, and trap-code.
14	diag pp encap	Displays ingress encapsulation information.
15	diag pp fli	Displays forward block information.
16	kbp kaps_show	Displays the table contents of 0xData/valid_num_of_bits/max_num_of_bits format.
17	kbp kaps_db_stats	Displays kaps table stats.
18	diag pp dblem	Displays the lookup keys and result from LEM DB.
19	diag pp mode	Displays the configuration set by the mode_info_set API.
20	diag prge_info	Displays PRG programs.
21	diag prge_last	Displays the last program that was chosen by the egress programmable editor.
22	diag template all	Displays the template manager diagnostics.
23	diag pp vtt	<ul style="list-style-type: none"> [last=0] - displays the valid VTT programs [last=1] - displays the last invoked VTT program

Table 5: PP Commands

#	Command	Description
24	diag pp vtt last=1	Displays the last invoked VTT program.
25	diag pp eve	
26	diag pp ipv4_mc	Displays the MC routing table.
27	stg show	Displays a list of the List STGs.
28	diag pp flp	Displays the valid FLP programs.
29	diag pp flp last=1	Displays the last invoked FLP program.
30	diag alloc all	Displays allocation management
31	diag dbal tif	Displays database abstraction layer diagnostics.
32	diag dbal lp	Displays database abstraction layer diagnostics.
33	diag pp cc	
34	diag pp occ	Displays the occupation maps status.
35	diag pp cos	Displays CoS mappings.
36	diag pp ECMP_load_balancing	Displays the ECMP load balancing information.
37	diag pp fec all	Runs FEC creation performance tests.
38	l2 show	Display MAC table.
39	diag field res	Displays PMF resources.
40	diag pp klp	
41	diag pp dbglem	
42	diag pp rif	Displays last packet profile information.
43	diag pp edrop	
44	ps	Displays ports.
45	show c	Displays counters.

OAM Enabled

This section lists the lines that need to be uncommented when OAM is enabled.

Table 6: OAM Enabled

#	Command	Description
1	#diag oam ep	Displays current EPs on the device.
2	#diag oam lu	Displays OAM lookups.
3	#diag oam prge	Displays current OAMP programs on the device.
4	#diag oam count oamp	Displays OAMP counters.



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