

Tema - Lab 7 MBS

Capacitate: 64 MB

Adresare pe 8, 16 bit

Magistrala de date: 16 bit
de adrese: 30 linii

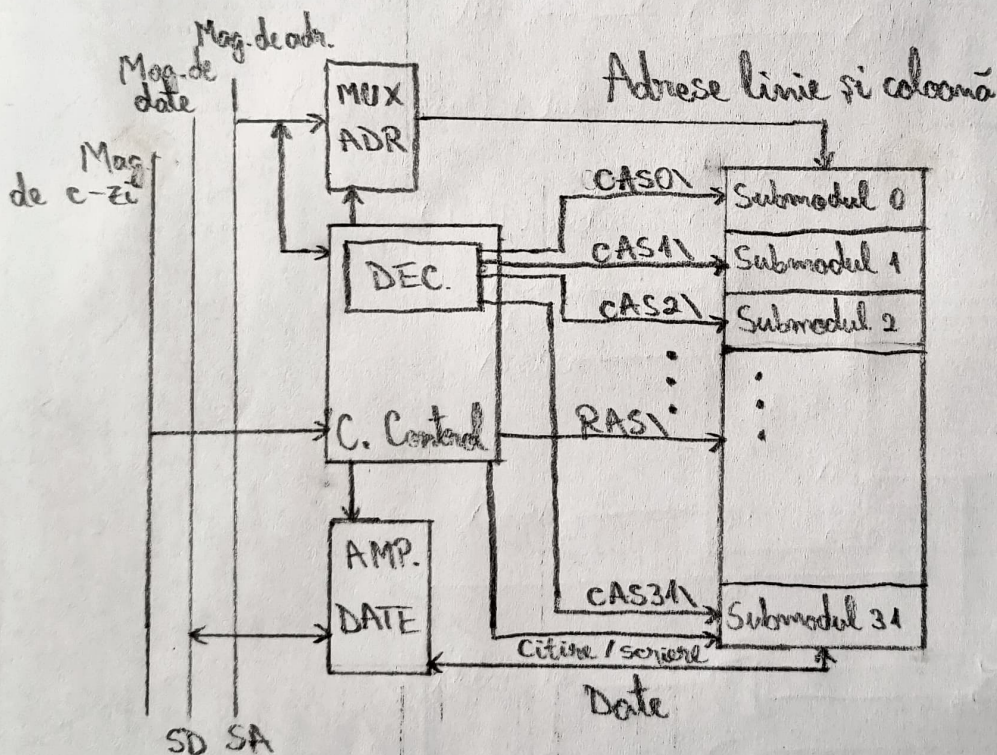
Adresa de start: B0000H

Circ. de memorie: $1M * 8 \text{ bit}$

A. Schema generală a modului de memorie:

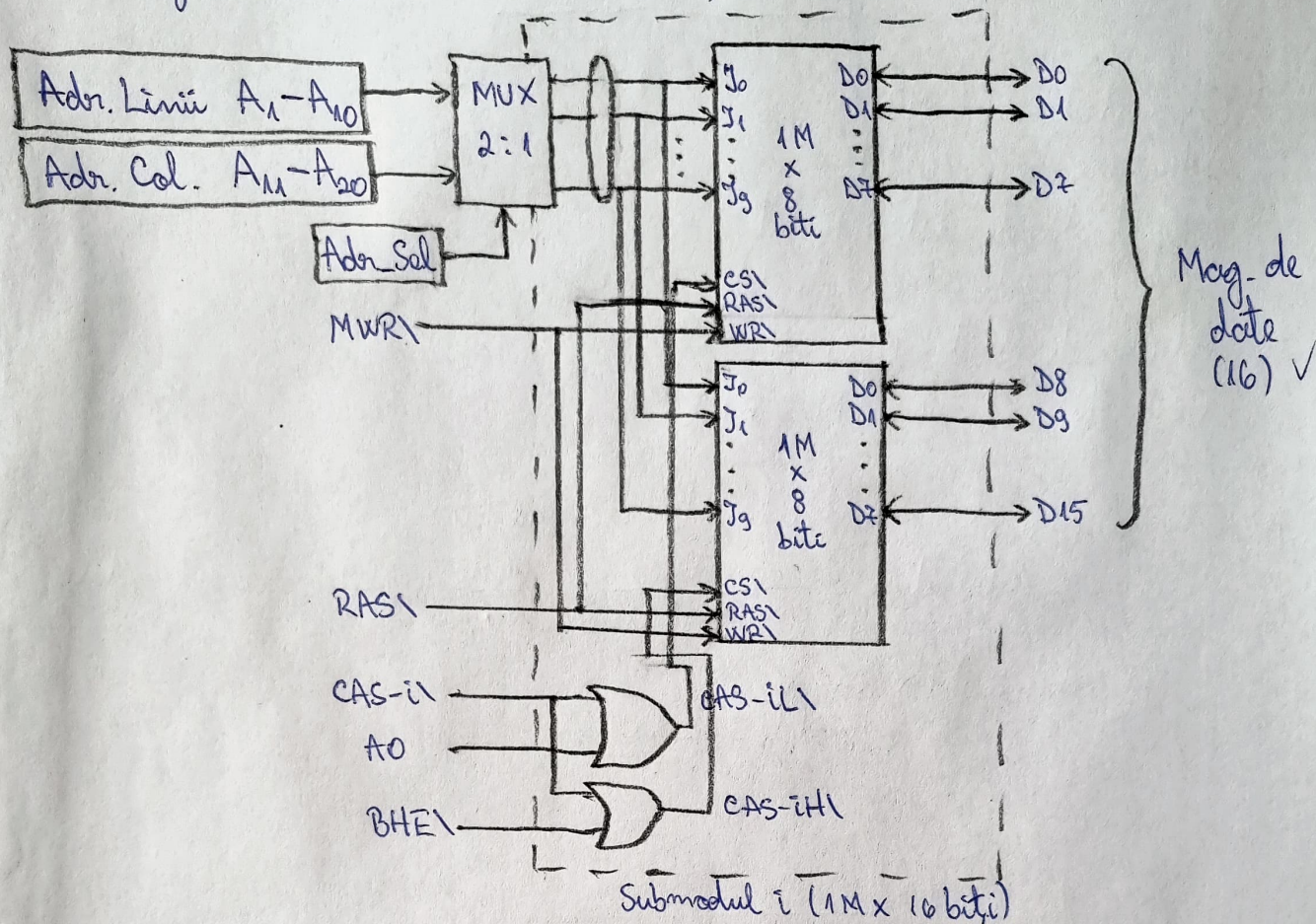
$$\text{nr. de submodule} = \frac{\text{capacitatea}}{\text{memorie} * \text{biti}} = \frac{64 \text{ MB}}{1M * 2B} = \frac{64}{2} = 32 \text{ submodule (0-31)}$$

pt. schemă: $1 \times \text{RAS}$
 $32 \text{ submodule} = 32 \times \text{CAS}$



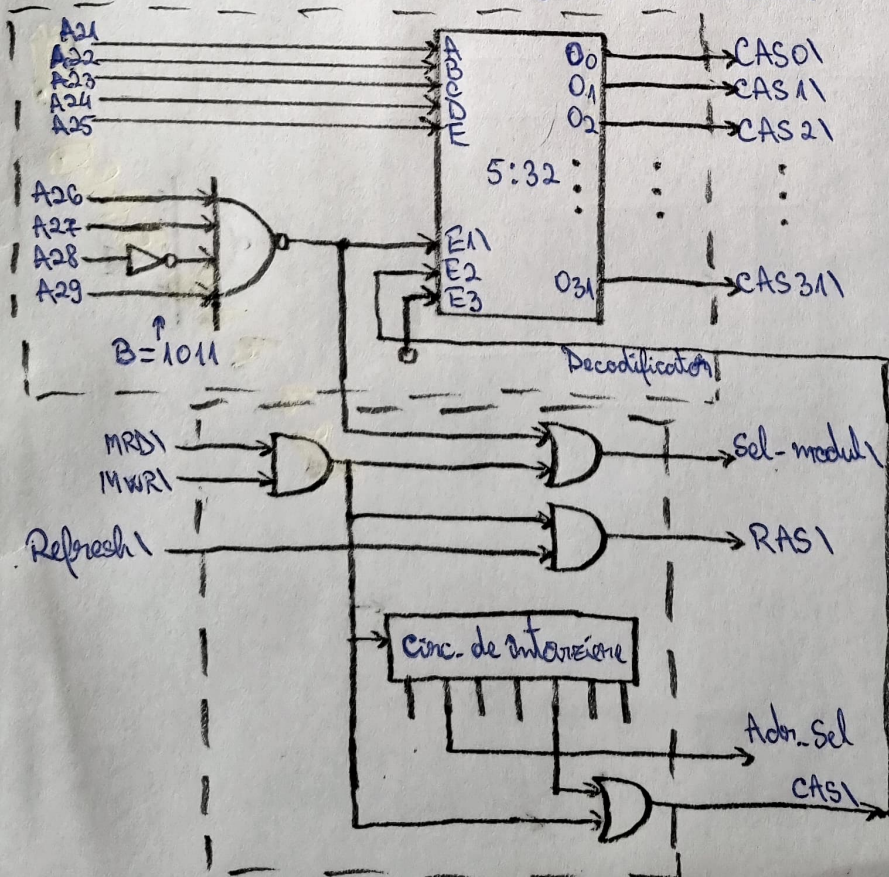
b) Schema unui submodul de memorie

Magistrala de adr.: 2^{20} ($M=20$) \rightarrow 20 adrese



c) Circuit de comandă și decodificare

nr. de in. = $2^5 = 32 \Rightarrow$ 5 intrări (A-E)



$29 + \frac{1}{30}$ linii adr.