


Areas \ Tasks	System Design Specifications (t1)	Hardware Implementation (t2)	Vertical Slice Integration (t3)
Integration	System Level Design	System Specs	System Integration
Data Source stage	Format/flow/bandwidth	Firmware Specs Hardware Testing	 <i>in stages</i>
Data Input stage	Format/flow/bandwidth	Firmware Specs Hardware Testing	
PRB (Pattern Recognition)	Format/flow/bandwidth	Firmware Specs Hardware Testing	
PRM (Mezzanine)	Format/flow/bandwidth	Firmware Specs Hardware Testing	
AM (or new approach)	Interface spec	Design/Testing	
Track Fitting	Interface spec	Design/Testing	
Simulation	System Level Simulation	Hardware Emulation	System Emulation