


Areas \ Tasks	System Design Specifications (t1)	Hardware Implementation (t2)	Vertical Slice Integration (t3)
Data Source stage	TK-DAQ CMS-UK (?)	?	 <i>in stages</i>
Data Input Board	TK-DAQ CMS-UK (?)	?	
PRB (Pattern Recognition)	Northwestern/FNAL /INFN	Northwestern/FNAL	
PRM (Mezzanine)	FNAL/INFN/CERN	FNAL/INFN/CERN	
AM (or new approach)	FNAL/INFN	FNAL/INFN	
Track Fitting	Lyon/INFN/Cornell/ FNAL	Lyon/INFN/Cornell/ FNAL	
Simulation	Lyon/FNAL/INFN/CERN/F lorida/Cornell/...	Group working on the hardware	