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## LIN 1 Transceiver

The schematic diagram illustrates the LIN 1 Transceiver circuit. It features two integrated circuits: U1 (ADuM121N) and U3 (6TJA1029T).

**U1 (ADuM121N) Pin Connections:**

- VOA (Pin 2):** Connected to LIN\_RXDD.
- VIB (Pin 3):** Connected to LIN\_TXDD.
- VIA (Pin 7):** Connected to RXD (Pin 1) of U3.
- VOB (Pin 6):** Connected to TXD (Pin 4) of U3.
- Power:** +3V3 and +5P are connected to pins 1 and 8. 100nF capacitors (C7) are connected from these pins to GND1.
- Ground:** Pins 4 and 5 are connected to GND1.

**U3 (6TJA1029T) Pin Connections:**

- RXD (Pin 1):** Connected to VIA (Pin 7) of U1.
- TXD (Pin 4):** Connected to VOB (Pin 6) of U1.
- SLP (Pin 2):** Connected to GND1.
- LIN (Pin 6):** Connected to the switch (SW?) and a 220pF capacitor (C7) to GND1.
- Power:** +5P is connected to pin 5. +VECUD is connected to pin 7. A 10k resistor (R7) is connected from +5P to pin 5.

**Switch (SW?) Details:**

- Label:** A6S-1104-H.
- Control:** A 1k resistor (R7) is connected to the switch's control terminal.
- Output:** The switch's output terminal is connected to the LIN\_1\_BUS.

# Connectors

PTSM 0.5/ 3-2.5-H SMD R44

CAN\_H\_FD\_1 1  
CAN\_L\_FD\_1 2  
3

GND1

J?

Molex C-Grid III 3 pins

CAN\_H\_FD\_1 1  
CAN\_L\_FD\_1 2  
3

GND1

J?

Optionnal 2.54mm header

PTSM 0.5/ 3-2.5-H SMD R44

+VEUCUD 1  
LIN\_1\_BUS 2  
3

GND1

J?

Molex C-Grid III 3 pins

+VEUCUD 1  
LIN\_1\_BUS 2  
3

GND1

J?

