

12345678

A

B

C

D

E

F

Board

Flipper Zero
CAN FD / HS Module

File: Board.kicad_sch

Flipper Zero CAN FD Module
Designed by Morike TRAORE
©SERMA Safety & Security

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Sheet: /
File: Flipper_Zero_CAN_Module.kicad_sch

Title:

Size: A3Date:KiCad E.D.A. kicad 7.0.6

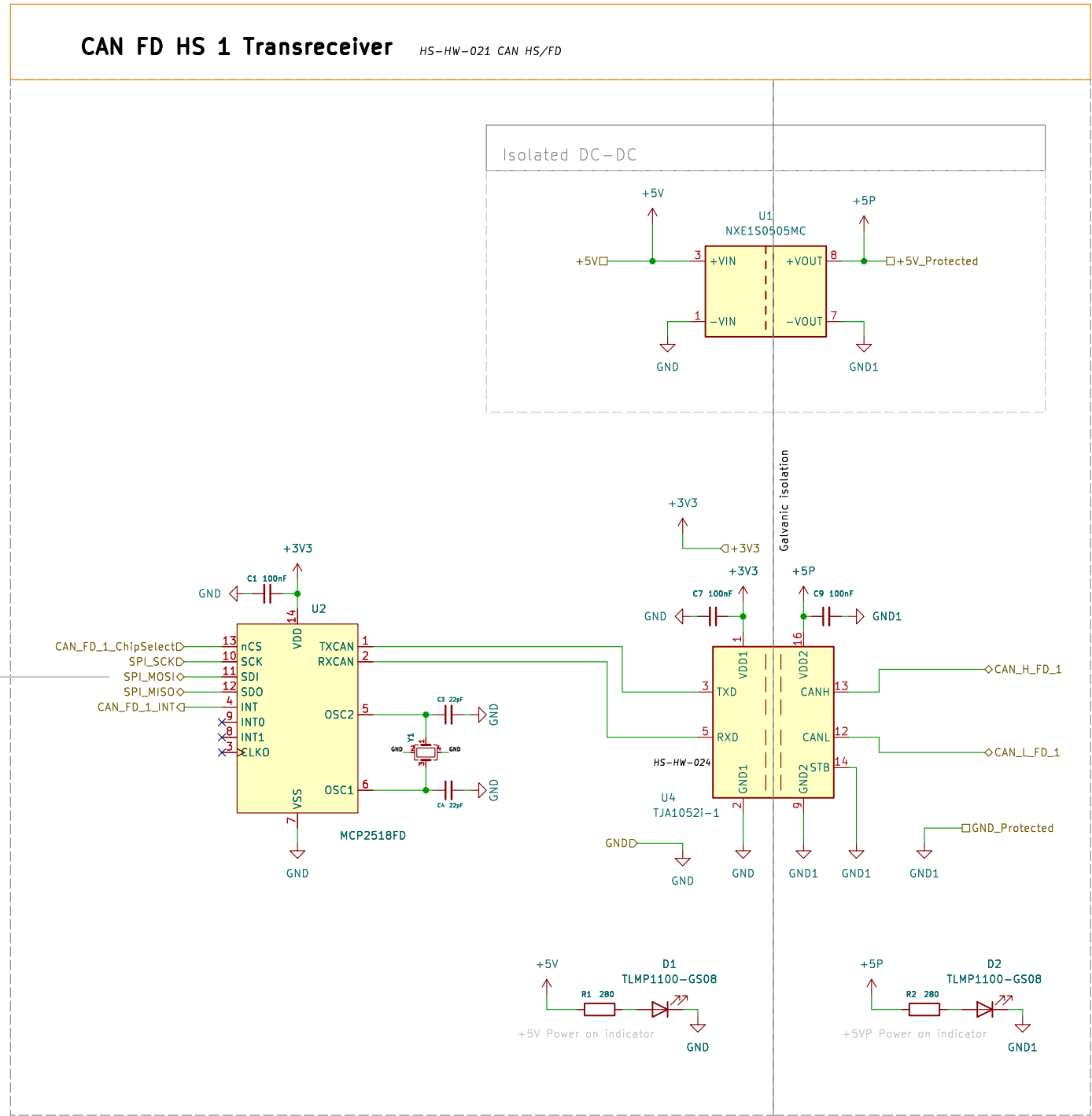
Rev:Id: 1/2

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Diagram illustrating the hardware connections for the Flipper Zero (U7) to the SoC (System on Chip).

The connections are as follows:

- 5V** (Pin 1) connects to **+5V**.
- A7** (Pin 2) connects to **+3V3**.
- A6** (Pin 3) connects to **SPI_MOSI**.
- A4** (Pin 4) connects to **CAN_FD_1_ChipSelect**.
- B3** (Pin 5) connects to **SPI_SCK**.
- B2** (Pin 6) connects to **CAN_FD_2_ChipSelect**.
- C3** (Pin 7) connects to **CAN_FD_1_INT**.
- GND** (Pin 8) connects to **GND**.
- 3V3** (Pin 9) connects to **3V3**.
- SWC** (Pin 10) connects to **GND**.
- GND** (Pin 11) connects to **GND**.
- SIO** (Pin 12) connects to **GND**.
- TX** (Pin 13) connects to **CAN_FD_2_INT**.
- RX** (Pin 14) connects to **GND**.
- C1** (Pin 15) connects to **GND**.
- C0** (Pin 16) connects to **GND**.
- 1W** (Pin 17) connects to **GND**.
- GND** (Pin 18) connects to **GND**.



Connectors

PTSM 0.5/ 3-2.5-H SMD R44

CAN_H_FD_1 1
CAN_L_FD_1 2
3

J6

GND1

Molex C-Grid III 3 pins

CAN_H_FD_1 1
CAN_L_FD_1 2
3

J1

GND1

Optional 2.54mm header

