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1 Introduction

I made this out of pure boredom and an active will to procrastinate school work. This is my first attempt at an ISA, don't expect good design, speed, or logical reasons for the way something is implemented.

All registers are 1 word wide, where a word is 16-bits. There is a single address space which ranges from `0x0000-0xFFFF`, the `PC` is set to `0x0` and the all the other registers contain an undefined value.

2 Registers

There are 8 16-bit registers, the most important being the `PC` program counter and the `SP` stack pointer. There is also an accumulator `AC` and index `ID` register, the accumulator stores the result of arithmetic and the index is used in loops. There are also 4 general purpose registers `R0-R3`.

Register	Location
PC	0x0
SP	0x1
AC	0x2
ID	0x3
R0	0x4
R1	0x5
R2	0x6
R3	0x7