

2.1.9 Pin Function Definition (FM33LC0xxN)

Pin Number				Pin Function	Descriptions
LQFP64 LQFP48 QFN32 TSSOP20					
1	47	2	18	NRST	Global reset pin
2	48	3	19	PA13	GPIO
				UART0_RX	UART Receive
				LPUART0_RX Low power	UART receive
				ADC_IN6	ADC Input Channels
3	1	4	20	PA14	GPIO
				UART0_TX	UART Send
				LPUART0_TX Low Power	UART Transmit
				ADC_IN7	ADC Input Channels
4	2	5	1	PA15	GPIO
				WKUP0	External wake-up pin
				SVS	External power supply detection
				ADC_IN10	ADC Input Channels
5	3			PA0	GPIO
				COM0	LCD driver COM port
				UART4_RX	UART Receive
6	4			PA1	GPIO
				COM1	LCD driver COM port
				UART4_TX	UART Send
7	5			PA2	GPIO
				COM2	LCD driver COM port
				UART0_RX	UART Receive
				LPUART0_RX Low power	UART receive
8	6			PA3	GPIO
				COM3	LCD driver COM port
				UART0_TX	UART Send
				LPUART0_TX Low Power	UART Transmit
9	7			PA4	GPIO
				COM4/SEG28 LCD driver	COM/SEG terminal
				GPT1_CH3 General timer	external channel
				COMP2_INN1 Comparator	input
10	8			PA5	GPIO
				COM5/SEG29 LCD driver	COM/SEG terminal
				GPT1_CH4 General timer	external channel
				COMP2_INN2 Comparator	input
11	9			PA6	GPIO
				COM6/SEG30 LCD driver	COM/SEG terminal
				OPA1_INN2	OPA Input Channels
12	10			PA7	GPIO
				COM7/SEG31	LCD driver COM/SEG terminal
				OPA1_INP2	OPA Input Channels

Pin Number				Pin Function	Descriptions
LQFP64	LQFP48	QFN32	TSSOP20		
13	11	6		PA8	GPIO
				SEG0	LCD driver SEG terminal
				LPT32_CH1	Low Power Timer External Channel
				COMP2_INP1	Comparator input
14	12	7		PA9	GPIO
				SEG1	LCD driver SEG terminal
				LPT32_CH2	Low Power Timer External Channel
				COMP2_INP2	Comparator input
15	13	8		PA10	GPIO
				WKUP1	External wake-up pin
				SEG2	LCD driver SEG terminal
				LPT32_ETR	Low power timer external trigger input
				COMP1_INN1	Comparator input
16				PA11	GPIO
				SCL	I2C Clock
17				PA12	GPIO
				SDA	I2C Data
18				PB0	GPIO
				ADC_EXSYNC	ADC external enable
19				PB1	GPIO
				ADC_EXSAMP	ADC external sampling control
20	14	9		PB2	GPIO
				WKUP2	External wake-up pin
				UART4_RX	UART Receive
				ATIM_CH1N	Advanced timer external channel
21	15	10		PB3	GPIO
				UART4_TX	UART Send
				ATIM_CH2N	Advanced Timer External Channel
22	16		2	PB4	GPIO
				SEG3	LCD driver SEG terminal
				ATIM_CH1	Advanced Timer External Channel
23	17			PB5	GPIO
				SEG4	LCD driver SEG terminal
				ATIM_CH2	Advanced Timer External Channel
24				PB6	GPIO
				SEG5	LCD driver SEG terminal
				ATIM_CH3	Advanced Timer External Channel
25				PB7	GPIO
				SEG6	LCD driver SEG terminal
				ATIM_CH4	Advanced Timer External Channel
26	18	11	3	PB8	GPIO
				SEG7	LCD driver SEG terminal
				SPI1_SS	SPI Chip Select
				ATIM_CH3N	Advanced Timer External Channel
27	19	12	4	PB9	GPIO
				SEG8	LCD driver SEG terminal

Pin Number				Pin Function	Descriptions
LQFP64	LQFP48	QFN32	TSSOP20		
				ANATST SPI1_SCK GPT0_ETR General purpose timer external trigger input	
					Simulation test channel
					SPI Clock
				PB10 SEG9 OPA1_INN1 SPI1_MISO GPT0_CH1 General timer external channel	GPT0_ETR General purpose timer external trigger input
28	20	13	5		GPIO
					LCD driver SEG terminal
					OPA Input
					SPI Data Line
				PB11 SEG10 OPA1_INP1 SPI1_ONE GPT0_CH2 General timer external channel	GPT0_CH1 General timer external channel
29	21	14	6		GPIO
					LCD driver SEG terminal
					OPA Input
					SPI Data Line
				PB12 WKUP3 FOUT1 ATIM_ETR	GPT0_CH2 General timer external channel
30	22		7		GPIO
					External wake-up pin
					Clock frequency output
				PB13 SEG11 OPA2_INN1 UART1_RX LPUART1_RX LPUART Receive	Advanced timer external trigger input
31	23	15			GPIO
					LCD driver SEG terminal
					OPA Input
					UART Receive
				PB14 SEG12 OPA2_INP1 UART1_TX LPUART1_TX	LPUART1_RX LPUART Receive
32	24	16			GPIO
					LCD driver SEG terminal
					OPA Input
					UART Send
				VSS VDD PC0 OPA2_INN2 GPT1_CH1 General purpose timer external channel	LPUART Send
33					Ground
34					Power supply
					GPIO
35	25				OPA Input
				PC1 OPA2_INP2 GPT1_CH2 General purpose timer external channel	GPT1_CH1 General purpose timer external channel
36	26				GPIO
					OPA Input
					GPT1_CH2 General purpose timer external channel
				PC2 XTHIN UART1_RX LPUART1_RX Low power	PC2
37	27	17			GPIO
					High frequency crystal input
					UART Receive
				UART1_TX LPUART1_TX Low Power PC3 XTHOUT	LPUART1_RX Low power
					UART Transmit
38	28	18			GPIO
					High frequency crystal output
				UART1_TX LPUART1_TX Low Power PC4	UART Send
					LPUART1_TX Low Power
39	29	19			UART Transmit
				SEG13 OPA1_OUT	PC4
					LCD driver SEG terminal
					OPA Output

Pin Number				Pin Function	Descriptions
LQFP64	LQFP48	QFN32	TS\$OP20		
				UART5_RX	UART Receive
40	30	20		PC5	GPIO
				SEG14	LCD driver SEG terminal
				OPA2_OUT	OPA Output
				UART5_TX	UART Send
				PC6	GPIO
41	31		8	WKUP4	External wake-up pin
				SEG15	LCD driver SEG terminal
				GPT1_ETR	General purpose timer external trigger input
				ADC_IN11	ADC Input Channels
				PC7	GPIO
42	32			SEG16	LCD driver SEG terminal
				SPI2_SS_N	SPI Chip Select
				ADC_IN8	ADC Input Channels
				PC8	GPIO
43	33	21		SEG17	LCD driver SEG terminal
				SPI2_SCK	SPI Clock
				ADC_IN9	ADC Input Channels
				PC9	GPIO
44	34	22		SEG18	LCD driver SEG terminal
				SPI2_MISO	SPI Data Line
				ADC_IN0	ADC Input Channels
				PC10	GPIO
45	35	23		WKUP5	External wake-up pin
				SEG19	LCD driver SEG terminal
				SPI2_MOSI	SPI Data Line
				ADC_IN1	ADC Input Channels
				PC11	GPIO
46				SEG20	LCD driver SEG terminal
				U7816CLK	7816 Interface Clock
				GPT0_CH3	General timer external channel
				PC12	GPIO
47				SEG21	LCD driver SEG terminal
				U7816I/Q816 interface data	
				GPT0_CH4	General timer external channel
				PD9	GPIO
48	36	24	11	XT32KO	32768Hz crystal oscillator output pin
				PD10	GPIO
49	37	25	10	XT32KI	32768Hz crystal input pin
				VDD15	LDO output, external 100nF capacitor to ground
50	38	26	9	VSS	land
51	39	27	12	VDD	power supply
53				VSSA	Analog Ground
54				VDDA	Analog Power Supply
55	41	29		PD11	GPIO
				WKUP6	External wake-up pin

Pin Number				Pin Function	Descriptions
LQFP64	LQFP48	QFN32	TSSOP20		
				FOUT0	Clock frequency output
				ATIM_BRK1 Advanced timer brake input	
				ADC_IN2	ADC Input Channels
56	42	30	14	PD0	GPIO
				SEG22	LCD driver SEG terminal
				UART5_RX	UART Receive
				ADC_IN3	ADC Input Channels
57	43	31	15	PD1	GPIO
				SEG23	LCD driver SEG terminal
				UART5_TX	UART Send
				ADC_IN4	ADC Input Channels
58				PD2	GPIO
				SEG24	LCD driver SEG terminal
				SPI1_SSN	SPI Chip Select
				ADC_IN5	ADC Input Channels
59				PD3	GPIO
				SEG25	LCD driver SEG terminal
				SPI1_SCK	SPI Clock
60				PD4	GPIO
				SEG26	LCD driver SEG terminal
				SPI1_MISO	SPI Data
				COMP1_INP1 Comparator	input
61				PD5	GPIO
				SEG27	LCD driver SEG terminal
				SPI1_ONE	SPI Data
				COMP1_INP2 Comparator	input
62	44		16	PD6	GPIO
				WKUP7	External wake-up pin
				ANATST Analog Test Channel	
				ATIM_BRK2 Advanced timer	brake input
63	45	32	17	PD7	GPIO
				SWCLK	SWD interface clock
64	46	1		PD8	GPIO
				SWIO	SWD Interface Data

Table 2-2 FM33LC0xxN pin list