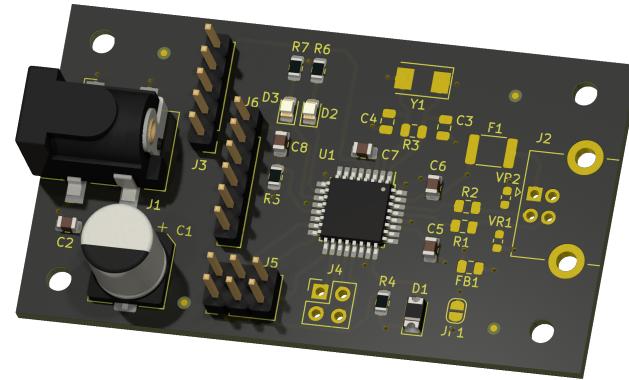


# 0 Assembly Document

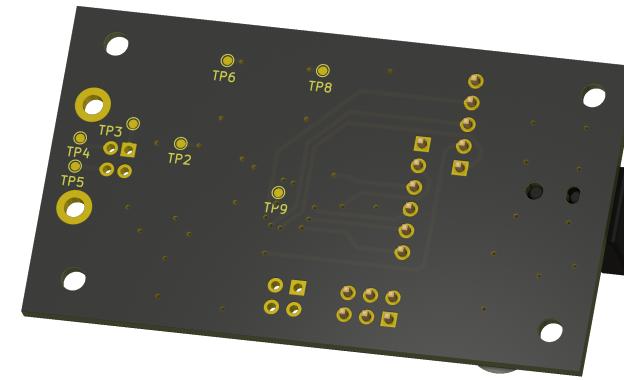
A

**Top View**



B

**Bottom View**



C

		Comments:	Company: INTI	Variant: PRELIMINARY	Git Hash: d5db5df
		Board Name: <b>0</b>	Project Name: <b>Test KDT</b>		
		Sheet Title: Top/Bottom View	File Name: KDT_Hierarchical_KiBot.kicad_pcb	Designer: SET	Date: 2024-04-13 Revision: + (Unreleased)
		Sheet Path:	Reviewer:	Size: <b>A4</b>	Sheet: <b>1</b> of <b>3</b>

# 0 Assembly Document

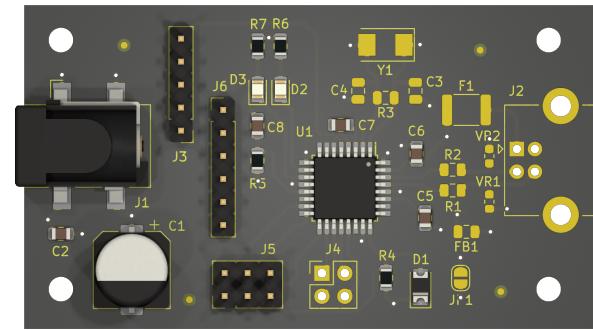
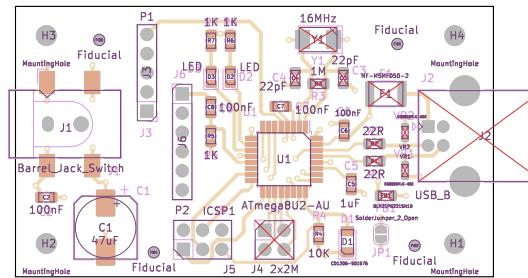
## Top Assembly (Scale 1:1)

### Components

Type	Front Side	Back Side	Total
THT	3	0	3
SMD	15	0	15
Total	18	0	18

#### ASSEMBLY NOTES (UNLESS OTHERWISE SPECIFIED)

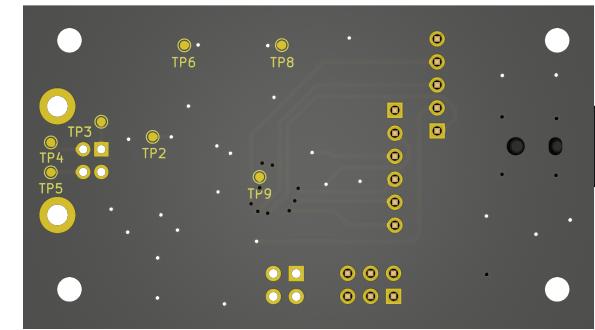
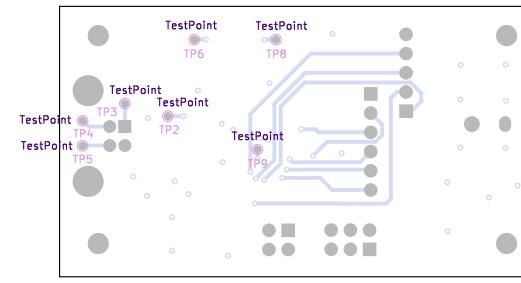
- DO NOT POPULATE PARTS ARE MARKED WITH A RED CROSS.
- DO NOT POPULATE PARTS ARE NOT PRESENT IN THE BOM.
- IF CONFLICTING INFORMATION IS FOUND BETWEEN THE ASSEMBLY FILE AND BOM, BOM SHOULD BE USED AS THE MAIN SOURCE.
- CONFORMAL COATING IS NOT REQUIRED.
- DOT IDENTIFIES PIN #1 LOCATION AND DEVICE ORIENTATION WHEN VIEWED FROM THE TOP.



	Comments:  INTI	Company:  INTI	Variant:  PRELIMINARY	Git Hash:  d5db5df
	Board Name:  0	Project Name:  Test KDT		
	Sheet Title:  Top Assembly (Scale 1:1)	File Name:  KDT_Hierarchical_KiBot.kicad_pcb	Designer:  SET	Date:  2024-04-13
	Sheet Path:  	Reviewer:  	Size:  A4	Sheet:  2 of 3

# 0 Assembly Document

**Bottom Assembly (Scale 1:1)**



	Comments:	Company: INTI	Variant: PRELIMINARY	Git Hash: d5db5df
	Board Name: <b>0</b>		Project Name: <b>Test KDT</b>	
	Sheet Title: Bottom Assembly (Scale 1:1)	File Name: KDT_Hierarchical_KiBot.kicad_pcb	Designer: SET	Date: 2024-04-13 Revision: + (Unreleased)
	Sheet Path:		Reviewer:	Size: <b>A4</b> Sheet: <b>3</b> of 3