

0

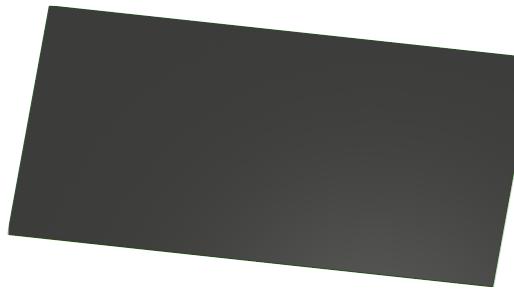
Variant: DRAFT

2025-12-10

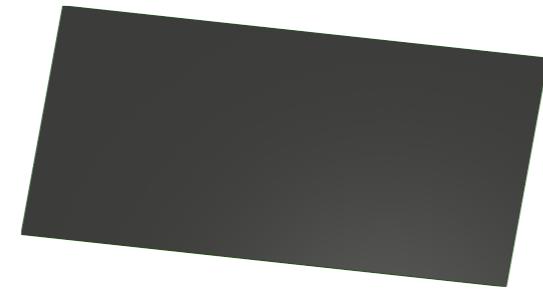
Rev + (Unreleased)

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TOP VIEW



BOTTOM VIEW



NOTES

Comment

Not fitted components are marked as

DRAFT - Very early stage of schematic, ignore details.

PRELIMINARY - Close to final schematic.

CHECKED - There shouldn't be any mistakes. Contact the engineer if you find any.

RELEASED - A board with this schematic has been sent to production.

Date: 10-Dec-2025

DESIGN CONSIDERATIONS

DESIGN NOTE:
Example text for informational design notes.

DESIGN NOTE:
Example text for debug notes.

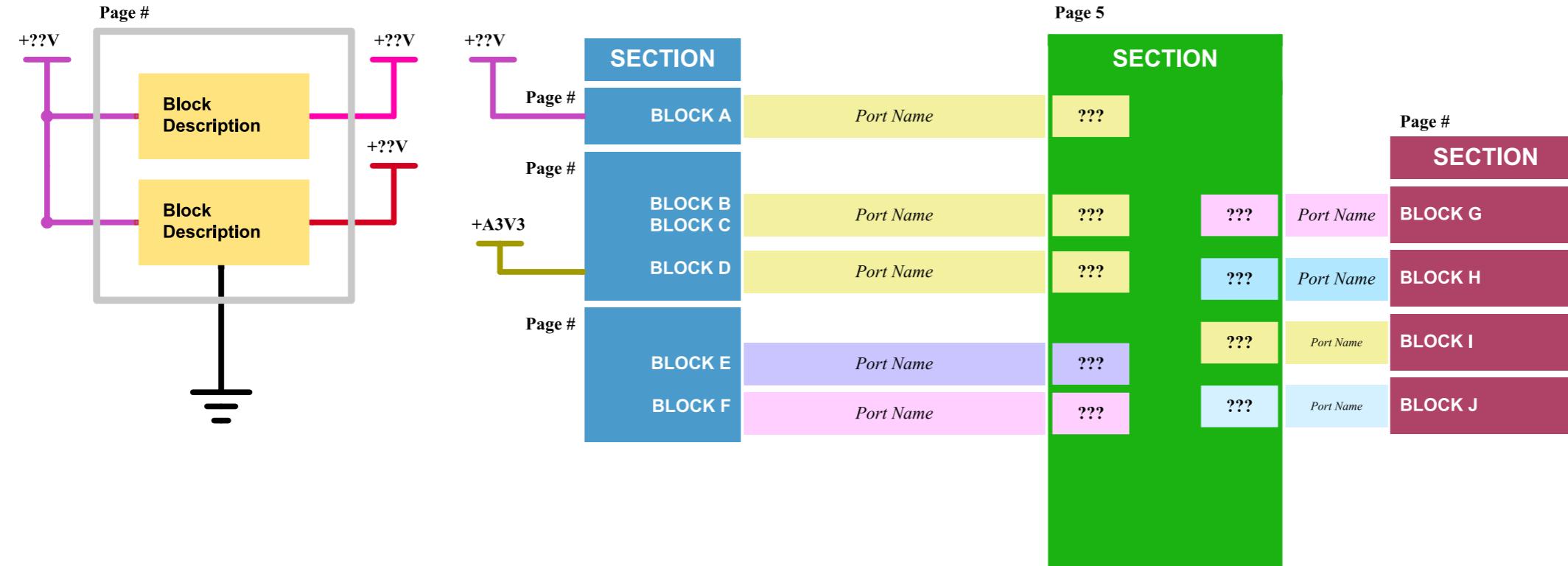
DESIGN NOTE:
Example text for cautionary design notes.

DESIGN NOTE:
Example text for critical design notes.

LAYOUT NOTE:
Example text for critical layout guidelines.

	Comments:	Company:		Variant: DRAFT	Git Hash: c440c30
		INTI			
	Board Name:	Project Name:		Test KDT	
		0			
	Sheet Title:	File Name:	KDT_Hierarchical_KiBot.kicad_sch	Designer:	Date: 2025-01-12 Revision: + (Unreleased)
	Sheet Path:	/		Reviewer:	Size: A3 Sheet: 1 of 7

[2] Block Diagram

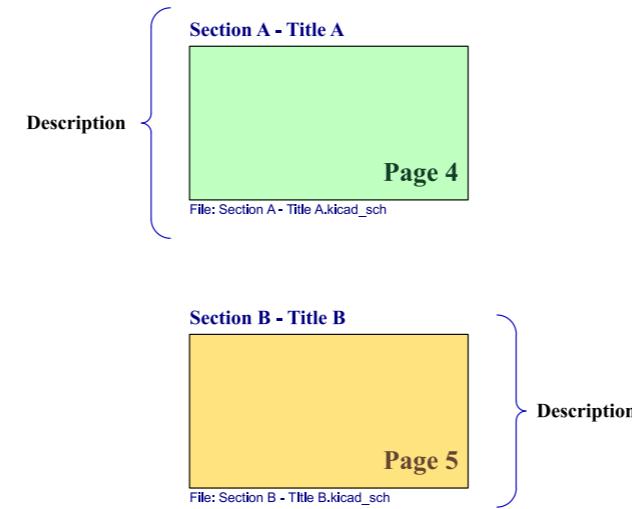


Target specifications:

Input voltage:	?? - ?? V
Spec 2	??
Spec 3	??
Spec 4	??

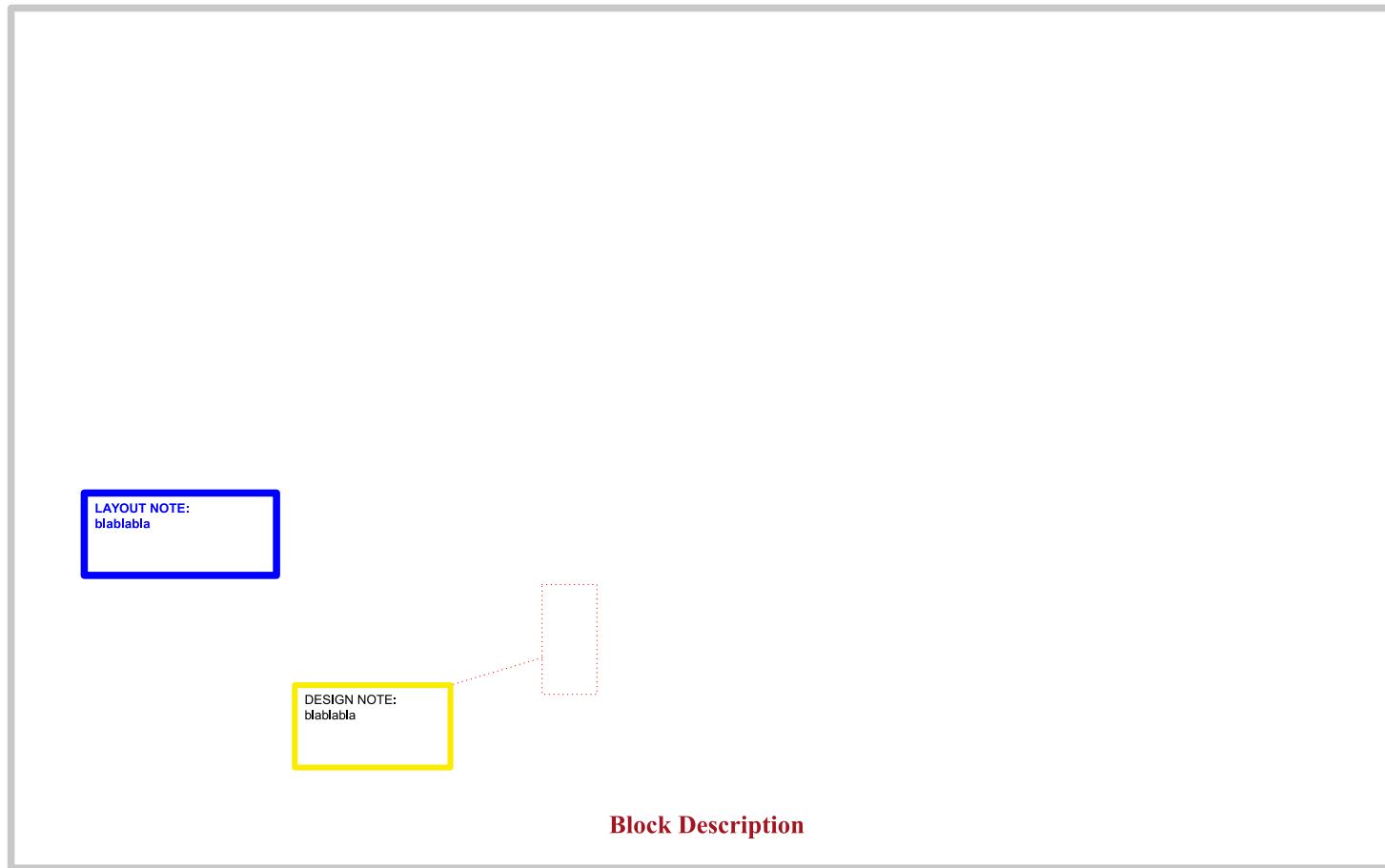
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		Board Name: 0	Project Name: Test KDT	
	Sheet Title: Block Diagram	File Name: Block Diagram.kicad_sch	Designer: SET	Date: 2025-01-12 + (Unreleased)
	Sheet Path: /Block Diagram/	Reviewer:	Size: A3	Sheet: 2 of 7

[3] Project Architecture



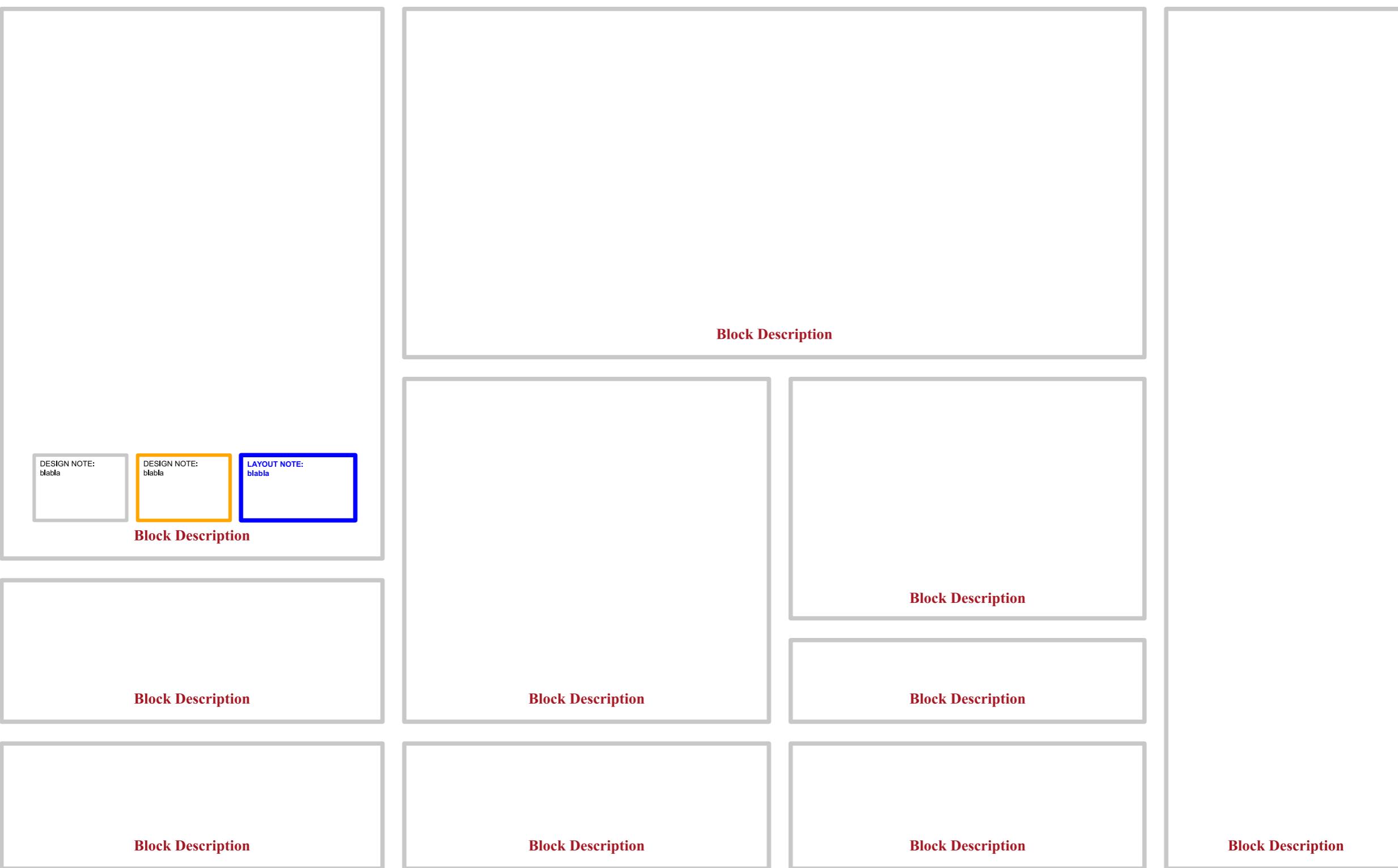
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	Sheet Title: Project Architecture	File Name: Project Architecture.kicad_sch	Designer: SET	Date: 2025-01-12 + (Unreleased)
	Sheet Path: /Project Architecture/		Reviewer:	Size: A3 Sheet: 3 of 7

[4] Sheet Title A



		Comments: AN5346 STM32G474 Datasheet p.81 J. Pieper ADC investigation	Company: INTI	Variant: DRAFT	Git Hash: c440c30
		Board Name: 0		Project Name: Test KDT	
		Sheet Title: Sheet Title A	File Name: Section A - Title A.kicad_sch	Designer: SET	Date: 2025-01-12 Revision: + (Unreleased)
		Sheet Path: /Project Architecture/Section A - Title A/	Reviewer:	Size: A4	Sheet: 4 of 7

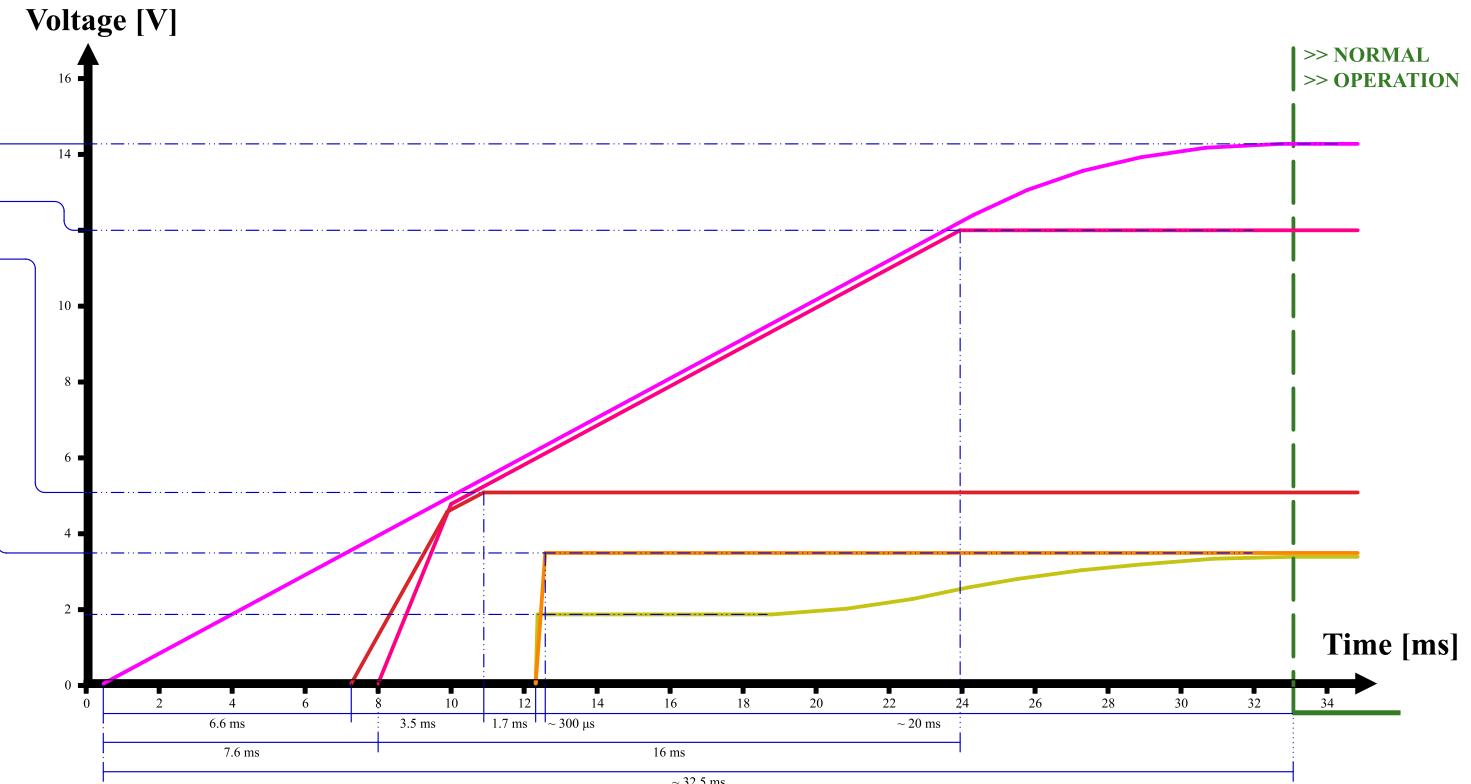
[5] Sheet Title B



	Comments: References: Flexible I/O worked examples Flexible I/O source configuration	Company: INTI	Variant: DRAFT	Git Hash: c440c30
	Board Name: 0		Project Name: Test KDT	
	Sheet Title: Sheet Title B	File Name: Section B - Title B.kicad_sch	Designer: SET	Date: 2025-01-12 + (Unreleased)
	Sheet Path: /Project Architecture/Section B - Title B/	Reviewer:	Size: A3	Sheet: 5 of 7

[6] Power - Sequencing

NAME	SOURCE	LEVEL
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%



		Comments: Sheet Title: Power - Sequencing Sheet Path: /Power - Sequencing/	Company: INTI		Variant: DRAFT	Git Hash: c440c30
			Board Name: 0			
			Sheet Title: Power - Sequencing	File Name: Power - Sequencing.kicad_sch	Designer: SET	Date: 2025-01-12
			Reviewer: /Power - Sequencing/		Size: A4	Revision: + (Unreleased)
1	2	3	4	5	6	D

[7] Revision History

	Comments:	Company: INTI	Variant: DRAFT	Git Hash: c440c30
		Board Name: 0	Project Name: Test KDT	
	Sheet Title: Revision History	File Name: Revision History.kicad_sch	Designer: SET	Date: 2025-01-12 Revision: + (Unreleased)
	Sheet Path: /Revision History/		Reviewer:	Size: A4 7 of 7