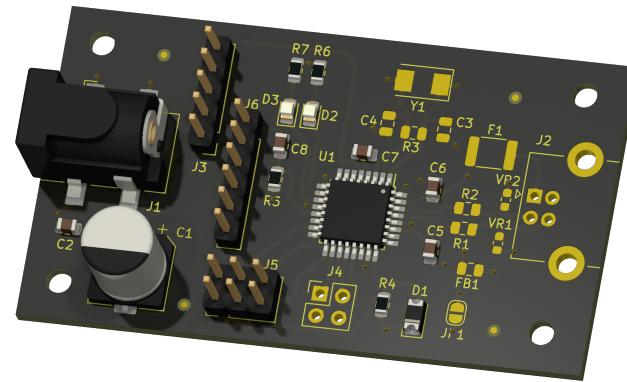


# Test KDT example Assembly Document

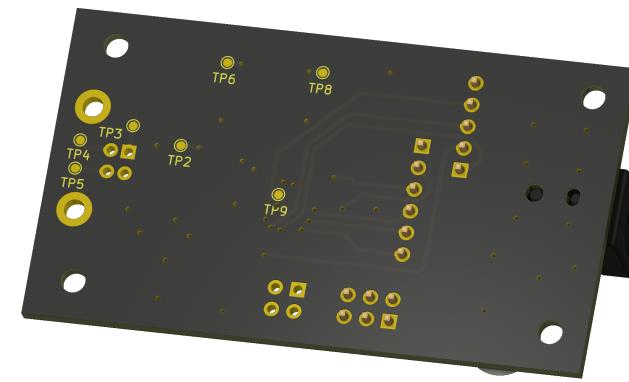
A

**Top View**



B

**Bottom View**



C

D

|  |  |  |                      |   |
|--|--|--|----------------------|---|
|  | Comments:                              | Company:<br>INTI                               | Variant:<br>RELEASED | Git Hash:<br>67e13c0                              |
|  | Board Name:<br><b>Test KDT example</b> |  |                      | Project Name:<br><b>Test KDT</b>                  |
|  | Sheet Title:<br>Top/Bottom View        | File Name:<br>KDT_Hierarchical_KiBot.kicad_pcb | Designer:<br>SET     | Date:<br>2024-04-13      Revision:<br>0.1.7       |
|  | Sheet Path:                            |  | Reviewer:            | Size:<br><b>A4</b> Sheet:<br><b>1</b> of <b>3</b> |

# Test KDT example Assembly Document

A

A

## Components

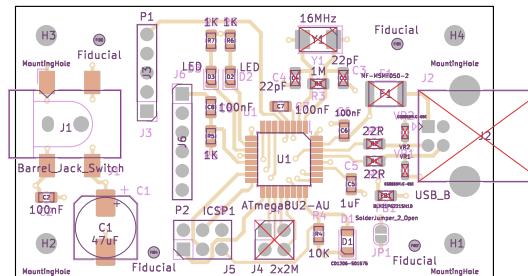
| Type         | Front Side | Back Side | Total     |
|--------------|------------|-----------|-----------|
| THT          | 3          | 0         | 3         |
| SMD          | 15         | 0         | 15        |
| <b>Total</b> | <b>18</b>  | <b>0</b>  | <b>18</b> |

## ASSEMBLY NOTES (UNLESS OTHERWISE SPECIFIED)

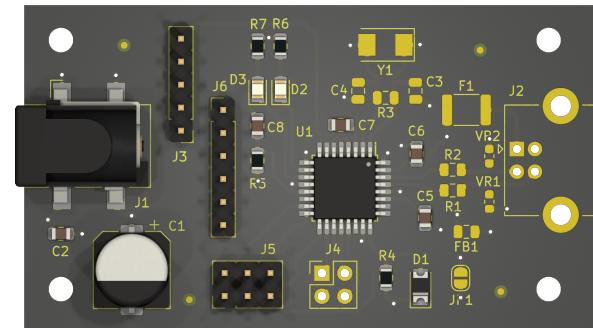
- 1) DO NOT POPULATE PARTS ARE MARKED WITH A RED CROSS.
  - 2) DO NOT POPULATE PARTS ARE NOT PRESENT IN THE BOM.
  - 3) IF CONFLICTING INFORMATION IS FOUND BETWEEN THE ASSEMBLY FILE AND BOM, BOM SHOULD BE USED AS THE MAIN SOURCE.
  - 4) CONFORMAL COATING IS NOT REQUIRED.
  - 5) DOT IDENTIFIES PIN #1 LOCATION AND DEVICE ORIENTATION WHEN VIEWED FROM THE TOP.

B

B



## Top Assembly (Scale 1:1)



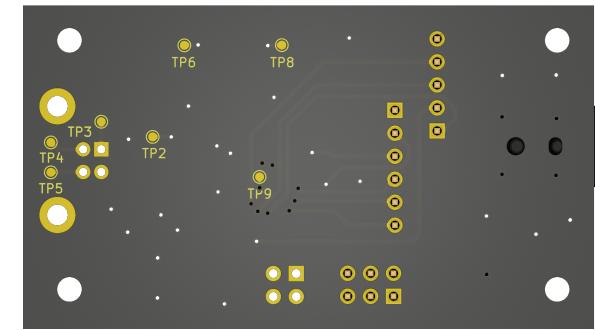
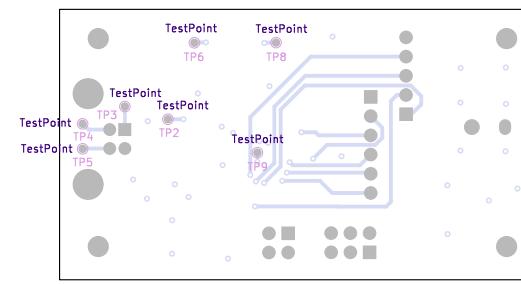
C

C

|  |  |  |                  |   |
|--|--|--|------------------|---|
|  | Comments:                                | Company:<br><b>INTI</b>                        | Variant:         | Git Hash:                               |
|  |  | Board Name:<br><b>Test KDT example</b>         | Project Name:    | <b>Test KDT</b>                         |
|  | Sheet Title:<br>Top Assembly (Scale 1:1) | File Name:<br>KDT_Hierarchical_KiBot.kicad_pcb | Designer:<br>SET | Date: 2024-04-13<br>Revision: 0.1.7     |
|  | Sheet Path:                              |  | Reviewer:        | Size: <b>A4</b><br><b>2</b> of <b>3</b> |

# Test KDT example Assembly Document

**Bottom Assembly (Scale 1:1)**



|  |   |  |                      |   |
|--|---|--|----------------------|---|
|  | Comments:                                   | Company:<br>INTI                               | Variant:<br>RELEASED | Git Hash:<br>67e13c0                        |
|  | Board Name:<br><b>Test KDT example</b>      |  |                      | Project Name:<br><b>Test KDT</b>            |
|  | Sheet Title:<br>Bottom Assembly (Scale 1:1) | File Name:<br>KDT_Hierarchical_KiBot.kicad_pcb | Designer:<br>SET     | Date:<br>2024-04-13      Revision:<br>0.1.7 |
|  | Sheet Path:                                 |  | Reviewer:            | Size:<br><b>A4</b> Sheet:<br><b>3</b> of 3  |