

0

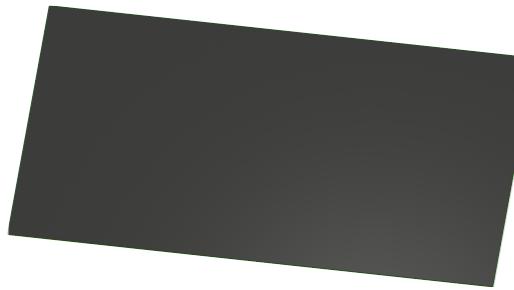
# Variant: DRAFT

2025-12-10

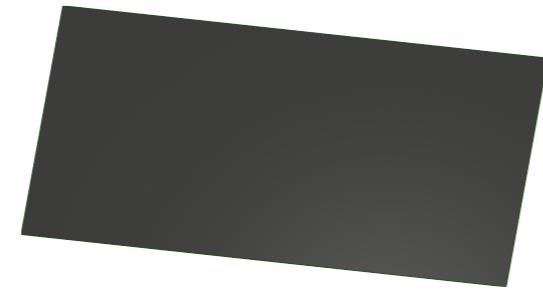
Rev + (Unreleased)

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## TOP VIEW



## BOTTOM VIEW



## NOTES

Comment

Not fitted components are marked as

DRAFT - Very early stage of schematic, ignore details.

PRELIMINARY - Close to final schematic.

CHECKED - There shouldn't be any mistakes. Contact the engineer if you find any.

RELEASED - A board with this schematic has been sent to production.

Date: 10-Dec-2025

## DESIGN CONSIDERATIONS

DESIGN NOTE:  
Example text for informational design notes.

DESIGN NOTE:  
Example text for debug notes.

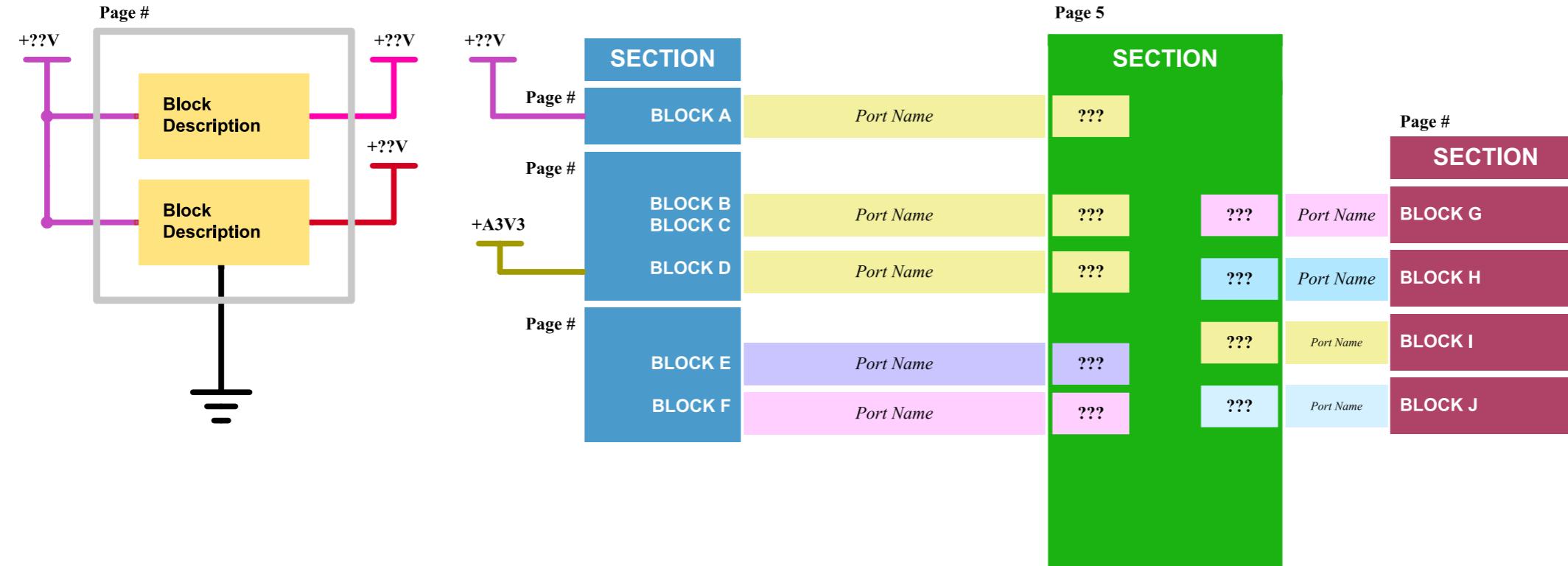
DESIGN NOTE:  
Example text for cautionary design notes.

DESIGN NOTE:  
Example text for critical design notes.

LAYOUT NOTE:  
Example text for critical layout guidelines.

	Comments:	Company:		Variant: DRAFT	Git Hash: c440c30
		INTI			
	Board Name:	Project Name:		Test KDT	
		0			
	Sheet Title:	File Name:	KDT_Hierarchical_KiBot.kicad_sch	Designer:	Date: 2025-01-12 Revision: + (Unreleased)
	Sheet Path:	/		Reviewer:	Size: A3 Sheet: 1 of 7

# [2] Block Diagram

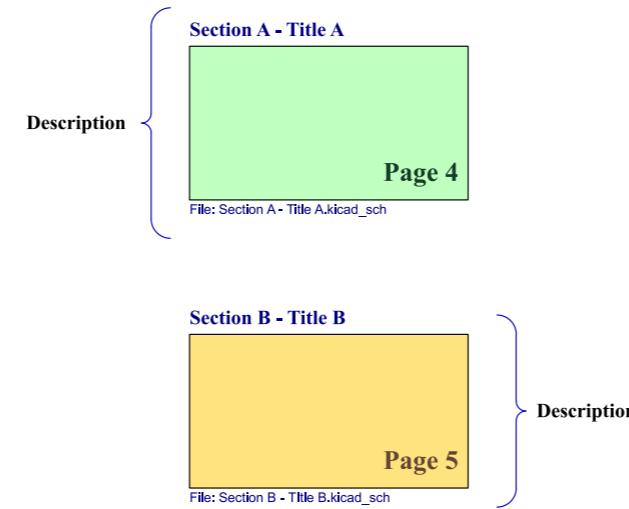


## Target specifications:

<b>Input voltage:</b>	<b>?? - ?? V</b>
<b>Spec 2</b>	<b>??</b>
<b>Spec 3</b>	<b>??</b>
<b>Spec 4</b>	<b>??</b>

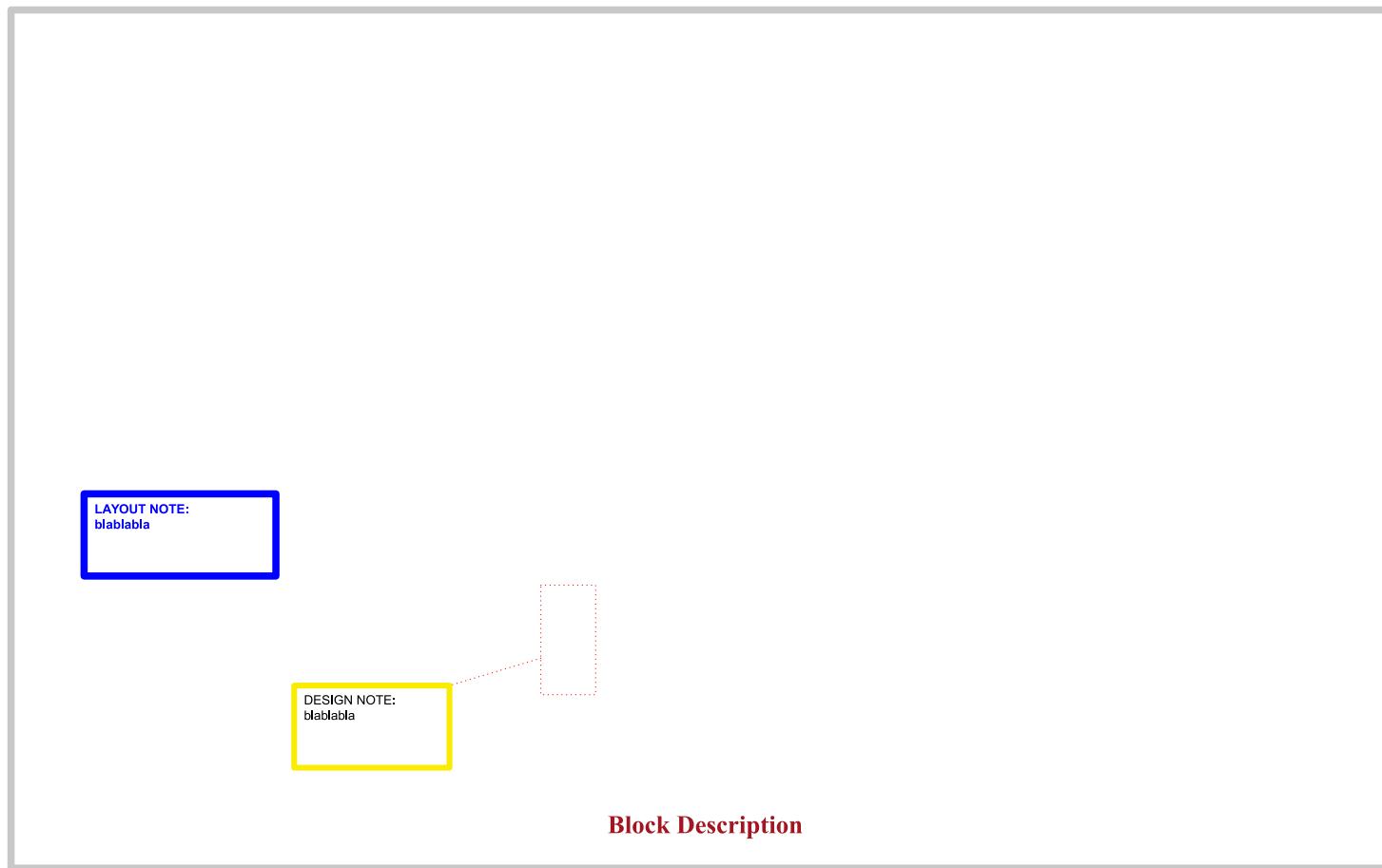
	<b>Comments:</b> INTI	<b>Company:</b> INTI	<b>Variant:</b> DRAFT	<b>Git Hash:</b> c440c30
		<b>Board Name:</b> <b>0</b>	<b>Project Name:</b> <b>Test KDT</b>	
	<b>Sheet Title:</b> Block Diagram	<b>File Name:</b> Block Diagram.kicad_sch	<b>Designer:</b> SET	<b>Date:</b> 2025-01-12 + (Unreleased)
	<b>Sheet Path:</b> /Block Diagram/		<b>Reviewer:</b>	<b>Size:</b> <b>A3</b> <b>2 of 7</b>

# [3] Project Architecture



	Comments:	Company: INTI	Variant: DRAFT	Git Hash: c440c30
	Board Name: <b>0</b>		Project Name: <b>Test KDT</b>	
	Sheet Title: Project Architecture	File Name: Project Architecture.kicad_sch	Designer: SET	Date: 2025-01-12 + (Unreleased)
	Sheet Path: /Project Architecture/		Reviewer:	Size: <b>A3</b> Sheet: <b>3 of 7</b>

# [4] Sheet Title A



		Comments: AN5346 STM32G474 Datasheet p.81 J. Pieper ADC investigation	Company: INTI	Variant: DRAFT	Git Hash: c440c30
		Board Name: 0		Project Name: <b>Test KDT</b>	
		Sheet Title: Sheet Title A	File Name: Section A - Title A.kicad_sch	Designer: SET	Date: 2025-01-12      Revision: + (Unreleased)
		Sheet Path: /Project Architecture/Section A - Title A/	Reviewer:	Size: <b>A4</b>	Sheet: <b>4</b> of <b>7</b>

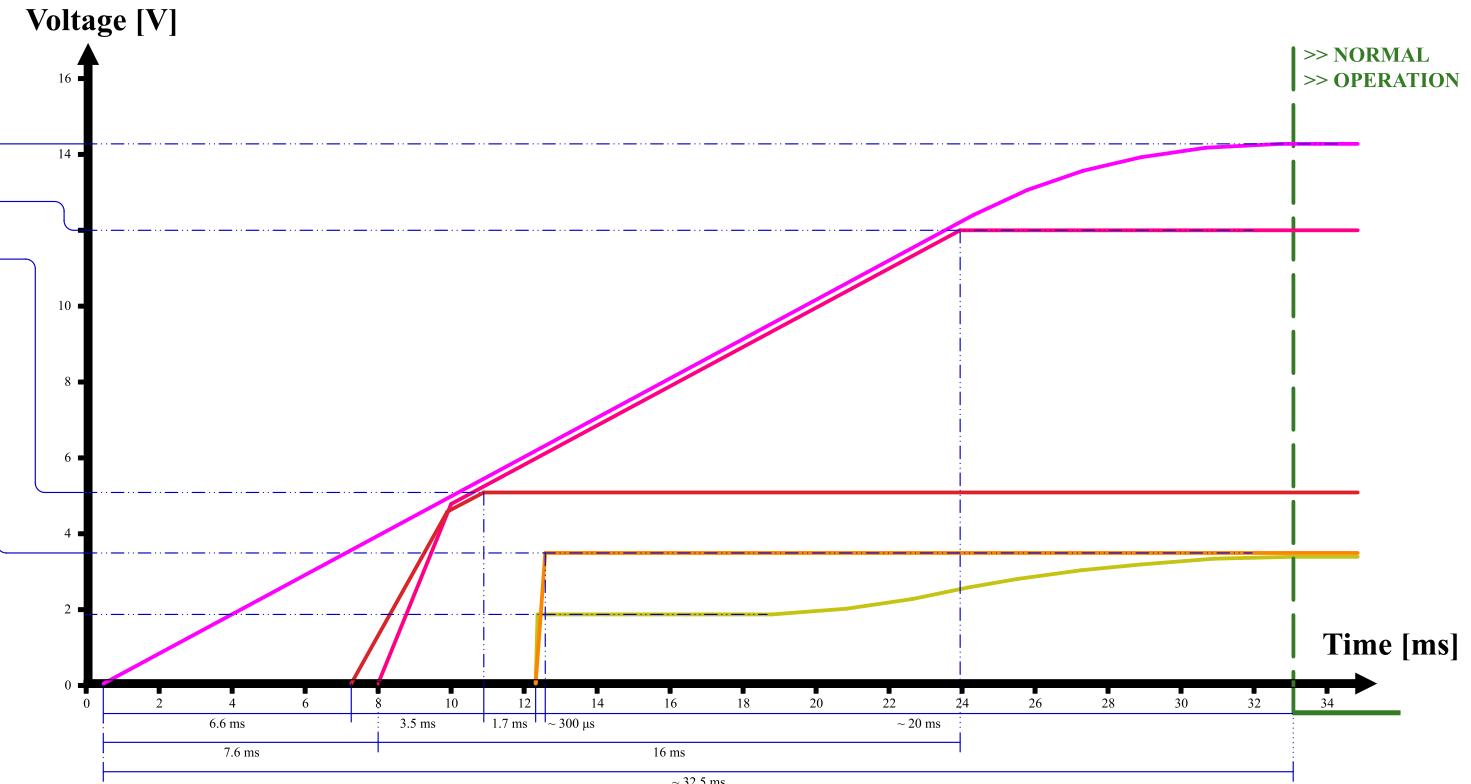
# [5] Sheet Title B



	Comments: References: Flexible I/O worked examples Flexible I/O source configuration	Company: INTI	Variant: DRAFT	Git Hash: c440c30
	Board Name: 0		Project Name: <b>Test KDT</b>	
	Sheet Title: Sheet Title B	File Name: Section B - Title B.kicad_sch	Designer: SET	Date: 2025-01-12 + (Unreleased)
	Sheet Path: /Project Architecture/Section B - Title B/	Reviewer:	Size: <b>A3</b>	Sheet: <b>5</b> of <b>7</b>

# [6] Power - Sequencing

NAME	SOURCE	LEVEL
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%



			Comments:	Company:	Variant: DRAFT	Git Hash: c440c30
			INTI			
			Board Name: <b>0</b>	Project Name: <b>Test KDT</b>		
			Sheet Title: Power - Sequencing	File Name: Power - Sequencing.kicad_sch	Designer: SET	Date: 2025-01-12
Sheet Path: <a href="#">/Power - Sequencing/</a>			Reviewer:	Size: <b>A4</b>	Sheet: <b>6</b> of 7	

## [7] Revision History

	Comments:	Company: <b>INTI</b>	Variant: DRAFT	Git Hash: c440c30
		Board Name: <b>0</b>	Project Name: <b>Test KDT</b>	
	<b>Sheet Title:</b> Revision History	<b>File Name:</b> Revision History.kicad_sch	<b>Designer:</b> SET	<b>Date:</b> 2025-01-12 <b>Revision:</b> + (Unreleased)
	<b>Sheet Path:</b> /Revision History/		<b>Reviewer:</b>	<b>Size:</b> <b>A4</b> <b>Sheet:</b> <b>7</b> of <b>7</b>