

0

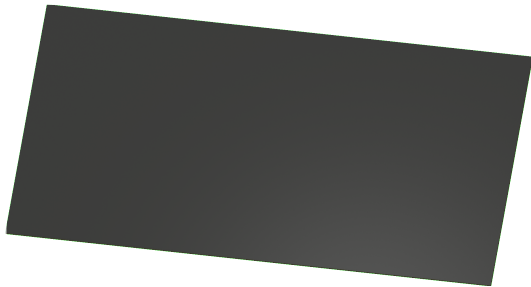
Variant: DRAFT

2025-12-10

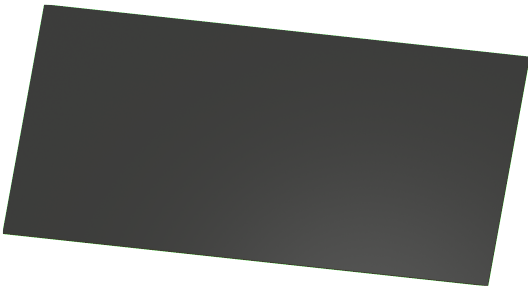
Rev + (Unreleased)

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TOP VIEW



BOTTOM VIEW



NOTES

Comment

Not fitted components are marked as X

- DRAFT - Very early stage of schematic, ignore details.
- PRELIMINARY - Close to final schematic.
- CHECKED - There shouldn't be any mistakes. Contact the engineer if you find any.
- RELEASED - A board with this schematic has been sent to production.

Date: 10-Dec-2025

DESIGN CONSIDERATIONS

DESIGN NOTE:
Example text for
informational design
notes.

DESIGN NOTE:
Example text for
debug notes.

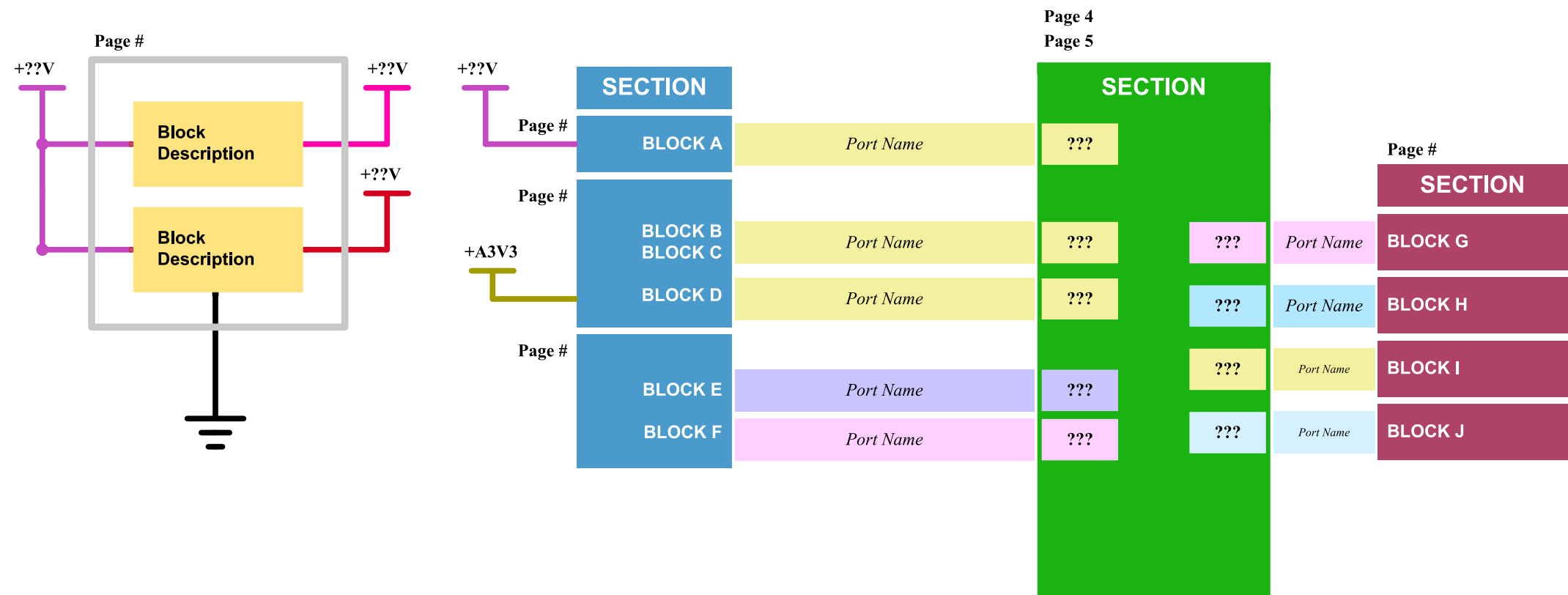
DESIGN NOTE:
Example text for
cautionary design
notes.

DESIGN NOTE:
Example text for
critical design
notes.

LAYOUT NOTE:
Example text for
critical layout
guidelines.

	Comments:	Company: INTI		Variant: DRAFT	Git Hash: c440c30
		Board Name: 0		Project Name: Test KDT	
	Sheet Title:	File Name: KDT_Hierarchical_KiBot.kicad_sch	Designer: SET	Date: 2025-01-12	Revision: + (Unreleased)
	Sheet Path: /		Reviewer:	Size: A3	Sheet: 1 of 7

[2] Block Diagram



Target specifications:	
Input voltage:	?? - ?? V
Spec 2	??
Spec 3	??
Spec 4	??

	Comments:	Company:		Variant:	Git Hash:
		INTI		DRAFT	c440c30
		Board Name:		Project Name:	
		0		Test KDT	
Sheet Title:	Block Diagram	File Name:	Block Diagram.kicad_sch	Designer:	SET
Sheet Path:	/Block Diagram/	Reviewer:		Date:	2025-01-12
				Revision:	+
				Size:	2025-01-12
				Sheet:	2 of 7

	1	2	3	4	5	6	7	8
A	<div>[3] Project Architecture</div>							
B	<div><div><div>Description</div><div><div>Section A - Title A</div><div><div>Page 4</div><div>File: Section A - Title A.kicad_sch</div></div></div></div><div><div>Section B - Title B</div><div><div>Page 5</div><div>File: Section B - Title B.kicad_sch</div></div></div><div>Description</div></div>							
C								
D								
E								
F	1	2	3	4	5	6	7	8

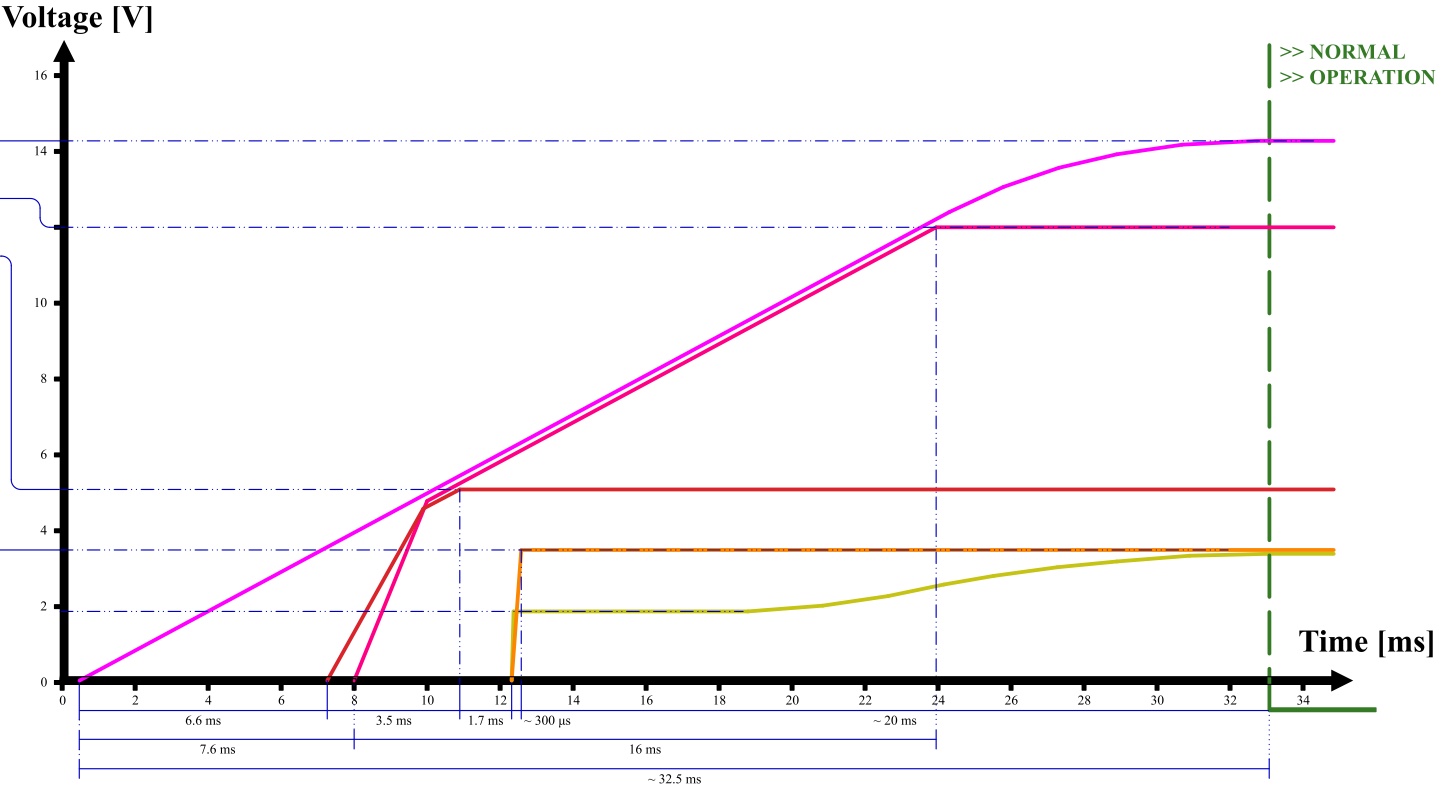
	Comments:	Company: INTI		Variant: DRAFT	Git Hash: c440c30
		Board Name: 0		Project Name: Test KDT	
	Sheet Title: Project Architecture	File Name: Project Architecture.kicad_sch	Designer: SET	Date: 2025-01-12	Revision: + (Unreleased)
	Sheet Path: /Project Architecture/		Reviewer:	Size: A3	Sheet: 3 of 7

[4] Sheet Title A					
<div><div>LAYOUT NOTE: blablabla</div><div>DESIGN NOTE: blablabla</div><div></div><div>Block Description</div></div>					
			Comments: AN5346 STM32G474 Datasheet p.81 J. Pieper ADC investigation	Company: INTI	Variant: DRAFT
			Sheet Title: Sheet Title A	Board Name: 0	Git Hash: c440c30
			Sheet Path: /Project Architecture/Section A - Title A/	Project Name: Test KDT	
			File Name: Section A - Title A.kicad_sch	Designer: SET	Date: 2025-01-12
			Reviewer:	Size: A4	Revision: + (Unreleased)
				Sheet: 4 of 7	

[illegible]

[6] Power - Sequencing

NAME	SOURCE	LEVEL
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%



	Comments:	Company:	Variant:	Git Hash:
		INTI	DRAFT	c440c30
	Board Name:	Project Name:		
	0	Test KDT		
Sheet Title:	File Name:	Designer:	Date:	Revision:
Power - Sequencing	Power - Sequencing.kicad_sch	SET	2025-01-12	+ (Unreleased)
Sheet Path:	Reviewer:	Size:	Sheet:	
/Power - Sequencing/		A4	6 of 7	

[7] Revision History

	Comments:	Company: INTI		Variant: DRAFT	Git Hash: c440c30
		Board Name: 0		Project Name: Test KDT	
	Sheet Title: Revision History	File Name: Revision History.kicad_sch	Designer: SET	Date: 2025-01-12	Revision: + (Unreleased)
	Sheet Path: /Revision History/		Reviewer:	Size: A4	Sheet: 7 of 7