

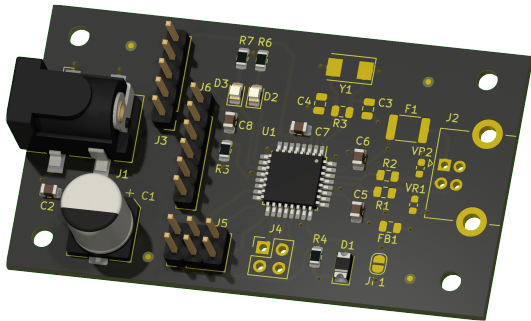
Test KDT example

Variant: RELEASED

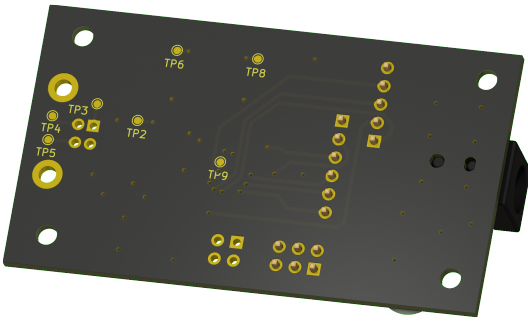
2025-12-12
Rev 0.1.3

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TOP VIEW



BOTTOM VIEW



NOTES

- Comment
- Not fitted components are marked as **X**
- DRAFT** - Very early stage of schematic, ignore details.
- PRELIMINARY** - Close to final schematic.
- CHECKED** - There shouldn't be any mistakes. Contact the engineer if you find any.
- RELEASED** - A board with this schematic has been sent to production.

Date: 12-Dec-2025

DESIGN CONSIDERATIONS

DESIGN NOTE:

Example text for informational design notes.

DESIGN NOTE:

Example text for debug notes.

DESIGN NOTE:

Example text for cautionary design notes.

DESIGN NOTE:

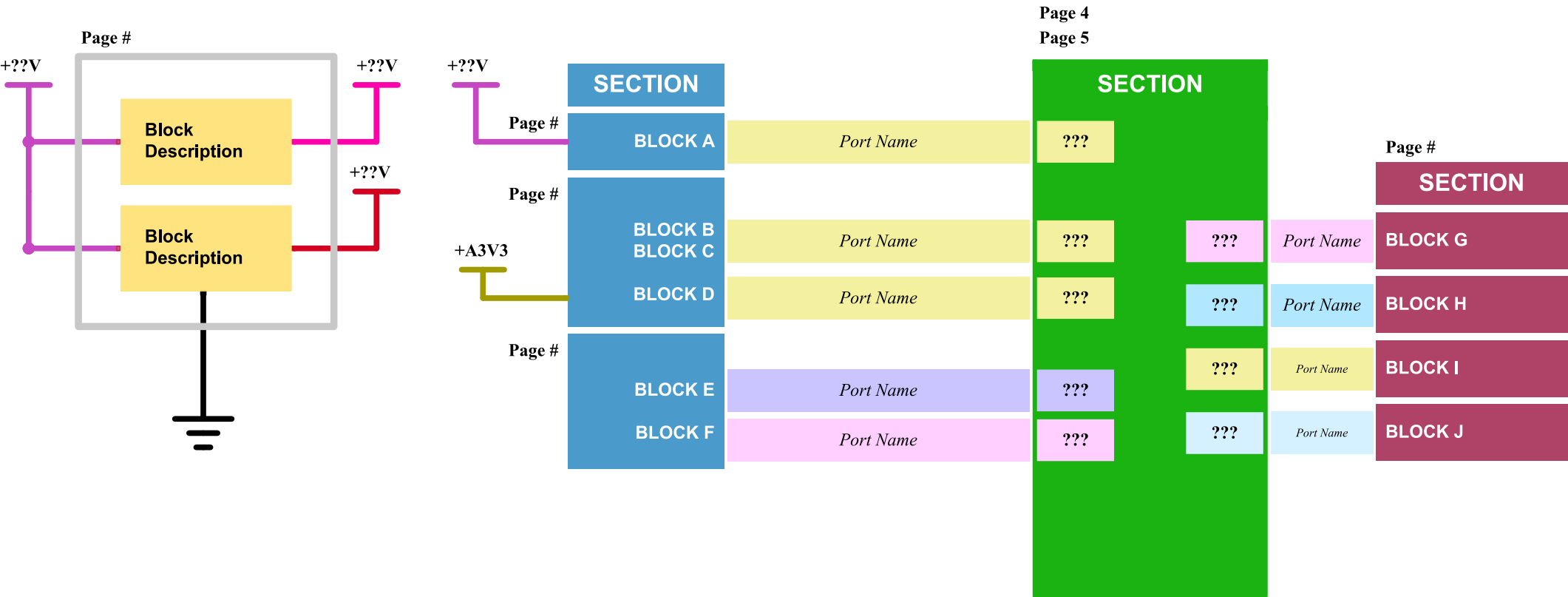
Example text for critical design notes.

LAYOUT NOTE:

Example text for critical layout guidelines.

	Comments:	Company:		Variant:		Git Hash:	
		INTI		RELEASED		c440c30	
		Board Name:			Project Name:		
		Test KDT example			Test KDT		
	Sheet Title:	File Name:		Designer:	Date:	Revision:	
	KDT_Hierarchical_KiBot.kicad_sch		SET	2025-01-12	0.1.3		
Sheet Path:			Reviewer:	Size:	Sheet:		
	/			A3	1 of 7		

[2] Block Diagram



Target specifications:	
Input voltage:	?? - ?? V
Spec 2	??
Spec 3	??
Spec 4	??

	Comments:	Company:		Variant:		Git Hash:		
		INTI		RELEASED		c440c30		
		Board Name:		Project Name:				
			Test KDT example		Test KDT			
	Sheet Title:	File Name:		Designer:	Date:	Revision:		
Block Diagram	Block Diagram.kicad_sch		SET	2025-01-12	0.1.3			
Sheet Path:			Reviewer:	Size:	Sheet:			
/Block Diagram/				A3	2 of 7			

[3] Project Architecture

Description

Section A - Title A

Page 4

File: Section A - Title A.kicad_sch

Section B - Title B

Page 5

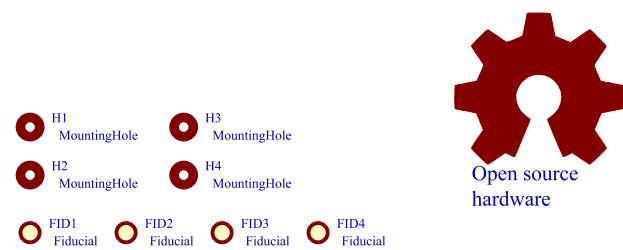
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Description

	Comments:	Company:		Variant:	Git Hash:
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	Board Name:		Project Name:		
	Test KDT example		Test KDT		
	Sheet Title:	File Name:	Designer:	Date:	Revision:
Project Architecture	Project Architecture.kicad_sch	SET	2025-01-12	0.1.3	
Sheet Path:		Reviewer:	Size:	Sheet:	
/Project Architecture/			A3	3 of 7	

KiRot variants example

Not a real life board



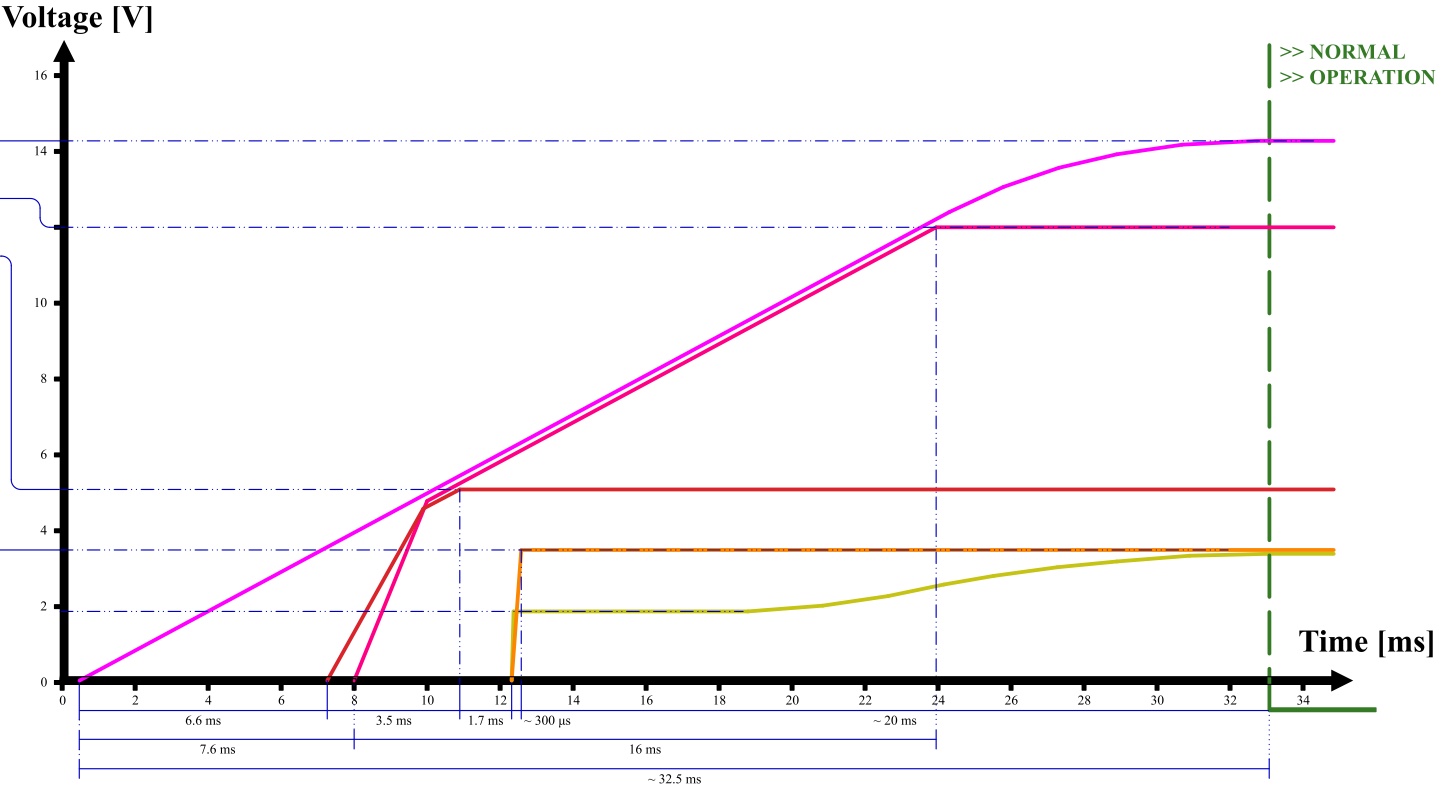
Block Description

Comments:	Company:		Variant:	Git Hash:
	INTI		RELEASED	c440c30
	Board Name:		Project Name:	
	Test KDT example		Test KDT	
Sheet Title:	File Name:	Designer:	Date:	Revision:
Sheet Title A	Section A - Title A.kicad_sch	SET	2025-01-12	0.1.3
Sheet Path:		Reviewer:	Size:	Sheet:
/Project Architecture/Section A - Title A/			A3	4 of 7

[illegible]

[6] Power - Sequencing

NAME	SOURCE	LEVEL
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%



	Comments:	Company: INTI	Variant: RELEASED	Git Hash: c440c30
		Board Name: Test KDT example	Project Name: Test KDT	
	Sheet Title: Power - Sequencing	File Name: Power - Sequencing.kicad_sch	Designer: SET	Date: 2025-01-12
	Sheet Path: /Power - Sequencing/	Reviewer:	Size: A4	Revision: 0.1.3
				Sheet: 6 of 7

[7] Revision History

	Comments:	Company: INTI		Variant: RELEASED	Git Hash: c440c30
		Board Name: Test KDT example		Project Name: Test KDT	
	Sheet Title: Revision History	File Name: Revision History.kicad_sch	Designer: SET	Date: 2025-01-12	Revision: 0.1.3
	Sheet Path: /Revision History/		Reviewer:	Size: A4	Sheet: 7 of 7