

# Test KDT example

Variant: RELEASED

2025-12-12

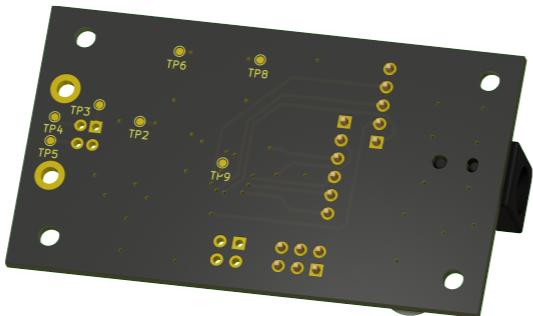
Rev 0.1.7

| Page | Index                | Page | Index | Page | Index | Page | Index |
|------|----------------------|------|-------|------|-------|------|-------|
| 1    | Cover Page           | 11   | ..... | 21   | ..... | 31   | ..... |
| 2    | Block Diagram        | 12   | ..... | 22   | ..... | 32   | ..... |
| 3    | Project Architecture | 13   | ..... | 23   | ..... | 33   | ..... |
| 4    | Section A - Title A  | 14   | ..... | 24   | ..... | 34   | ..... |
| 5    | Section B - Title B  | 15   | ..... | 25   | ..... | 35   | ..... |
| 6    | Power - Sequencing   | 16   | ..... | 26   | ..... | 36   | ..... |
| 7    | Revision History     | 17   | ..... | 27   | ..... | 37   | ..... |
| 8    | .....                | 18   | ..... | 28   | ..... | 38   | ..... |
| 9    | .....                | 19   | ..... | 29   | ..... | 39   | ..... |
| 10   | .....                | 20   | ..... | 30   | ..... | 40   | ..... |

## TOP VIEW



## BOTTOM VIEW



## NOTES

Comment

Not fitted components are marked as

DRAFT - Very early stage of schematic, ignore details.

PRELIMINARY - Close to final schematic.

CHECKED - There shouldn't be any mistakes. Contact the engineer if you find any.

RELEASED - A board with this schematic has been sent to production.

Date: 12-Dec-2025

## DESIGN CONSIDERATIONS

DESIGN NOTE:  
Example text for informational design notes.

DESIGN NOTE:  
Example text for debug notes.

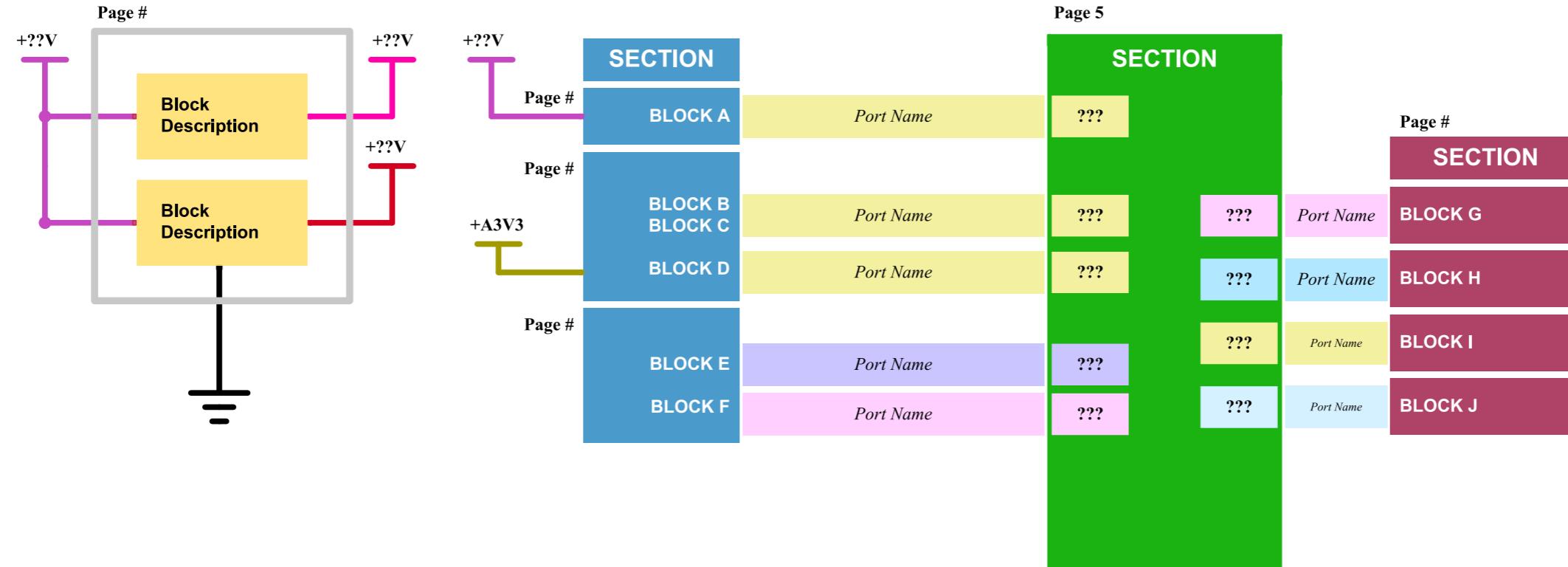
DESIGN NOTE:  
Example text for cautionary design notes.

DESIGN NOTE:  
Example text for critical design notes.

LAYOUT NOTE:  
Example text for critical layout guidelines.

|              |  |                  |                           |                         |
|--------------|--|------------------|---------------------------|-------------------------|
| Comments:    | Company:<br>INTI                               |                  | Variant:<br>RELEASED      | Git Hash:<br>c440c30    |
| Board Name:  | Test KDT example                               |                  | Project Name:<br>Test KDT |                         |
| Sheet Title: | File Name:<br>KDT_Hierarchical_KiBot.kicad_sch | Designer:<br>SET | Date:<br>2025-01-12       | Revision:<br>0.1.7      |
| Sheet Path:  | /  | Reviewer:        | Size:<br><b>A3</b>        | Sheet:<br><b>1 of 7</b> |

# [2] Block Diagram

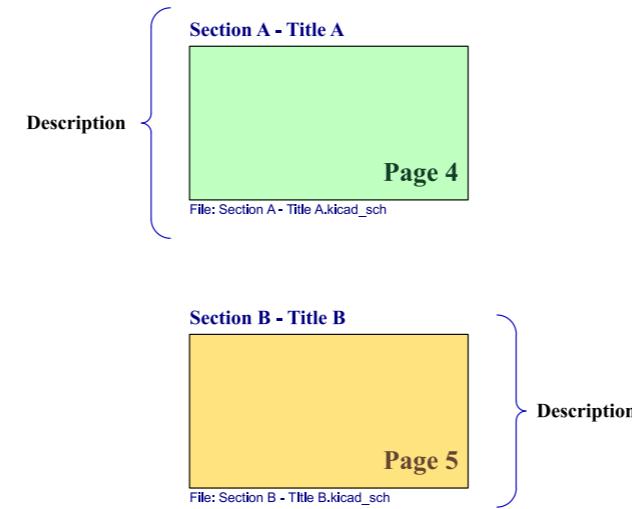


## Target specifications:

|                |           |
|----------------|-----------|
| Input voltage: | ?? - ?? V |
| Spec 2         | ??        |
| Spec 3         | ??        |
| Spec 4         | ??        |

|  |                                |  |                  |                                  |                         |
|--|--------------------------------|--|------------------|----------------------------------|-------------------------|
|  | Comments:<br><br>INTI          | Company:<br>INTI                       |                  | Variant:<br>RELEASED             | Git Hash:<br>c440c30    |
|  |                                | Board Name:<br><b>Test KDT example</b> |                  | Project Name:<br><b>Test KDT</b> |                         |
|  | Sheet Title:<br>Block Diagram  | File Name:<br>Block Diagram.kicad_sch  | Designer:<br>SET | Date:<br>2025-01-12              | Revision:<br>0.1.7      |
|  | Sheet Path:<br>/Block Diagram/ |  | Reviewer:        | Size:<br><b>A3</b>               | Sheet:<br><b>2 of 7</b> |

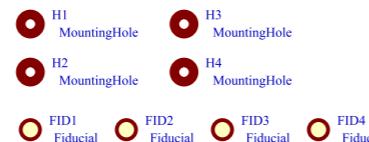
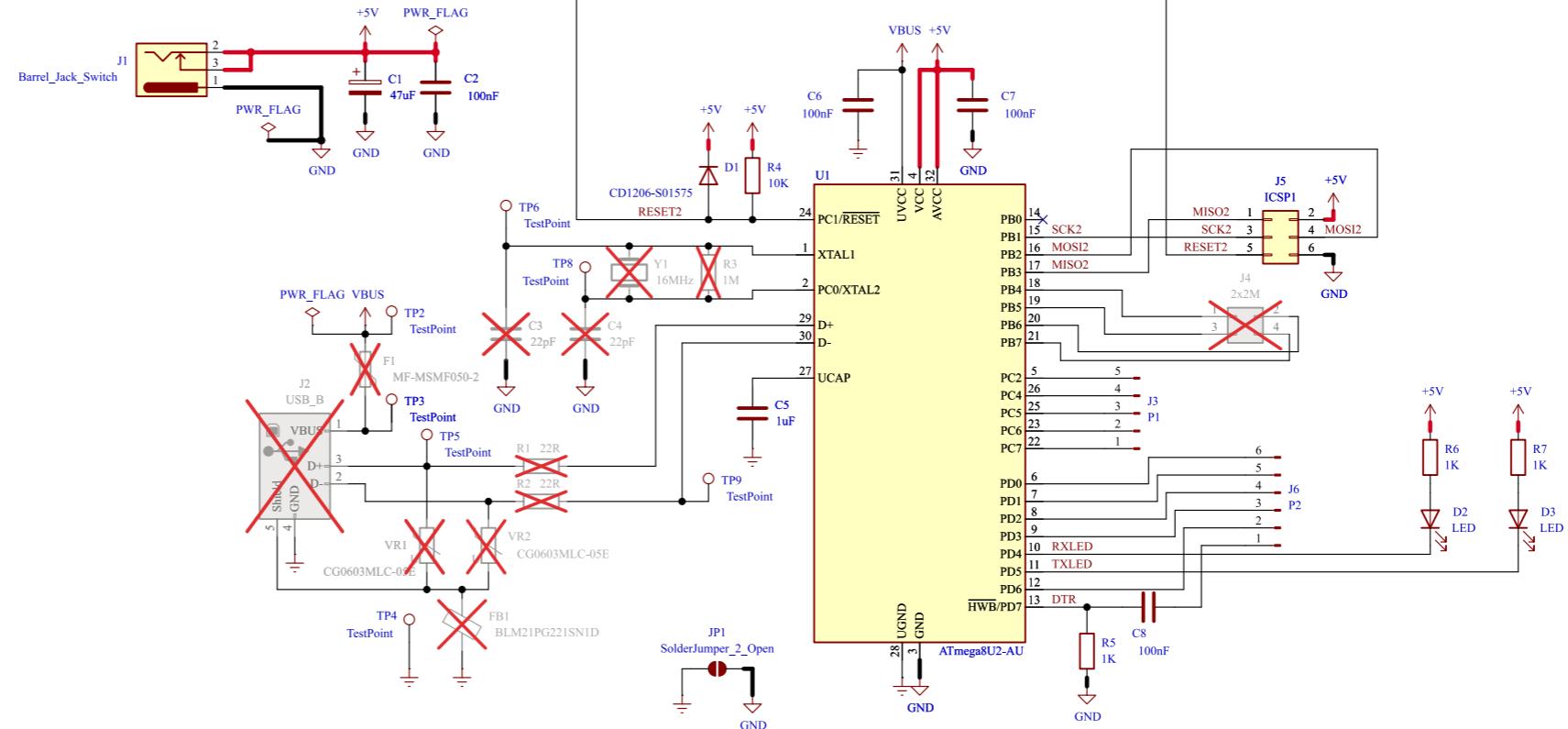
# [3] Project Architecture



|  |  |  |                      |  |
|--|--|--|----------------------|--|
|  | Comments:                              | Company:<br>INTI                             | Variant:<br>RELEASED | Git Hash:<br>c440c30                       |
|  | Board Name:<br><b>Test KDT example</b> | Project Name:<br><b>Test KDT</b>             |                      |  |
|  | Sheet Title:<br>Project Architecture   | File Name:<br>Project Architecture.kicad_sch | Designer:<br>SET     | Date:<br>2025-01-12                        |
|  | Sheet Path:<br>/Project Architecture/  |  | Reviewer:            | Size:<br><b>A3</b> Sheet:<br><b>3 of 7</b> |

# [4] Sheet Title A

KiBot variants example  
Not a real life board



Open source  
hardware



This logo has a REF starting with #  
Won't be passed to the PCB

## Block Description

| Comments:   | Company:                                    | Variant:                         | Git Hash:               |
|---|---|----------------------------------|-------------------------|
|   | INTI  | RELEASED                         | c440c30                 |
| Board Name:<br><b>Test KDT example</b>                    |   | Project Name:<br><b>Test KDT</b> |                         |
| Sheet Title:<br>Sheet Title A                             | File Name:<br>Section A - Title A.kicad_sch | Designer:<br>SET                 | Date:<br>01.7           |
| Sheet Path:<br>/Project Architecture/Section A - Title A/ |   | Reviewer:                        | Size:<br><b>A3</b>      |
|   |   |                                  | Sheet:<br><b>4 of 7</b> |

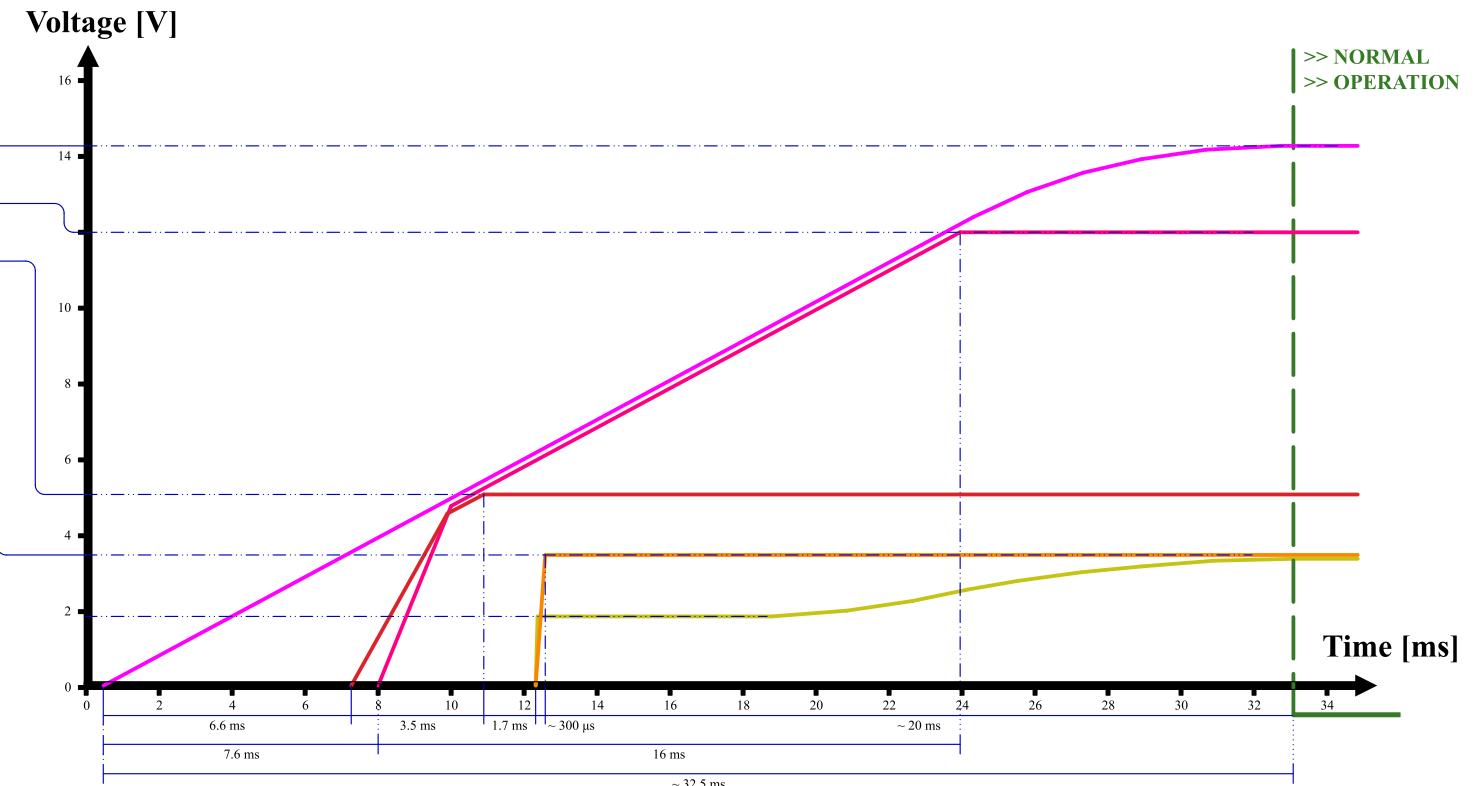
# [5] Sheet Title B



|  |   |   |                                  |   |
|--|---|---|----------------------------------|---|
|  | Comments:<br>References:<br>Flexible I/O worked examples<br>Flexible I/O source configuration | Company:<br>INTI                            | Variant:<br>RELEASED             | Git Hash:<br>c440c30                          |
|  | Board Name:<br><b>Test KDT example</b>  |   | Project Name:<br><b>Test KDT</b> |   |
|  | Sheet Title:<br>Sheet Title B   | File Name:<br>Section B - Title B.kicad_sch | Designer:<br>SET                 | Date:<br>2025-01-12<br>Revision:<br>0.1.7     |
|  | Sheet Path:<br>/Project Architecture/Section B - Title B/                                     |   | Reviewer:                        | Size:<br><b>A3</b><br>Sheet:<br><b>5 of 7</b> |

# [6] Power - Sequencing

| NAME | SOURCE | LEVEL       |
|------|--------|-------------|
| +V?? | ??     | ? - ? ± ??% |
| +V?? | ??     | ? - ? ± ??% |
| +V?? | ??     | ? - ? ± ??% |
| +V?? | ??     | ? - ? ± ??% |
| +V?? | ??     | ? - ? ± ??% |



|  |  |   |  |                         |
|--|--|---|--|-------------------------|
|  |  | Comments:<br><br>Company:<br>INTI<br><br>Board Name:<br><b>Test KDT example</b> | Variant:                                   |                         |
|  |  |   | RELEASED                                   |                         |
|  |  |   | Project Name:                              |                         |
|  |  |   | <b>Test KDT</b>                            |                         |
|  |  | Sheet Title:<br>Power - Sequencing  | File Name:<br>Power - Sequencing.kicad_sch | Designer:<br>SET        |
|  |  | Sheet Path:<br>/Power - Sequencing/   |  | Date:<br>2025-01-12     |
|  |  | Reviewer:<br>   |  | Revision:<br>0.1.7      |
|  |  | Size:<br><b>A4</b>  |  | Sheet:<br><b>6</b> of 7 |

## [7] Revision History

|  |  |   |                                  |   |
|--|--|---|----------------------------------|---|
|  | Comments:                                | Company:<br><b>INTI</b>                         | Variant:<br>RELEASED             | Git Hash:<br>c440c30  |
|  |  | Board Name:<br><b>Test KDT example</b>          | Project Name:<br><b>Test KDT</b> |   |
|  | <b>Sheet Title:</b><br>Revision History  | <b>File Name:</b><br>Revision History.kicad_sch | <b>Designer:</b><br>SET          | <b>Date:</b><br>2025-01-12 <b>Revision:</b><br>0.1.7            |
|  | <b>Sheet Path:</b><br>/Revision History/ |   | <b>Reviewer:</b>                 | <b>Size:</b><br><b>A4</b> <b>Sheet:</b><br><b>7</b> of <b>7</b> |