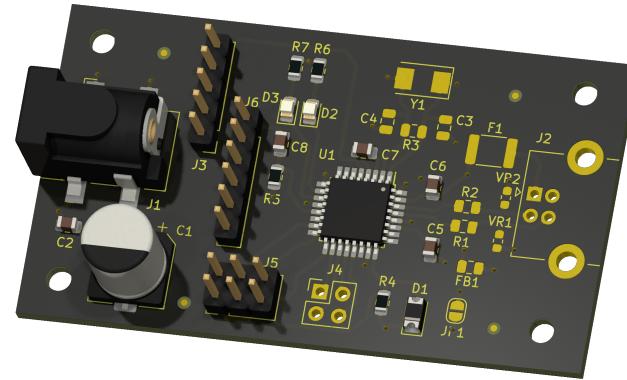


Test KDT example Assembly Document

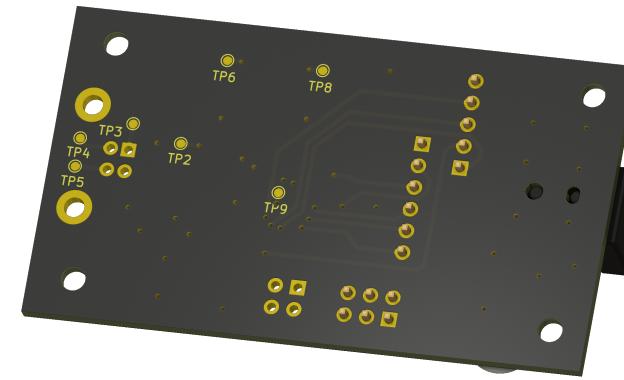
A

Top View



B

Bottom View



C

D

	Comments:	Company: INTI	Variant: RELEASED	Git Hash: d5db5df
	Board Name: Test KDT example			Project Name: Test KDT
	Sheet Title: Top/Bottom View	File Name: KDT_Hierarchical_KiBot.kicad_pcb	Designer: SET	Date: 2024-04-13 Revision: 0.1.1
	Sheet Path:		Reviewer:	Size: A4 Sheet: 1 of 3

Test KDT example Assembly Document

A

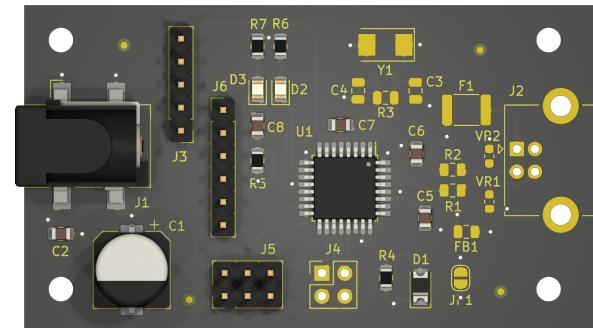
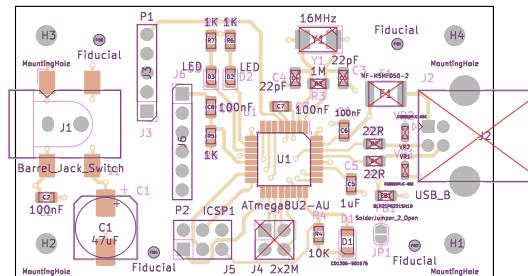
Top Assembly (Scale 1:1)

Components

Type	Front Side	Back Side	Total
THT	3	0	3
SMD	15	0	15
Total	18	0	18

ASSEMBLY NOTES (UNLESS OTHERWISE SPECIFIED)

- 1) DO NOT POPULATE PARTS ARE MARKED WITH A RED CROSS.
- 2) DO NOT POPULATE PARTS ARE NOT PRESENT IN THE BOM.
- 3) IF CONFLICTING INFORMATION IS FOUND BETWEEN THE ASSEMBLY FILE AND BOM, BOM SHOULD BE USED AS THE MAIN SOURCE.
- 4) CONFORMAL COATING IS NOT REQUIRED.
- 5) DOT IDENTIFIES PIN #1 LOCATION AND DEVICE ORIENTATION WHEN VIEWED FROM THE TOP.



B

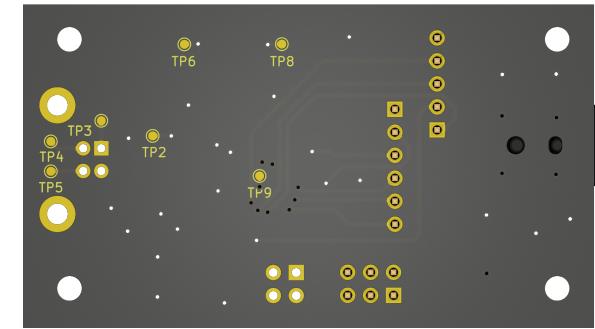
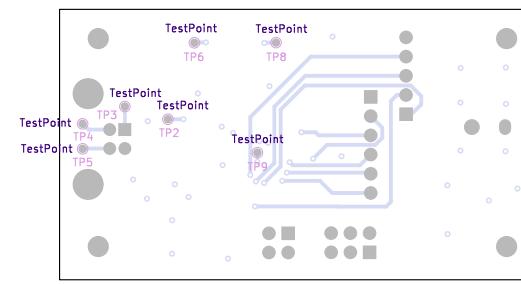
C

D

	Comments: INTI	Company: INTI	Variant: RELEASED	Git Hash: d5db5df
	Board Name: Test KDT example		Project Name: Test KDT	
	Sheet Title: Top Assembly (Scale 1:1)	File Name: KDT_Hierarchical_KiBot.kicad_pcb	Designer: SET	Date: 2024-04-13 Revision: 0.1.1
	Sheet Path:		Reviewer:	Size: A4 Sheet: 2 of 3

Test KDT example Assembly Document

Bottom Assembly (Scale 1:1)



	Comments:	Company: INTI	Variant: RELEASED	Git Hash: d5db5df
	Board Name: Test KDT example			Project Name: Test KDT
	Sheet Title: Bottom Assembly (Scale 1:1)	File Name: KDT_Hierarchical_KiBot.kicad_pcb	Designer: SET	Date: 2024-04-13 Revision: 0.1.1
	Sheet Path:		Reviewer:	Size: A4 Sheet: 3 of 3