

# Test KDT example

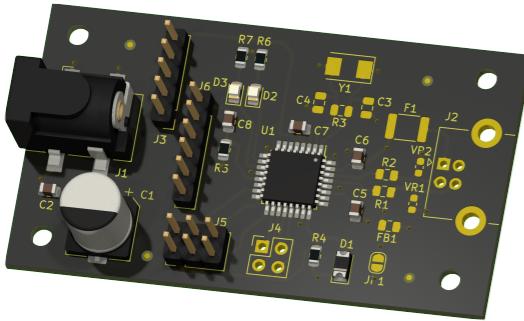
Variant: RELEASED

2025-12-12

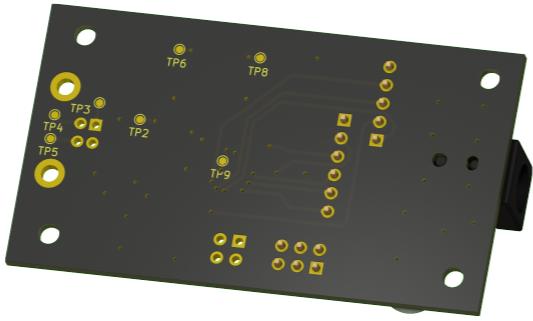
Rev 0.1.4

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## TOP VIEW



## BOTTOM VIEW



## NOTES

Comment

Not fitted components are marked as

DRAFT - Very early stage of schematic, ignore details.

PRELIMINARY - Close to final schematic.

CHECKED - There shouldn't be any mistakes. Contact the engineer if you find any.

RELEASED - A board with this schematic has been sent to production.

Date: 12-Dec-2025

## DESIGN CONSIDERATIONS

DESIGN NOTE:  
Example text for informational design notes.

DESIGN NOTE:  
Example text for debug notes.

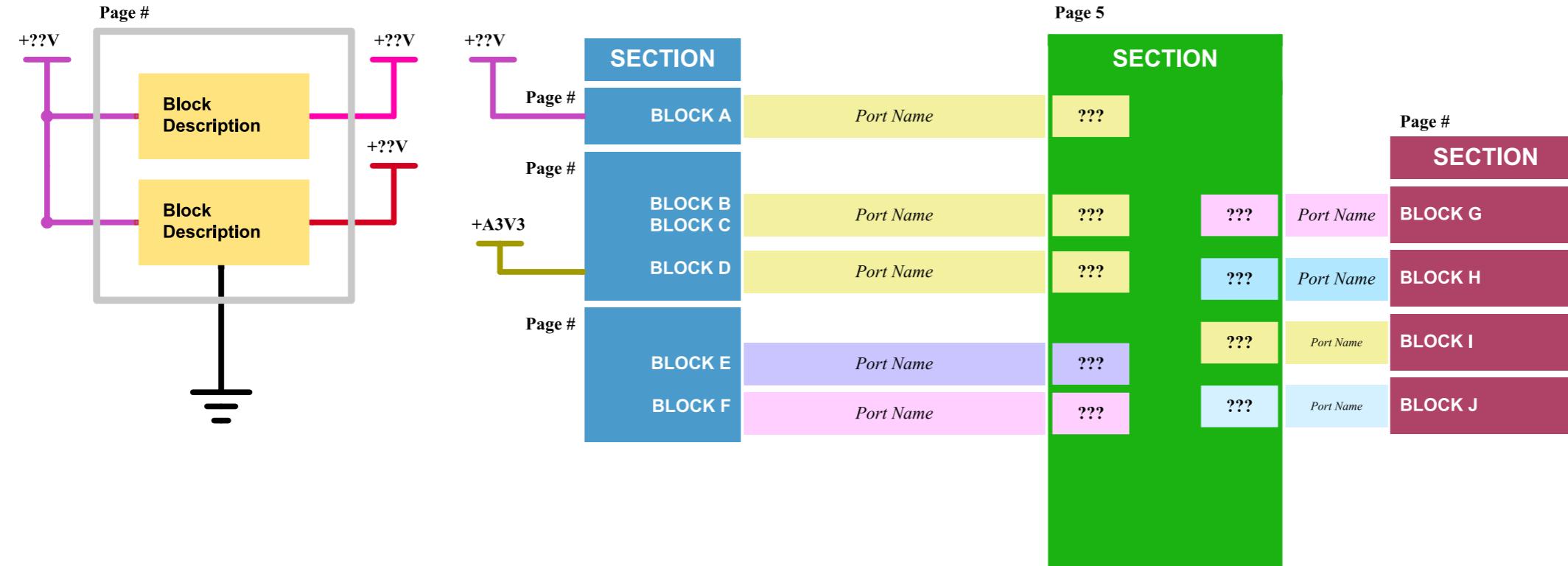
DESIGN NOTE:  
Example text for cautionary design notes.

DESIGN NOTE:  
Example text for critical design notes.

LAYOUT NOTE:  
Example text for critical layout guidelines.

Comments:	Company: INTI		Variant: RELEASED	Git Hash: c440c30
Board Name:	Test KDT example		Project Name: Test KDT	
Sheet Title:	File Name: KDT_Hierarchical_KiBot.kicad_sch	Designer: SET	Date: 2025-01-12	Revision: 0.1.4
Sheet Path:	/	Reviewer:	Size: <b>A3</b>	Sheet: <b>1</b> of <b>7</b>

# [2] Block Diagram

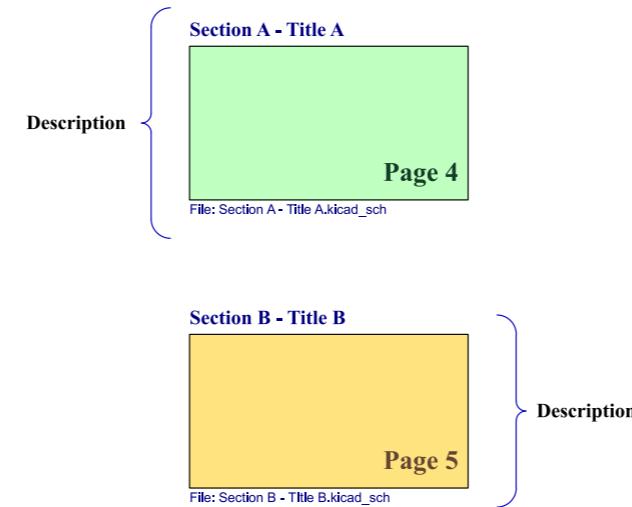


## Target specifications:

Input voltage:	?? - ?? V
Spec 2	??
Spec 3	??
Spec 4	??

	Comments:  INTI	Company: INTI	Variant: RELEASED	Git Hash: c440c30
		Board Name: <b>Test KDT example</b>	Project Name: <b>Test KDT</b>	
	Sheet Title: Block Diagram	File Name: Block Diagram.kicad_sch	Designer: SET	Date: 2025-01-12
	Sheet Path: /Block Diagram/	Reviewer:	Size: <b>A3</b>	Revision: 0.1.4

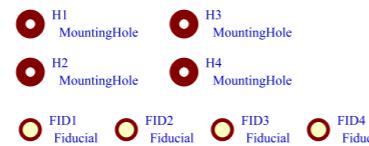
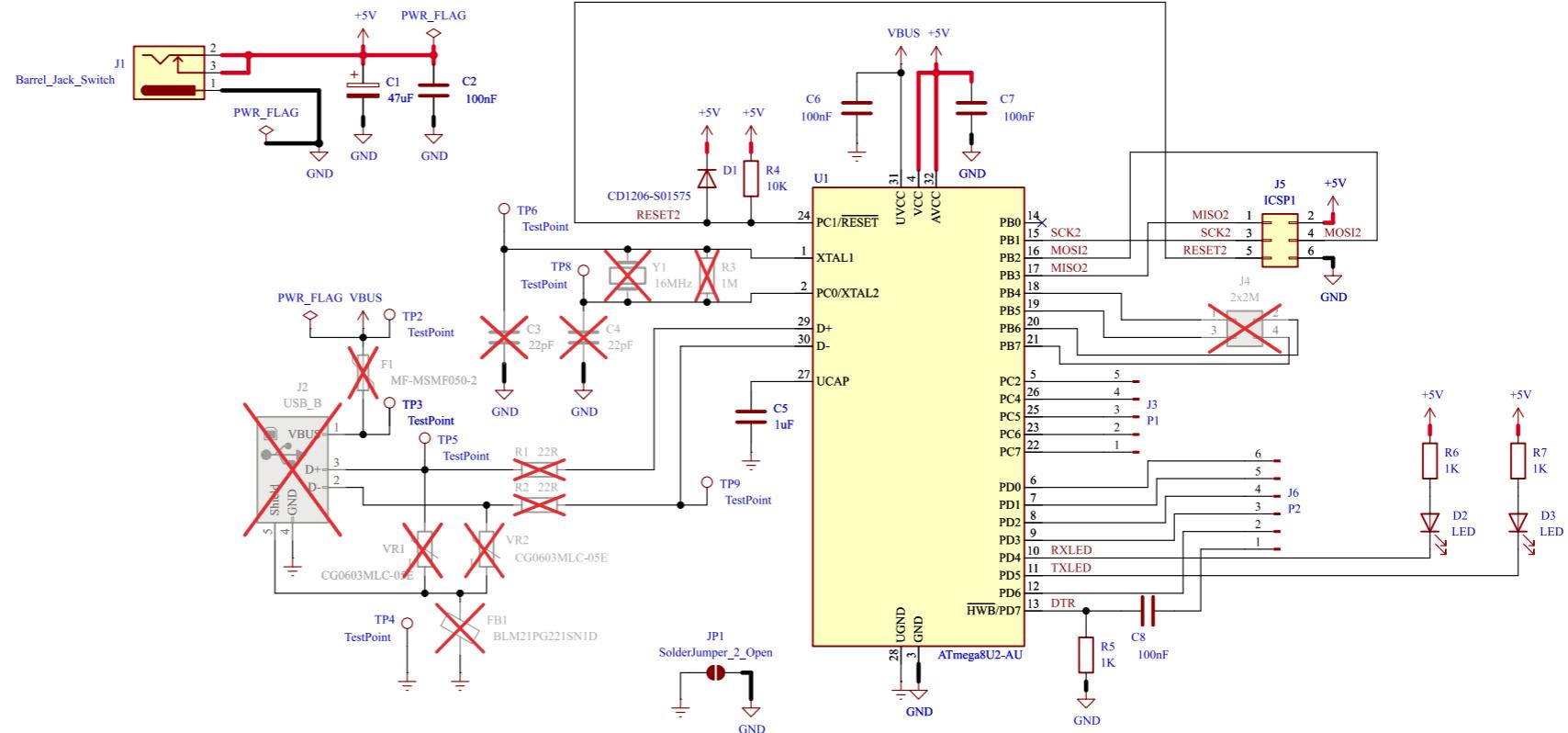
# [3] Project Architecture



	Comments:	Company: INTI	Variant: RELEASED	Git Hash: c440c30
	Board Name: <b>Test KDT example</b>	Project Name: <b>Test KDT</b>		
	Sheet Title: Project Architecture	File Name: Project Architecture.kicad_sch	Designer: SET	Date: 2025-01-12
	Sheet Path: /Project Architecture/		Reviewer:	Revision: 0.1.4
			Size: <b>A3</b>	Sheet: <b>3 of 7</b>

# [4] Sheet Title A

KiBot variants example  
Not a real life board



Open source  
hardware

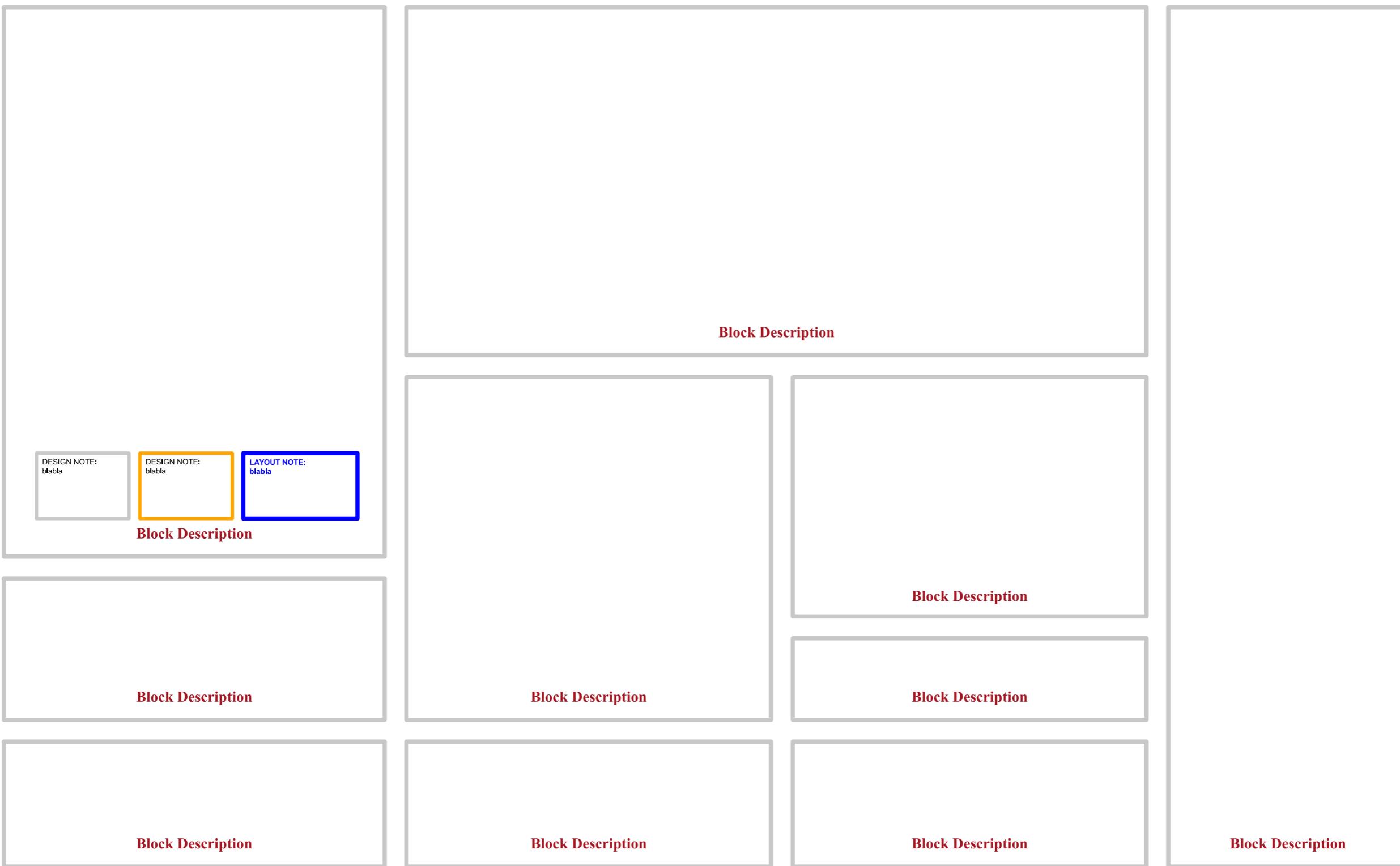


This logo has a REF starting with #  
Won't be passed to the PCB

## Block Description

Comments:	Company:	Variant:	Git Hash:
INTI		RELEASED	c440c30
Board Name: <b>Test KDT example</b>		Project Name: <b>Test KDT</b>	
Sheet Title: Sheet Title A	File Name: Section A - Title A.kicad_sch	Designer: SET	Date: 01.4
Sheet Path: /Project Architecture/Section A - Title A/		Reviewer:	Size: <b>A3</b>
			Sheet: <b>4 of 7</b>

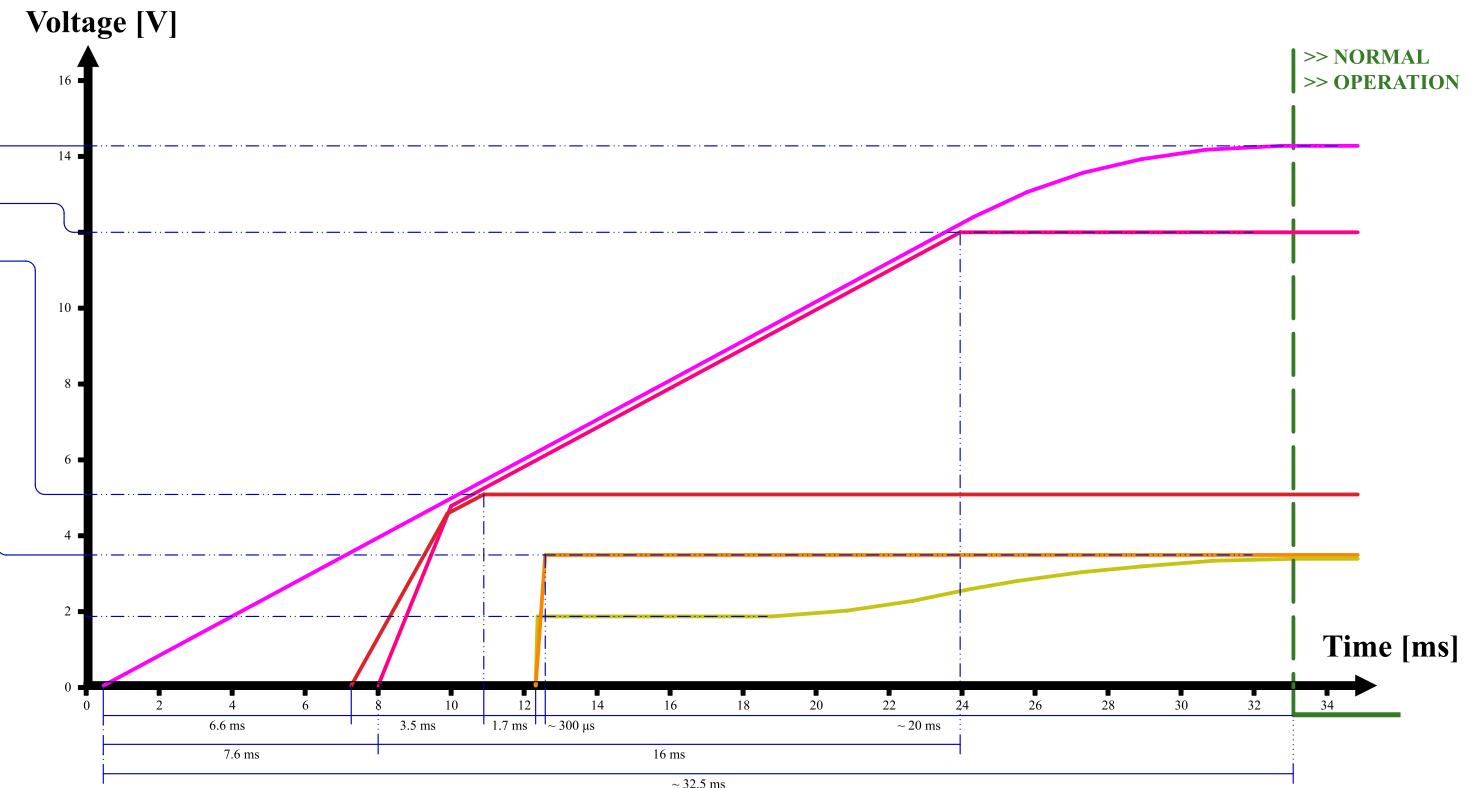
# [5] Sheet Title B



	Comments: References: Flexible I/O worked examples Flexible I/O source configuration	Company: INTI	Variant: RELEASED	Git Hash: c440c30
	Board Name: <b>Test KDT example</b>		Project Name: <b>Test KDT</b>	
	Sheet Title: Sheet Title B	File Name: Section B - Title B.kicad_sch	Designer: SET	Date: 2025-01-12
	Sheet Path: /Project Architecture/Section B - Title B/		Reviewer:	Revision: 0.1.4
			Size: <b>A3</b>	Sheet: <b>5 of 7</b>

# [6] Power - Sequencing

NAME	SOURCE	LEVEL
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%



		Comments:  Board Name: <b>Test KDT example</b>	Company: INTI		Variant: RELEASED	Git Hash: c440c30
			Project Name: <b>Test KDT</b>			
			Sheet Title: Power - Sequencing	File Name: Power - Sequencing.kicad_sch	Designer: SET	Date: 2025-01-12
			Revision: 0.1.4		Reviewer: A4	Size: <b>A4</b>
Sheet Path: <a href="/Power - Sequencing/">/Power - Sequencing/</a>			Sheet: <b>6</b> of 7			

# [7] Revision History

A					A
B					B
C					C
D					D
		Comments:	Company: INTI	Variant: RELEASED	Git Hash: c440c30
			Board Name: <b>Test KDT example</b>	Project Name: <b>Test KDT</b>	
		Sheet Title: Revision History	File Name: Revision History.kicad_sch	Designer: SET	Date: 2025-01-12    Revision: 0.1.4
		Sheet Path: <a href="#">/Revision History/</a>		Reviewer:	Size: <b>A4</b> Sheet: <b>7</b> of 7