

note of experiment in week5

zxp

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1 environment

cpu: Intel(R) Xeon(R) Gold 6330 CPU @ 2.00GHz (56 cores were applied)

gpu: rtx3090(a piece was applied)

System: CentOS7

Compiler: 9.5

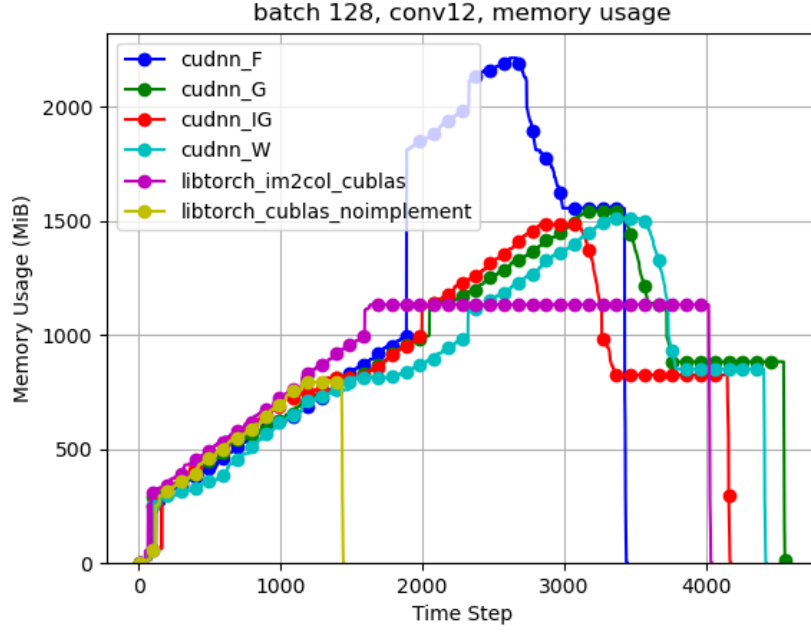


Figure 1: memory

2 Experiment

The memory usage of some algorithms for last week's data with batch=128 and conv=12 was plotted as a trend plot, and the memory transform without calculating convolution was tested

2.1 Analysis

Very strange, We need Data of im2win and cublas.

3 Experiment

im2win uses tensor1d to manage the memory, manually move the memory from CPU to GPU, no longer use libtorch, and adds the support for blocking input batch, but I think it is rational to directly do not add time of the memory copying, the code needs to be changed. The correctness is only tested at present