

The University of Alabama

ECE 380

Lab 6

Priyanshu Sethi

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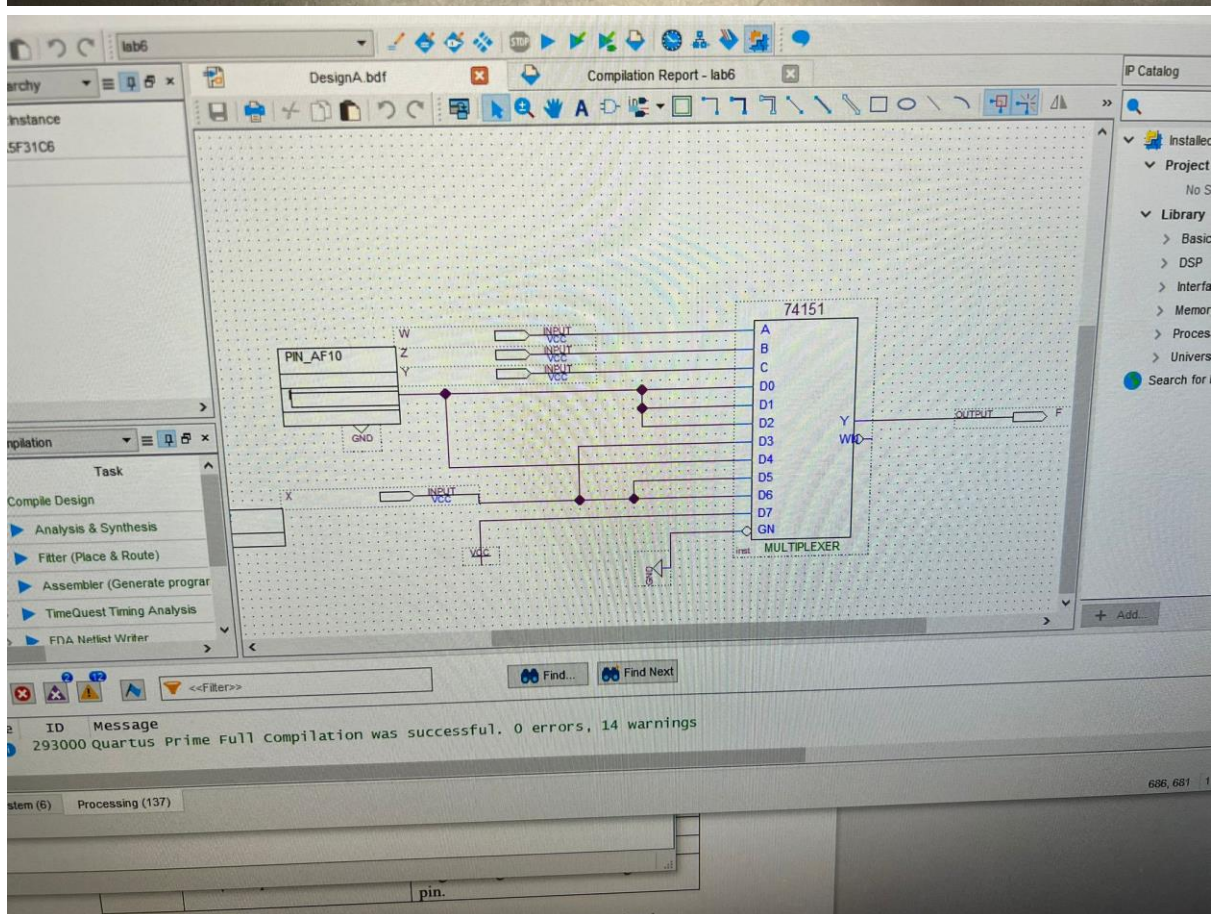
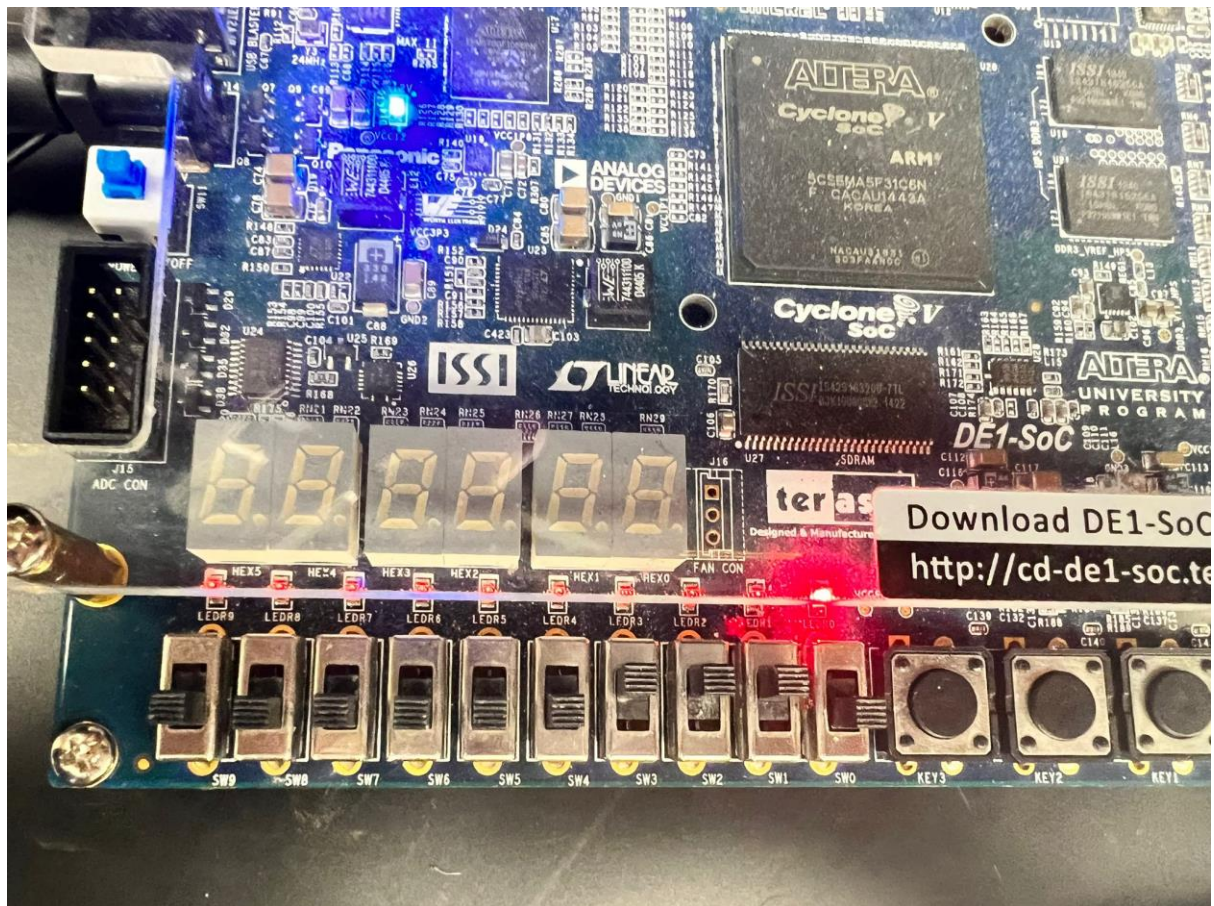
Introduction

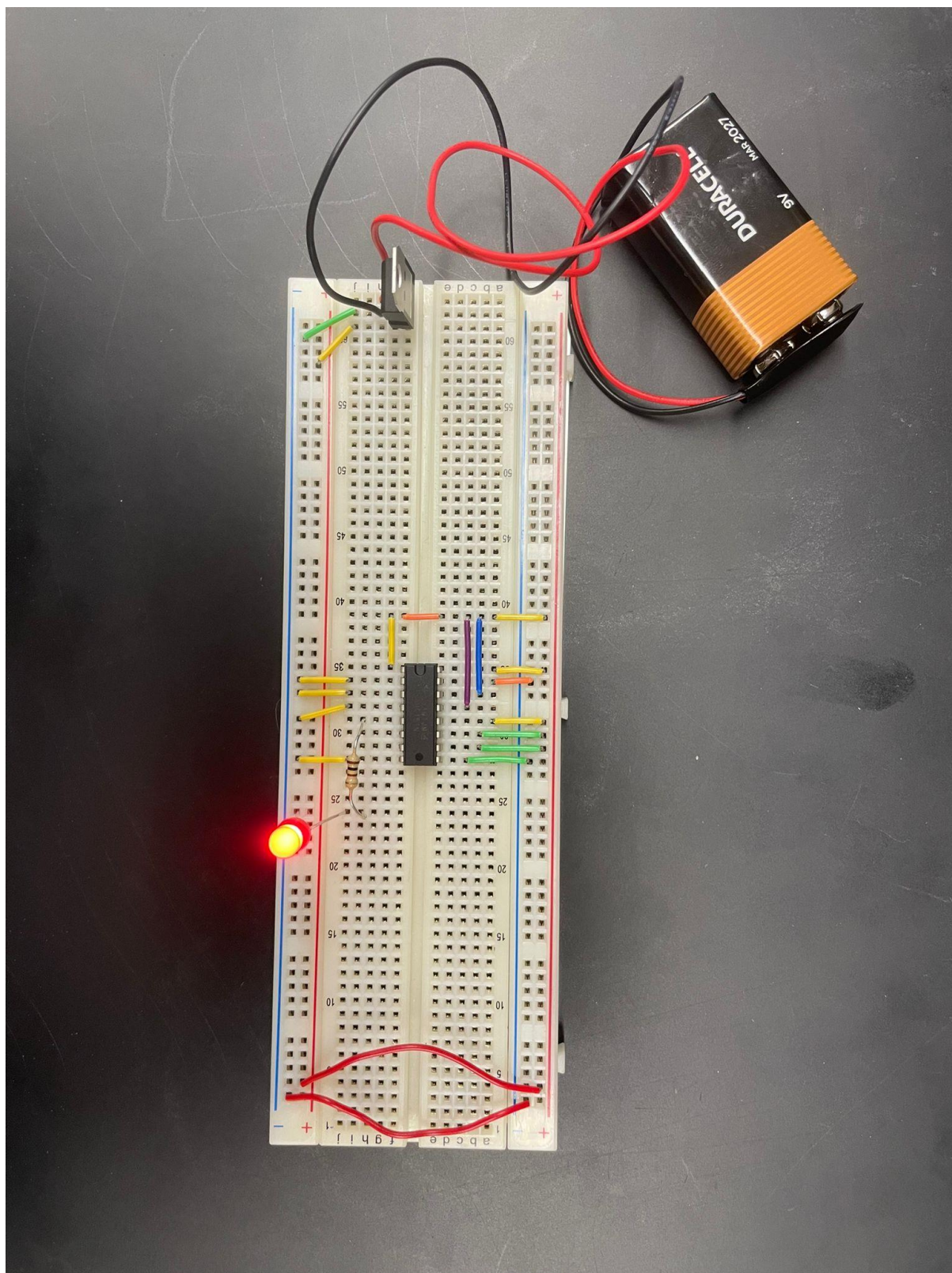
In this lab, we used an 8-to-1 multiplexer 74151 and a 555 timer. We used them to make a combinational circuit and create clock signals for sequential circuits.

Procedure

- 1)- We implemented a four input majority function using a single 74151 multiplexer. We then, worked on creating functional simulations for the same.
- 2)- We then used the provided breadboarding kit to implement the same circuit as described in the above step.
- 3)- We watched the given youtube video and worked on creating a clock signal using the breadboard and 555 timer.

Images





PRYANSHU SETHI
GAURAV SRIVASTAVA

| Lab Score Sheet | | |
|--------------------|-------|--------------------------|
| Lab Procedures | Score | Lab TA comments/Initials |
| Design A (20 pts) | 20 | deep |
| Design B (20 pts) | 20 | cd |
| Design C (30 pts) | 30 | gera |
| Lab Report | | |
| Lab report: 30 pts | | |
| Total Score | | |

